

# API Reference for C FPGA Interface

---

## Errors

**const NiFpga\_Status NiFpga\_Status\_Success = 0 [static]**

No errors or warnings.

**const NiFpga\_Status NiFpga\_Status\_MemoryFull = -52000 [static]**

A memory allocation failed.

**const NiFpga\_Status NiFpga\_Status\_FeatureUnsupported = -52002 [static]**

The requested feature is not supported.

**const NiFpga\_Status NiFpga\_Status\_SoftwareFault = -52003 [static]**

An unexpected software error occurred.

**const NiFpga\_Status NiFpga\_Status\_InvalidParameter = -52005 [static]**

The parameter to a function was invalid.

**const NiFpga\_Status NiFpga\_Status\_ResourceNotFound = -52006 [static]**

A needed resource was not found. This could be the NiFpga.dll/NiFpga.out library, or the .lvbitx bitfile.

**const NiFpga\_Status NiFpga\_Status\_ResourceNotInitialized = -52010 [static]**

A needed resource was not properly initialized. This could occur if NiFpga\_Initialize was not called.

**const NiFpga\_Status NiFpga\_Status\_FpgaBusy = -61141 [static]**

The FPGA is busy.

**const NiFpga\_Status NiFpga\_Status\_FpgaInternalError = -61499 [static]**

An unexpected internal error occurred.

---

## Types

**typedef int32\_t NiFpga\_Status**

The result of a function. Negative values are errors and positive values are warnings.

**typedef uint32\_t NiFpga\_Bool**

A boolean value; either NiFpga\_False or NiFpga\_True.

**typedef uint32\_t NiFpga\_Session**

A handle to an FPGA session.

**typedef void\* NiFpga\_IrqContext**

See NiFpga\_ReserveIrqContext for more information.

---

## Constants

**const NiFpga\_Bool NiFpga\_False = 0 [static]**

Represents a false condition.

**const NiFpga\_Bool NiFpga\_True = 1 [static]**

Represents a true condition.

**const uint32\_t NiFpga\_Infinite = 0xFFFFFFFF [static]**

Represents an infinite timeout.

**enum NiFpga\_Irq**

Enumeration of all possible IRQs.

**Enumerator:**

*NiFpga\_Irq\_0*  
*NiFpga\_Irq\_1*  
*NiFpga\_Irq\_2*  
*NiFpga\_Irq\_3*  
*NiFpga\_Irq\_4*  
*NiFpga\_Irq\_5*  
*NiFpga\_Irq\_6*  
*NiFpga\_Irq\_7*  
*NiFpga\_Irq\_8*  
*NiFpga\_Irq\_9*  
*NiFpga\_Irq\_10*  
*NiFpga\_Irq\_11*  
*NiFpga\_Irq\_12*  
*NiFpga\_Irq\_13*  
*NiFpga\_Irq\_14*  
*NiFpga\_Irq\_15*  
*NiFpga\_Irq\_16*  
*NiFpga\_Irq\_17*  
*NiFpga\_Irq\_18*  
*NiFpga\_Irq\_19*  
*NiFpga\_Irq\_20*  
*NiFpga\_Irq\_21*  
*NiFpga\_Irq\_22*  
*NiFpga\_Irq\_23*  
*NiFpga\_Irq\_24*  
*NiFpga\_Irq\_25*  
*NiFpga\_Irq\_26*  
*NiFpga\_Irq\_27*  
*NiFpga\_Irq\_28*  
*NiFpga\_Irq\_29*  
*NiFpga\_Irq\_30*  
*NiFpga\_Irq\_31*

### enum NiFpga\_OpenAttribute

Attributes that NiFpga\_Open accepts.

#### Enumerator:

*NiFpga\_OpenAttribute\_NoRun*

### enum NiFpga\_CloseAttribute

Attributes that NiFpga\_Close accepts.

#### Enumerator:

*NiFpga\_CloseAttribute\_NoResetIfLastSession*

### enum NiFpga\_RunAttribute

Attributes that NiFpga\_Run accepts.

#### Enumerator:

*NiFpga\_RunAttribute\_WaitUntilDone*

---

## Required Functions

### NiFpga\_Status NiFpga\_Initialize (void)

This must be called before all other function calls.

#### Warning:

This function is not thread safe.

#### Returns:

result of the call

### NiFpga\_Status NiFpga\_Finalize (void)

This must be called after all other function calls.

#### Warning:

This function is not thread safe.

#### Returns:

result of the call

---

## Status Functions

### static NiFpga\_Bool NiFpga\_IsFatal (const NiFpga\_Status *status*) [inline, static]

Tests whether a status is fatal. Errors are fatal but warnings are not.

#### Parameters:

*status* status to check for fatal

### static NiFpga\_Bool NiFpga\_IsNotFatal (const NiFpga\_Status *status*) [inline, static]

Tests whether a status is non-fatal. Success and warnings are not fatal.

#### Parameters:

*status* status to check for fatal

**static NiFpga\_Status NiFpga\_SetStatus (NiFpga\_Status \*const *status*, const NiFpga\_Status *newStatus*) [inline, static]**

Conditionally sets the status to a new value. The previous status will be preserved unless the new status is "more fatal". This means that warnings or errors will overwrite successes, and only errors will overwrite warnings. Errors will never be overwritten with new errors, and warnings will never be overwritten with new warnings.

**Parameters:**

*status* status to conditionally set  
*newStatus* new status value that may be set

**#define NiFpga\_IfIsNotFatal(status, expression)**

Only evaluates the expression if the status is not fatal. The expression must evaluate to an NiFpga\_Status, such as a call to any NiFpga\_\* function, as the status will be set to the returned status if the expression is evaluated.

This macro can be used to mimic status chaining in LabVIEW, where the status doesn't have to be explicitly checked after each call. Such code may look as follows:

```
NiFpga_Status status = NiFpga_Status_Success; NiFpga_IfIsNotFatal(status, NiFpga_WriteU32(...));  
NiFpga_IfIsNotFatal(status, NiFpga_WriteU32(...)); NiFpga_IfIsNotFatal(status,  
NiFpga_WriteU32(...));
```

**Parameters:**

*status* status to check for fatal  
*expression* expression to call if the incoming status is not fatal

---

## Session Functions

**NiFpga\_Status NiFpga\_Open (const char \* *bitfile*, const char \* *signature*, const char \* *resource*, uint32\_t *attribute*, NiFpga\_Session \* *session*)**

Opens a session to the FPGA. This call will ensure that the contents of the bitfile are programmed to the FPGA. The FPGA will be run unless the NoRun attribute is used.

Note that the correct signature must be passed to ensure that your application was built with the correct generated header file for the bitfile found on disk.

On PharLap ETS systems, this path should be an absolute path starting with "C:\\".

**Parameters:**

*bitfile* path to the bitfile  
*signature* signature of the bitfile  
*resource* RIO resource string to open ("RIO0" or "rio://mysystem/RIO")  
*attribute* bitwise OR of any NiFpga\_OpenAttributes, or 0  
*session* outputs the session handle, which must be closed when no longer needed

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Close (NiFpga\_Session *session*, uint32\_t *attribute*)**

Closes the session to the FPGA. The FPGA will be reset unless either another session is still open or the NoResetIfLastSession attribute is used.

**Parameters:**

*session* handle to a currently open session  
*attribute* bitwise OR of any NiFpga\_CloseAttributes, or 0

**Returns:**

result of the call

---

## Method Functions

**NiFpga\_Status NiFpga\_Run (NiFpga\_Session *session*, uint32\_t *attribute*)**

Runs the FPGA VI on the target. If WaitUntilDone is used, the FPGA will run until it finishes executing (if ever).

**Parameters:**

*session* handle to a currently open session  
*attribute* bitwise OR of any NiFpga\_RunAttributes, or 0

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Abort (NiFpga\_Session *session*)**

Aborts the FPGA VI.

**Parameters:**

*session* handle to a currently open session

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Reset (NiFpga\_Session *session*)**

Resets the FPGA VI.

**Parameters:**

*session* handle to a currently open session

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Download (NiFpga\_Session *session*)**

Re-downloads the FPGA bitstream to the target.

**Parameters:**

*session* handle to a currently open session

**Returns:**

result of the call

---

## Read/Write Functions

**NiFpga\_Status NiFpga\_ReadBool (NiFpga\_Session *session*, uint32\_t *indicator*, NiFpga\_Bool \* *value*)**

Reads a boolean value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadI8 (NiFpga\_Session *session*, uint32\_t *indicator*, int8\_t \* *value*)**

Reads a signed 8-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadU8 (NiFpga\_Session *session*, uint32\_t *indicator*, uint8\_t \* *value*)**

Reads an unsigned 8-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadI16 (NiFpga\_Session *session*, uint32\_t *indicator*, int16\_t \* *value*)**

Reads a signed 16-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadU16 (NiFpga\_Session *session*, uint32\_t *indicator*, uint16\_t \* *value*)**

Reads an unsigned 16-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadI32 (NiFpga\_Session *session*, uint32\_t *indicator*, int32\_t \* *value*)**

Reads a signed 32-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadU32 (NiFpga\_Session *session*, uint32\_t *indicator*, uint32\_t \* *value*)**

Reads an unsigned 32-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadI64 (NiFpga\_Session *session*, uint32\_t *indicator*, int64\_t \* *value*)**

Reads a signed 64-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadU64 (NiFpga\_Session *session*, uint32\_t *indicator*, uint64\_t \* *value*)**

Reads an unsigned 64-bit integer value from a given indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*value* outputs the value that was read

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteBool (NiFpga\_Session *session*, uint32\_t *control*, NiFpga\_Bool *value*)**

Writes a boolean value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Writel8 (NiFpga\_Session *session*, uint32\_t *control*, int8\_t *value*)**

Writes a signed 8-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteU8 (NiFpga\_Session *session*, uint32\_t *control*, uint8\_t *value*)**

Writes an unsigned 8-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Writel16 (NiFpga\_Session *session*, uint32\_t *control*, int16\_t *value*)**

Writes a signed 16-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteU16 (NiFpga\_Session *session*, uint32\_t *control*, uint16\_t *value*)**

Writes an unsigned 16-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Writel32 (NiFpga\_Session *session*, uint32\_t *control*, int32\_t *value*)**

Writes a signed 32-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call



**NiFpga\_Status NiFpga\_WriteU32 (NiFpga\_Session *session*, uint32\_t *control*, uint32\_t *value*)**

Writes an unsigned 32-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_Writel64 (NiFpga\_Session *session*, uint32\_t *control*, int64\_t *value*)**

Writes a signed 64-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteU64 (NiFpga\_Session *session*, uint32\_t *control*, uint64\_t *value*)**

Writes an unsigned 64-bit integer value to a given control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*value* value to write

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayBool (NiFpga\_Session *session*, uint32\_t *indicator*, NiFpga\_Bool \* *values*, uint32\_t *size*)**

Reads boolean values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*values* outputs the values that were read  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayI8 (NiFpga\_Session *session*, uint32\_t *indicator*, int8\_t \* *values*, uint32\_t *size*)**

Reads signed 8-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*values* outputs the values that were read  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayU8 (NiFpga\_Session *session*, uint32\_t *indicator*, uint8\_t \*  
*values*, uint32\_t *size*)**

Reads unsigned 8-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session

*indicator* indicator/control from which to read

*values* outputs the values that were read

*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayI16 (NiFpga\_Session *session*, uint32\_t *indicator*, int16\_t \*  
*values*, uint32\_t *size*)**

Reads signed 16-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session

*indicator* indicator/control from which to read

*values* outputs the values that were read

*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayU16 (NiFpga\_Session *session*, uint32\_t *indicator*, uint16\_t \*  
*values*, uint32\_t *size*)**

Reads unsigned 16-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session

*indicator* indicator/control from which to read

*values* outputs the values that were read

*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayI32 (NiFpga\_Session *session*, uint32\_t *indicator*, int32\_t \*  
*values*, uint32\_t *size*)**

Reads signed 32-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session

*indicator* indicator/control from which to read

*values* outputs the values that were read

*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayU32 (NiFpga\_Session *session*, uint32\_t *indicator*, uint32\_t \* *values*, uint32\_t *size*)**

Reads unsigned 32-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*values* outputs the values that were read  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayI64 (NiFpga\_Session *session*, uint32\_t *indicator*, int64\_t \* *values*, uint32\_t *size*)**

Reads signed 64-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*values* outputs the values that were read  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadArrayU64 (NiFpga\_Session *session*, uint32\_t *indicator*, uint64\_t \* *values*, uint32\_t *size*)**

Reads unsigned 64-bit integer values from a given array indicator or control.

**Parameters:**

*session* handle to a currently open session  
*indicator* indicator/control from which to read  
*values* outputs the values that were read  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayBool (NiFpga\_Session *session*, uint32\_t *control*, const NiFpga\_Bool \* *values*, uint32\_t *size*)**

Writes boolean values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayI8 (NiFpga\_Session *session*, uint32\_t *control*, const int8\_t \* *values*, uint32\_t *size*)**

Writes signed 8-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayU8 (NiFpga\_Session session, uint32\_t control, const uint8\_t \* values, uint32\_t size)**

Writes unsigned 8-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayI16 (NiFpga\_Session session, uint32\_t control, const int16\_t \* values, uint32\_t size)**

Writes signed 16-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayU16 (NiFpga\_Session session, uint32\_t control, const uint16\_t \* values, uint32\_t size)**

Writes unsigned 16-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayI32 (NiFpga\_Session session, uint32\_t control, const int32\_t \* values, uint32\_t size)**

Writes signed 32-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write

*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayU32 (NiFpga\_Session *session*, uint32\_t *control*, const uint32\_t \* *values*, uint32\_t *size*)**

Writes unsigned 32-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayI64 (NiFpga\_Session *session*, uint32\_t *control*, const int64\_t \* *values*, uint32\_t *size*)**

Writes signed 64-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteArrayU64 (NiFpga\_Session *session*, uint32\_t *control*, const uint64\_t \* *values*, uint32\_t *size*)**

Writes unsigned 64-bit integer values to a given array control or indicator.

**Parameters:**

*session* handle to a currently open session  
*control* control/indicator to which to write  
*values* values to write  
*size* number of values in this array

**Returns:**

result of the call

---

## Interrupt Functions

**NiFpga\_Status NiFpga\_ReserveIrqContext (NiFpga\_Session *session*, NiFpga\_IrqContext \* *context*)**

IRQ contexts are single-threaded; Only one thread may be "waiting" on a particular context at any given time. Clients must reserve as many contexts as the application requires.

If a context is successfully reserved (returned status is not fatal), the context must be unreserved later. Otherwise, a memory leak will occur.

**Parameters:**

*session* handle to a currently open session  
*context* outputs the IRQ context

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_UnreserveIrqContext (NiFpga\_Session *session*, NiFpga\_IrqContext *context*)**

Unreserves an IRQ context obtained from NiFpga\_ReserveIrqContext.

**Parameters:**

*session* handle to a currently open session  
*context* IRQ context to unreserve

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WaitOnIrqs (NiFpga\_Session *session*, NiFpga\_IrqContext *context*, uint32\_t *irqs*, uint32\_t *timeout*, uint32\_t \* *irqsAsserted*, NiFpga\_Bool \* *timedOut*)**

Blocking call which stops the calling thread until the specified IRQs have been asserted on the FPGA, or the timeout period expires. An IRQ context must have been reserved earlier using NiFpga\_ReserveIrqContext. The context must not be in use by other threads at the time of this call.

This call can be used to wait on a set of IRQs. If any IRQ in the set is asserted by the FPGA, this call will return. The *irqsAsserted* parameter can be used to determine which IRQs were asserted in this case.

**Parameters:**

*session* handle to a currently open session  
*context* IRQ context with which to wait  
*irqs* bitwise OR of NiFpga\_Irqs  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*irqsAsserted* if non-NULL, outputs the set of IRQs that were asserted  
*timedOut* if non-NULL, outputs whether the timeout expired

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_AcknowledgeIrqs (NiFpga\_Session *session*, uint32\_t *irqs*)**

Acknowledges an IRQ or set of IRQs.

**Parameters:**

*session* handle to a currently open session  
*irqs* bitwise OR of NiFpga\_Irqs

**Returns:**

result of the call

---

## FIFO Functions

**NiFpga\_Status NiFpga\_ConfigureFifo (NiFpga\_Session *session*, uint32\_t *fifo*, uint32\_t *depth*)**

Configures the depth of a FIFO.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to configure  
*depth* the number of elements deep the fifo is

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_StartFifo (NiFpga\_Session session, uint32\_t fifo)**

Starts a FIFO.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to start

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_StopFifo (NiFpga\_Session session, uint32\_t fifo)**

Stops a FIFO.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to stop

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoBool (NiFpga\_Session session, uint32\_t fifo, NiFpga\_Bool \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of booleans.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoI8 (NiFpga\_Session session, uint32\_t fifo, int8\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of signed 8-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoU8 (NiFpga\_Session session, uint32\_t fifo, uint8\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of unsigned 8-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoI16 (NiFpga\_Session session, uint32\_t fifo, int16\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of signed 16-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoU16 (NiFpga\_Session session, uint32\_t fifo, uint16\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of unsigned 16-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoI32 (NiFpga\_Session session, uint32\_t fifo, int32\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of signed 32-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo from which to read  
*data* outputs the data that was read  
*count* number of elements to read  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining



**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoU32 (NiFpga\_Session session, uint32\_t fifo, uint32\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of unsigned 32-bit integers.

**Parameters:**

*session* handle to a currently open session

*fifo* the fifo from which to read

*data* outputs the data that was read

*count* number of elements to read

*timeout* timeout in milliseconds, or NiFpga\_Infinite

*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoI64 (NiFpga\_Session session, uint32\_t fifo, int64\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of signed 64-bit integers.

**Parameters:**

*session* handle to a currently open session

*fifo* the fifo from which to read

*data* outputs the data that was read

*count* number of elements to read

*timeout* timeout in milliseconds, or NiFpga\_Infinite

*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_ReadFifoU64 (NiFpga\_Session session, uint32\_t fifo, uint64\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Reads from a FIFO of unsigned 64-bit integers.

**Parameters:**

*session* handle to a currently open session

*fifo* the fifo from which to read

*data* outputs the data that was read

*count* number of elements to read

*timeout* timeout in milliseconds, or NiFpga\_Infinite

*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoBool (NiFpga\_Session session, uint32\_t fifo, const NiFpga\_Bool \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of booleans.

**Parameters:**

*session* handle to a currently open session

*fifo* the fifo to which to write

*data* data to write

*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoI8 (NiFpga\_Session session, uint32\_t fifo, const int8\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of signed 8-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoU8 (NiFpga\_Session session, uint32\_t fifo, const uint8\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of unsigned 8-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoI16 (NiFpga\_Session session, uint32\_t fifo, const int16\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of signed 16-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoU16 (NiFpga\_Session session, uint32\_t fifo, const uint16\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of unsigned 16-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoI32 (NiFpga\_Session session, uint32\_t fifo, const int32\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of signed 32-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoU32 (NiFpga\_Session session, uint32\_t fifo, const uint32\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of unsigned 32-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoI64 (NiFpga\_Session session, uint32\_t fifo, const int64\_t \* data, uint32\_t count, uint32\_t timeout, uint32\_t \* remaining)**

Writes to a FIFO of signed 64-bit integers.

**Parameters:**

*session* handle to a currently open session  
*fifo* the fifo to which to write  
*data* data to write  
*count* number of elements to write  
*timeout* timeout in milliseconds, or NiFpga\_Infinite  
*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call

**NiFpga\_Status NiFpga\_WriteFifoU64 (NiFpga\_Session *session*, uint32\_t *fifo*, const uint64\_t \*  
*data*, uint32\_t *count*, uint32\_t *timeout*, uint32\_t \* *remaining*)**

Writes to a FIFO of unsigned 64-bit integers.

**Parameters:**

*session* handle to a currently open session

*fifo* the fifo to which to write

*data* data to write

*count* number of elements to write

*timeout* timeout in milliseconds, or NiFpga\_Infinite

*remaining* if non-NULL, outputs the number of elements remaining

**Returns:**

result of the call