

# review articles

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**Separation logic is a key development in formal reasoning about programs, opening up new lines of attack on longstanding problems.**

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## Separation Logic

A FUNDAMENTAL TECHNIQUE in reasoning about programs is the use of logical assertions to describe properties of program states. Turing used assertions to argue about the correctness of a particular program in 1949,<sup>40</sup> and they were incorporated into general formal systems for program proving starting with the work of Floyd<sup>21</sup> and Hoare<sup>22</sup> in the 1960s. Hoare logic, which separation logic builds upon, is a formal system for proving specifications of the form

$\{ \text{precondition} \} \text{code} \{ \text{postcondition} \}$

where the precondition and postcondition are assertions describing properties of the input and output states. For example,

$\{x == N\} \text{code} \{x == N \wedge y == N!\}$

can serve as a specification of an imperative program that computes the factorial of the value held in variable  $x$  and places it in  $y$ .

Hoare logic and related systems worked very well for programs manipulating simple primitive data types such as for integers or strings, but proofs became more complex when dealing with structured data containing

embedded pointers. One of the founding papers of separation logic summarized the problem as follows.<sup>32</sup>

"The main difficulty is not one of finding an in-principle adequate axiomatization of pointer operations; rather there is a mismatch between simple intuitions about the way that pointer operations work and the complexity of their axiomatic treatments....when there is aliasing, arising from several pointers to a given cell, an alteration to a cell may affect the values of many syntactically unrelated expressions."

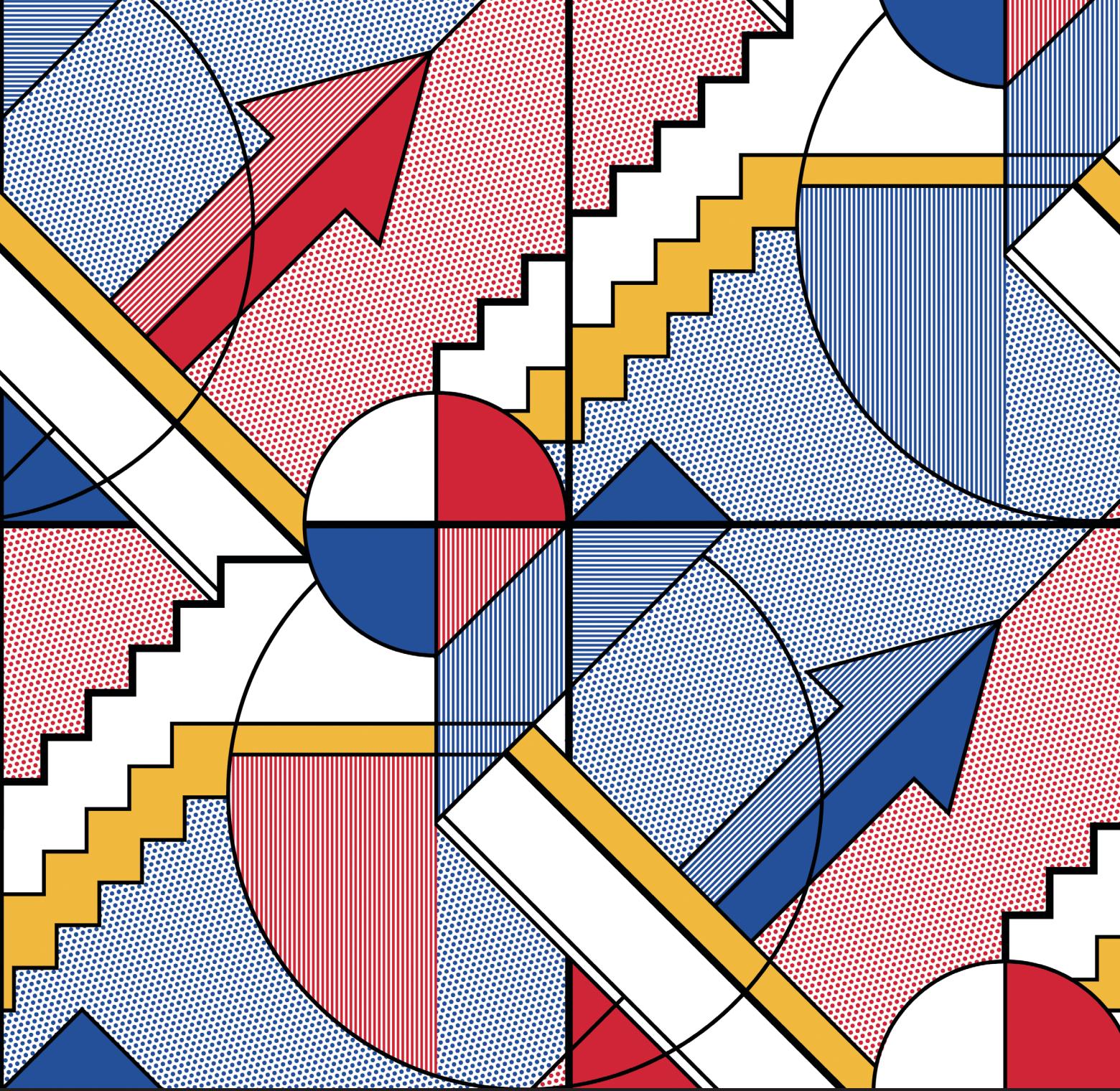
Bornat provided a good description of the struggles in reasoning about mutable data structures up to 2000.<sup>6</sup>

In joint work with John Reynolds and others we developed separation logic (SL) to address the fundamental problem of reasoning about programs that mutate data structures. From a special logic for heaps, it gradually evolved into a general theory for modular reasoning about concurrent as well as sequential programs. Efforts by many researchers established that the logic provides a basis for efficient proof search in automatic and semi-automatic proof tools, for example, giving rise to the Infer static analyzer, a tool that is in deployment at Facebook where it catches thousands of bugs per month before code reaches production in products used daily by over one billion people.

Separation logic is an extension of Hoare logic, which employs novel logical operators, most importantly the separating conjunction \* (pronounced "and

### » key insights

- Separation logic supports in-place updating of facts as we reason, in a way that mirrors in-place update of memory during execution, and this leads to logical proofs about imperative programs that match computational intuition.
- Separation logic supports scalable reasoning by using an inference rule (the frame rule) that allows a proof to be localized to the resources that a program component accesses (its footprint).
- Concurrent separation logic shows that modular reasoning about threads that share storage and other resources is possible.



separately") when writing assertions. For example, we might write:

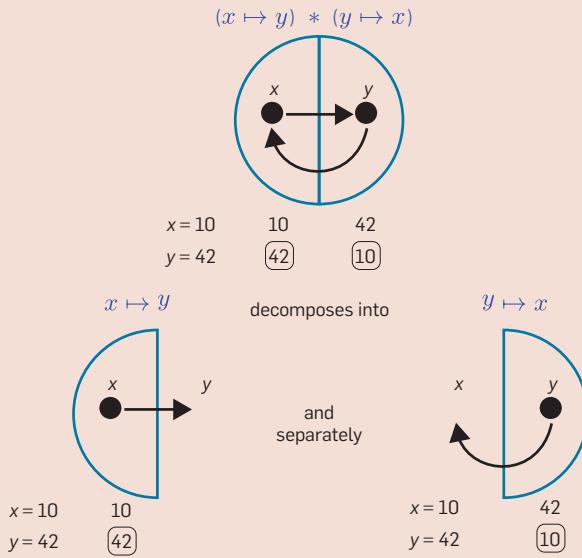
$$\begin{aligned} &\{x \mapsto 0 * y \mapsto 0\} \\ &[x] = y; \\ &[y] = x \\ &\{x \mapsto y * y \mapsto x\} \end{aligned}$$

as a specification of code that wires together two memory locations into a cyclic linked list. Here  $x \mapsto v$  says that pointer variable  $x$  holds the address of a memory location where  $v$  is stored (or more briefly,  $x$  points to  $v$ ), and a command of the form  $[x] = v$  updates the location referred to by  $x$  so that its contents becomes  $v'$ .

The use of  $*$  rather than the usual Boolean conjunction  $\wedge$  ensures  $x$  and  $y$  are not aliases—distinct names for the same location—so that we have a two-element cyclic list in the postcondition. A central principle is that a command that mutates a single location affects only one  $*$ -conjunct: operational in-place update is mirrored in the logic, addressing the key difficulty where “an alteration to a cell may affect the values of many syntactically unrelated expressions.”

Reynolds was the first to describe a program logic including the separating

conjunction; he defined an intuitionistic (constructive) logic with  $*$ ,<sup>37</sup> building on earlier ideas of Burstall.<sup>10</sup> O’Hearn, and Ishtiaq<sup>26</sup> realized the assertion language could be seen as an instance of the resource logic BI of O’Hearn and Pym;<sup>31</sup> they independently discovered the same intuitionistic logic as Reynolds, and also saw that a more powerful Boolean (nonconstructive) variant was possible in which one could reason about explicit memory management (Reynolds had assumed garbage collection). They also introduced the separating implication  $\star$ .

**Figure 1. Picture semantics.****Figure 2. Mathematical semantics.**

- Assume a partial commutative monoid  $(H, \circ, e)$ , where  $\circ : H \times H \rightarrow H$  and  $e \in H$ . Pre/post assertions denote elements of the powerset  $\mathcal{P}(H)$ .
- $*$  lifts  $\circ$  to the powerset  $\mathcal{P}(H)$ :  $P * Q$  is  $\{h_P \circ h_Q \mid h_P \circ h_Q \text{ defined and } h_P \in P \text{ and } h_Q \in Q\}$
- $\text{emp}$  denotes the singleton set of the empty heaplet:  $\{e\}$ .
- $*\neg$  is an implication quantifying over separate heaps:  $P \neg Q$  is  $\{h \mid \forall h_P. h \circ h_P \text{ defined and } h_P \in P \text{ implies } h \circ h_P \in Q\}$
- In the RAM model  $H$  is the set of finite partial functions from positive integers (addressable locations) to integers,  $h \circ h'$  is the union of functions with disjoint domain, and undefined when  $h$  and  $h'$  overlap.  $e$  is the empty partial function. The assertion  $n \mapsto m$  denotes the singleton set  $\{f\}$  where  $f$  maps  $n$  to  $m$  and is undefined elsewhere.
- To deal with variables and also quantifiers consider functions  $s$  from variables to integers, and extend the above semantics pointwise to pairs  $(s, h)$ .

SL for sequential programs reached maturity in a further paper of O'Hearn, Reynolds and Yang.<sup>32</sup> In that work O'Hearn proposed the following principle of local reasoning, both as a way to describe what was special about SL and as a guiding principle for development of reasoning methods.

"To understand how a program works, it should be possible for reasoning and specification to be confined to the cells that the program actually accesses. The value of any other cell will automatically remain unchanged."

A proof rule—the *frame rule*—allowed to infer that cells remain unchanged when they are not mentioned in a precondition. The frame rule was named in homage to the frame problem from artificial intelligence, which concerns axiomatizing state changes without enumerating all of the things that do not change. The frame rule is the key to scalable reasoning in SL.

Reynolds' influential survey article summarized the early developments up to 2002.<sup>38</sup> At the end of this early period, O'Hearn circulated a note that

proposed a concurrent separation logic (CSL). CSL showed efficient reasoning about threads that share access to storage, proofs that mirrored design principles espoused by Dijkstra at the birth of concurrent programming.<sup>16</sup> The correctness of CSL's proof rules (its 'soundness') turned out to be a formidable problem, solved eventually by Brookes. Brookes and O'Hearn were awarded the 2016 Gödel prize for their papers on CSL,<sup>8,30</sup> the significance of which was summed up as follows:

"For the last 30 years experts have regarded pointer manipulation as an unsolved challenge for program verification and shared-memory concurrency as an even greater challenge. Now, thanks to CSL, both of these problems have been elegantly and efficiently solved; and they have the same solution."

—2016 Gödel Prize citation<sup>a</sup>

It is worth remarking that the first part of this citation, about pointer manipulation, applies to sequential and not just concurrent SL.

After the early papers, research on SL expanded rapidly. Starting from a special logic for heaps SL has evolved into a general theory for modular reasoning. Non-standard models of SL based on an abstract model theory due to Pym provided many potential avenues for wider application, and Gardner and others realized that there exist non-standard models that support modular reasoning about intertwined structures as if they were separate. SL has even been applied to interfering processes using fine-grained concurrency, a situation far removed from the original intuitions of the logic.

SL is the basis of numerous automated proof tools, and it has been used in significant verification efforts. It has been used to provide the first verification of a crash-proof file system,<sup>14</sup> and to provide the first verification of a commercial, preemptive OS microkernel.<sup>41</sup> These verification efforts are semi-automatic, done by a human together with a proof assistant (in these cases, the Coq proof assistant). SL has also been used in static program analysis, where weaker properties than full correctness are targeted but with higher automation, so that the tool can scale better both in the sizes of codebases

<sup>a</sup> <https://bit.ly/2ywwlpp>

covered and the number of programmers served. Static analysis with SL has matured to the point where it has been applied industrially in the Facebook Infer program analyzer, an open source tool used at Facebook, Mozilla, Spotify, Amazon Web Services, and other companies ([www.fbinfer.com](http://www.fbinfer.com)).

The purpose of this article is to describe the basic ideas of SL as well as these and other developments.

### Separating Conjunction and Implication

Mathematical semantics has been critical to the discovery and further SL development, but many of the main points can be gleaned from “picture semantics.” Consider the first picture in Figure 1. We read the formula at the top of this figure as “ $x$  points to  $y$  and separately  $y$  points to  $x$ .” Going down the middle of the diagram is a line that represents a heap partitioning: a separating conjunction asks for a partitioning that divides the heap into parts, *heaplets*, satisfying its two conjuncts. At the bottom of the first picture is an example of a concrete memory description that corresponds to the diagram. There,  $x$  and  $y$  have values 10 and 42 (in the “environment,” or “register bank”), and 10 and 42 are themselves locations with the indicated contents (in the “heaplet,” or even “RAM”).

The indicated separating conjunction here is true of the pictured memory because the parts satisfy the conjuncts, as indicated in the second picture. The meaning of “ $x$  points to  $y$  and yet to nothing” is precisely disambiguated in the RAM description below the diagram:  $x$  and  $y$  denote values (10 and 42),  $x$ ’s value is an allocated memory address which contains  $y$ ’s value, but  $y$ ’s value is not allocated. The separating conjunction splits the heap/RAM, but it does not split the association of variables to values.

Generally speaking, the separating conjunction  $P * Q$  is true of a heap if it can be split into two heaplets, one of which makes  $P$  true and the other of which makes  $Q$  true. A distinction between  $*$  and Boolean conjunction  $\wedge$  is that  $P * P \neq P$  where  $P \wedge P = P$ . In particular,  $x \mapsto v * x \mapsto v$  is always false: there is no way to divide any heap in such a way that a cell  $x$  goes to both partitions.

$*$  is often used with linked structures. If  $list(x, y)$  describes an acyclic linked list running from  $x$  to  $y$ , then we can describe a structure with a list segment, followed by a single pointer, followed by a further list running up to 0 (null), as follows:

$$list(x, t) * t \mapsto y * list(y, 0)$$

This is the kind of structure you might need to consider when deleting an element from a list, or inserting one into it.

There is a further connective, the separating implication or “magic wand.”  $P \rightarrow Q$  says that whenever the current heaplet is extended with a separate heaplet satisfying  $P$ , the resulting combined heaplet will satisfy  $Q$ . For example,  $(x \mapsto -) * ((x \mapsto 3) \rightarrow Q)$  says that  $x$  is allocated in the current heap, and that if you mutate its contents to 3 then  $Q$  will hold. This describes the “weakest precondition” for the mutation  $[x] = 3$  with postcondition  $Q$ .<sup>26</sup>

Finally, there is an assertion  $\text{emp}$  which says “the heaplet is empty,”  $\text{emp}$  is the unit of  $*$ , so that  $P = \text{emp} * P = P * \text{emp}$ . Also,  $\rightarrow$  and  $*$  fit together in a way similarly to how implication  $\Rightarrow$  and conjunction  $\wedge$  do in standard logic. For example, the entailment

$$A * (A \rightarrow B) \vdash B$$

(where  $\vdash$  reads “entails”) is a SL relative of “modus ponens.”

Although we will concentrate on the informal picture semantics in this article, for the theoretically inclined we have included a glimpse of the formal semantics in Figure 2.

### Rules for Program Proof

Figure 3 contains a selection of proof rules of SL. The rules are divided into axioms for basic mutation commands (the “small axioms”) and inference rules for modular reasoning. An inference rule says “if you can derive what is above the line, then so can you what is below,” and the axioms are derivable true statements that are given. The small axioms are for a programming language with load and store instructions similar to an assembly language. If we vary the programming language the small axioms change. The concurrency rule uses a composition operator  $\parallel$  for running two processes in parallel, derived from Dijkstra’s parbegin/parend.<sup>16</sup>

The first small axiom just says that if  $x$  points to something beforehand, then it points to  $v$  afterward, and it says this for a small portion of the state in which  $x$  is the only active cell.

Figure 3. Separation logic proof system (a selection).

#### SMALL AXIOMS

*Pointer Write (Store)*

$$\{x \mapsto -\}[x] = v \{x \mapsto v\}$$

*Pointer Read (Load)*

$$\{x \mapsto v\}y = [x] \{y == v \wedge x \mapsto v\}$$

*Allocation*

$$\{\text{emp}\}x = \text{alloc}() \{x \mapsto -\}$$

*De-Allocation*

$$\{x \mapsto -\}\text{free}(x) \{\text{emp}\}$$

#### LOCAL REASONING RULES

*Frame Rule*

$$\frac{\{pre\}code \{post\}}{\{pre * frame\}code \{post * frame\}}$$

*Concurrency Rule*

$$\frac{\{pre_1\}process_1 \{post_1\} \quad \{pre_2\}process_2 \{post_2\}}{\{pre_1 * pre_2\}process_1 \parallel process_2 \{post_1 * post_2\}}$$

The second axiom says that if  $x$  points to  $v$  and we read  $x$  into  $y$ , then  $y$  will have value  $v$ . Here, we distinguish between the value in a variable or register ( $x$  and  $y$ ) and the  $r$ -value in a heap cell whose l-value is the value held in  $x$ . The second axiom assumes that  $x$  does not appear in syntactic expression  $v$  (see O'Hearn et al.<sup>32</sup> for a precise description of this and other variable side conditions).

The allocation axiom says: If you start with no heap, then you end with a heap of size 1. Conversely the De-Allocation axiom starts with a hap of size 1 and ends with the empty heap. The Application axiom assumes that allocation always succeeds. To model a case where allocation might fail we could use a disjunctive postcondition, like  $x \mapsto - \vee x = 0$ ; this is what tools such as SpaceInvader and Infer, discussed later, do for `malloc()` in  $C$ .

The small axioms are so named because each mentions a small amount of memory: a single memory cell. When people first see the axioms they can

come as a shock: aren't they too simple? Previous approaches had complex descriptions accounting for the effect of mutations on global properties of graph-like structures.<sup>6</sup>

In actuality, there is a sense in which the small axioms capture all that is needed to know about the statements they describe. In intuitive terms, we can say that imperative computation proceeds by in-place update, where these primitive statements update or access a single memory cell at a time; describing what happens to only that cell should be enough. The small axioms are thus an extreme illustration of the principle of local reasoning.

The frame rule in Figure 3 provides logical support for this intuition. It allows us to extend reasoning from one to multiple cells; so the seeming restriction to one cell in the small axioms is not a restriction at all, but rather a pleasantly succinct description. For instance, if we choose  $x \mapsto y$  as our frame then the first instance in Figure 4 gives the reasoning

for the second step of the code to wire up a cyclic linked list described at the start of the paper.

The ultimate theoretical support for the small axioms came from a completeness theorem in Yang's Ph.D. thesis.<sup>42</sup> He showed the small axioms and frame rule and several other inference rules (particularly Hoare's rules for strengthening preconditions and weakening postconditions, and a rule for existential quantifiers) can be used to derive all true Hoare triples for these statements.

Locality properties of program behavior, and their connection to logic,<sup>13,44</sup> are critical for these results:

"An assertion talks about a heaplet rather than the global heap, and a spec  $\{P\} C \{Q\}$  says that if  $C$  is given a heaplet satisfying  $P$  then it will never try to access heap outside of  $P$  (other than cells allocated during execution) and it will deliver a heaplet satisfying  $Q$  if it terminates."<sup>2</sup>

In-place reasoning as with the two-element cyclic list has been applied to many imperative programs. As an example, consider the insertion of a node  $y$  into a linked list after position  $x$ . We can do this in two steps: first we swing  $x$ 's pointer so it points to  $y$ , and then we swing  $y$  to point to  $z$  (the node after  $x$ ).

```
{x ↦ z * list(z) * y ↦ -}
[x] = y
{x ↦ y * list(z) * y ↦ -}
[y] = z
{x ↦ y * list(z) * y ↦ z}
```

Here, in the precondition for each step we write the frame in red; it is the blue that is updated in place. The reader can see how, using the small axiom for free together with the frame rule, we could reason about the converse case of removing an element from a list.

This example generalizes to many other list and tree algorithms: insertion, deletion, reversal, and so on. The SL proofs resemble the box-and-pointer arguments that have long been used informally in describing data structure mutation.

These ideas extend to concurrent programs; for example, the second rule instance in Figure 4 uses the concurrency rule to reasons about our two-element cyclic list, but wired up concurrently rather than sequentially. The \* in the precondition in this instance ensures that  $x$  and  $y$  are not aliases, so there is no data race in the parallel program.

**Figure 4. Frame and concurrency examples.**

$$\frac{\begin{array}{c} \{y \mapsto 0\} [y] = x \{y \mapsto x\} \\ \{(y \mapsto 0) * (x \mapsto y)\} [y] = x \{(y \mapsto x) * (x \mapsto y)\} \end{array}}{\begin{array}{c} \{x \mapsto 0\} [x] = y \{x \mapsto y\} \quad \{y \mapsto 0\} [y] = x \{y \mapsto x\} \\ \{(y \mapsto 0) * (x \mapsto 0)\} [x] = y \parallel [y] = x \{(y \mapsto x) * (x \mapsto y)\} \end{array}}$$

**Figure 5. deletetree example.**

```
(1) void deletetree(struct node *root)
{ if( root != 0 )
    { struct node *left = root->l, *right = root->r;
      deletetree(left);
      deletetree(right);
      free( root );
    } }
(2) Spec: {tree(root)} deletetree(root) {emp}
(3) tree(root) = if root == 0 then emp
                  else ∃xy. root ↦ [l: x, r: y] * tree(x) * tree(y).

(4) {root ↦ [l: left, r: right] * tree(left) * tree(right)}
      deletetree(left);
      {root ↦ [l: left, r: right] * emp * tree(right)}
      deletetree(right);
      {root ↦ [l: left, r: right] * emp * emp}
      free( root );
      {emp * emp * emp}
      {emp}
```

The concurrency rule is the main rule of CSL. In applying CSL to languages with dynamic thread creation instead of parbegin/parend different rules are needed, but the basic point that separation allows independent reasoning about processes carries over.

SL's concurrency rule took inspiration from the “disjoint concurrency rule” of Hoare.<sup>23</sup> Hoare's rule used  $\wedge$  in place of  $*$  together with side conditions to rule out interference.<sup>b</sup>  $*$  allows us to extend its applicability to pointer structures. But even without pointers, the CSL rule is more powerful. Indeed, upon seeing CSL

Hoare immediately exclaimed to the author: “We can prove parallel quicksort!” A direct proof can be given using  $*$  to recognize and unite disjoint array partitions.<sup>30</sup>

### Frames, Footprints, and Local Reasoning

The previous section describes how the separating conjunction leads to simple proofs of the individual steps of heap mutations, and how the frame rule embeds reasoning about small chunks of memory within larger memories. Here, the rules' more fundamental role as a basis for scalable reasoning is explained.

I illustrate by reasoning about a recursive program for deleting the nodes in a binary tree. Consider the C program in (1) of Figure 5. This program satisfies the specification in (2) of the figure, where the *tree* predicate says that its argument points to a binary tree in memory. The predicate is defined recursively in (3), with a diagram below depicting what is described by the *else* part of the definition. Note that here we are using a “points-to” predicate  $root \mapsto [l : x, r : y]$  for describing records with *l* and *r* fields.

The use of *emp* in the *if* branch of the definition means that *tree(r)* is true of a heaplet that contains all and only the cells in the tree; there are no additional cells. Thus, the specification of *deletetree(r)* does not mention nodes not in in the tree. This is analogous to what we did with the small axioms for basic statements in Figure 3,

<sup>b</sup> There are variable conditions in some presentations of SL, that can technically be done away with eliminated by using a version of  $*$  that separates variables as well as heap.<sup>34</sup> This article glosses over this issue.

and is a typical pattern in SL reasoning: “small specifications” are used which mention only the cells touched by the program component (its footprint).

The critical part of the proof of the program is presented in (4), where the precondition at the beginning is obtained by unwinding the recursive definition using the if condition  $root \mapsto 0$ . The proof steps then follow the intuitive description of the algorithm: the first recursive call deletes the left subtree, the second call deletes the right subtree, and the final statement deletes the root node. In the pictured reasoning, the overall specification of the procedure is applied as an induction hypothesis at each call site, together with the Frame Rule for showing that the parts not touched by recursive calls are left unchanged. For instance, the assertions for the second recursive call are an instance of the Frame Rule with the triple  $\{tree(right)\} deletetree(right) \{emp\}$  as the premise.

The simplicity of this proof comes about because of the principle of local reasoning. The frame rule allows in-place reasoning for larger-scale operations (entire procedures) than individual heap mutations. And it allows the specification to concentrate on the footprint of a procedure instead of the global state. Put contrapositively, the *deletetree* procedure could not be verified without the frame rule, unless we were to complicate the initial specification by including some representation of frame axioms (saying what does not change) to enable the proofs at the recursive call sites.

This reasoning uses a tree predicate suitable for reasoning about memory safety; it mentions that we have a tree, but not what data it holds. For functional correctness reasoning, it is typical to use inductive predicates that connect memory structures to mathematical entities. In place of *tree(root)* we could have a predicate *tree*( $\tau$ , *root*) that says *root* points to an area of memory representing the mathematical binary tree  $\tau$ , where a mathematical tree is either empty or an atom or a pair of trees. We could then specify a procedure for copying a tree using a postcondition of the form

$$tree(\tau, oldroot) * tree(\tau, newroot)$$

that says we have two structures in memory representing the same mathemati-

cal tree. An assertion like this would tell us that we could mutate one of the trees without affecting the other (at which point they would cease to represent the same tree).

For data structures without much sharing, such as variations on lists and trees, reasoning in SL is reminiscent of reasoning about purely functional programs: you unroll an inductive definition, then mutate, then roll it back up. Inductive definitions using  $*$  and mutation go well together. The first SL proof to address complex sharing was done by Yang in his Ph.D. thesis, where he provided a verification of the classic Schorr-Waite graph-marking algorithm. The algorithm works by reversing links during search, and then restoring them later: A space-saving representation of the stack of a recursive algorithm. Part of the main invariant in Yang's proof is

$$(listMarkedNodesR(stack, p) \\ * (restoredListR(stack,t) —* spansR(STree, root)))$$

capturing the idea that if you replace the list of marked nodes by a restored list, then you get a spanning tree. Yang's proof reflected the intuition that the algorithm works by a series of local surgeries that mutate small parts of the structure: The proof decomposed into verifications of the surgeries, and ways of combining them.

The idiomatic use of  $-*$  in assertions of the form  $A * (B -* C)$  to describe generalized update was elevated to a general principle in work of Hobor and Villard.<sup>25</sup> They give proofs of a number of programs with significant sharing, including graphs, dags, overlaid structures (for example, a list overlaying a tree), and culminating in the copying algorithm in Cheney's garbage collector.

Many papers on SL have avoided  $-*$ , often on the grounds that it complicates automation and is only needed for programs with significant sharing. However,  $-*$  is recently making something of a comeback. For example, it is used routinely as a basic tool in the Iris higher-order logic.<sup>29</sup>

### Concurrency, Ownership, and Separation

The concurrency rule in Figure 3 says: To prove a parallel composition we give each process a separate piece of state, and separately combine the postcon-

**Figure 6. Concurrency proofs.**

(1)	$\{tree(left)\} deletetree(left) \parallel \{tree(right)\} deletetree(right)$	$\{emp\}$	$\{emp * emp\}$
(2)	$\{emp\}$ $x = alloc();$ $\{x \mapsto -\}$ $put(x);$ $\{emp\}$	$\{emp * emp\}$	$\{emp\}$ $get(y);$ $\{y \mapsto -\}$ $free(y);$ $\{emp\}$

ditions for each process. The rule supports completely independent reasoning about processes. This rule can be used to provide straightforward proofs of processes that don't share access to storage. We mentioned parallel quicksort earlier, and `deletetree()` provides another illustration: we can run the two recursive calls in parallel rather than sequentially, as presented in the proof outline (1) in Figure 6.

In work on CSL, proof outlines are often presented in a spatial fashion like this: this outline shows the premises of the concurrency rule in the left and right Hoare triples, the overall precondition (the *pre1 \* pre2*) at the beginning, and the post at the end.

While this reasoning is simple, if CSL had only been able to reason about disjoint concurrency, where there is no inter-process interaction, then it would have rightly been considered rather restrictive. An important early example done with CSL was a pointer-transferring buffer, where one thread allocates a pointer and puts it into a buffer while the other thread reads it out and frees it. Crucially, not only is the pointer deemed to transfer from one process to another, but the "knowledge that it is allocated" transfers with the proof. The proof establishing absence of memory errors is shown in (2) of Figure 6. A way to implement the buffer code for `put` and `get` is to use locks to synchronize access to a shared variable and a Boolean to signal when the buffer is full. We will not delve into the subproofs of buffer operations here—for that, consult O'Hearn<sup>30</sup>—but we want to talk about a shift in perspective on the meanings of logical assertions that the proof (2) led to.

Notice the assertion `emp` after the `put(x)` statement in the left process.

We could not prove a mutation were we to place it there, because `emp` is not a sufficient precondition for any mutation; that is fortunate as such a mutation could lead to a race condition. But it is not the case that we know the global heap is empty, because the pointer `x` could still persist. Rather, the knowledge that it points to something has been forgotten, transferred to the second process where it materializes as  $y \mapsto -$ . A reading of assertions began to form based on the "right to dereference" or "ownership" (taken as synonymous with right to dereference). On this reading `emp` says "I don't have permission to dereference any heap," or "I own nothing," rather than "the heap is empty." Similarly,  $x \mapsto -$  says "I own `x`" (where "I" is the process from which the assertion is made).

The ownership transfer example made it clear that quite a few concurrent programs would have much simpler proofs than before. Modular proofs were provided of semaphore programs, of a toy memory manager, and programs with interacting resources. It seemed as if the proofs mirrored design principles used to simplify reasoning about concurrent processes, such as in Dijkstra's idea of *loosely connected processes*:

"[A]part from the (rare) moments of explicit intercommunication, the individual processes are to be regarded as completely independent of each other."<sup>16</sup>

However, the very feature that gave rise to the unexpected power, ownership transfer, made soundness (whether the rules prove only true statements) non-obvious. O'Hearn worked on soundness during 2001 and 2002, without success. In May of 2002 he turned to Brookes who eventually (with important input from

Reynolds), in 2004, proved the theorem, which justified the logic.

## Abstraction and the Fiction of Separation

There was considerable work on extending SL after those early papers. Some of it concentrated on different programming paradigms, such as object-oriented programming or scripting languages, or on additional programming primitives such as message passing, reentrant lock and fork/join concurrency. Besides extensions to cover an ever-greater variety of programming, two conceptual developments opened major new directions.

► In his Ph.D. thesis, Parkinson showed how **abstract predicates** (predicate variables) fit together nearly with `*` in the description of classes and other stateful data abstractions.<sup>33</sup>

► Gardner and others emphasized a concept of fictional separation, where **strong separation properties could be assumed of data abstractions**, even for implementations relying on sharing.

These ideas were first described in a sequential setting. Dinsdale-Young, Gardner and Wheelhouse described an implementation of a module of sequences in terms of linked lists and noted a mismatch: at the abstract level an operation might affect a small part of a sequence, where at the implementation level its footprint could involve the entire list; conversely, locality can increase with abstraction.<sup>19</sup> Meanwhile, Parkinson initially targeted a sequential subset of Java. Subsequent work showed how abstract predicates could be understood using higher-order versions of SL.<sup>5</sup>

While they could be expressed in a sequential setting, the ideas took flight when transported to concurrency. The CAP logic<sup>18</sup> combined insights on abstract predicates and fiction, along with those of CSL, to reason about data abstractions with interference in their implementations. The views theory<sup>17</sup> provided a foundation where separation does not appear in the normal execution semantics of programs, but only in an abstraction of it. Views showed that a simple version of CSL can embed many other techniques including even the classic rely-guarantee method;<sup>27</sup> this is surprising because rely-guarantee was invented for reasoning about interference, almost the opposite of the basis of original SL.

Today, advanced logics are often formulated as variations on the theme of “higher-order concurrent separation logic.” One of these, Verifiable C, is the foundation of Appel’s Verified Software Toolchain,<sup>1</sup> and includes an expressive higher-order logic supporting recursive predicates. Iris<sup>29</sup> encompasses reasoning about fine-grained concurrency and even relaxed memory, based on different instantiations of a single generic model. Iris has been used to provide a foundation of the type system of the Rust programming language,<sup>28</sup> which is very natural when you consider that ownership transfer is one of the central ideas in Rust.

Technically, these works are based on “non-standard models” of SL, different from the heaplet model but instances of Pym’s resource semantics as in Figure 2; see Pym et al.<sup>36</sup> There are many such models, including ones incorporating read and other permissions,<sup>7</sup> auxiliary state,<sup>39</sup> time,<sup>39</sup> protocols,<sup>29</sup> and others. Abstract SL<sup>13</sup> showed how general program logic could be defined based on these models, and the works just mentioned and others showed that some of them had surprising ramifications.

Fictional separation and views worked to reimagine fundamental concepts. The programs being proven go beyond the loosely connected processes that CSL was originally designed for. Significant new theoretical insights and soundness arguments were needed to justify the program-proof rules supporting the fine-grained concurrency examples.<sup>17</sup> This led to a flowering of interest and new ideas which is still in progress. A recent survey on CSL provides many more references in addition to those mentioned here.<sup>9</sup>

## Directions in Mechanized Reasoning

SL spawned new approaches to verification tools. In order to provide a taste of where the field has gone, we present a sampling of practical achievements; that is, we focus on the end points rather than the (important) advancements along the way that helped get there. Further references to the literature, including discussion on intermediate advances, may be found in the appendix (<https://bit.ly/2CQD9CU>).

*Mostly automatic verification.* Smallfoot,<sup>2</sup> from Calcagno, Berdine, and

O’Hearn, was the first SL verification tool. Given procedure pre/post specs, loop invariants and invariants governing lock usage, Smallfoot attempts to construct a proof. For the pointer-transferring buffer, given a buffer invariant and pre/post specs for put and get it can verify memory safety and race freedom.

Smallfoot used a decidable fragment of SL dubbed “symbolic heap,” formulae of the form  $B \wedge H$  where  $H$  is a separating conjunction of heap facts and  $B$  is a Boolean assertion over non-heap data. The format was chosen to make in-place symbolic execution efficient. Smallfoot’s heap facts were restricted to points-to assertions, linked lists and trees. Subsequent works extended symbolic heaps in numerous directions, covering more inductive definitions as well as arrays and arithmetic; see appendix (<https://bit.ly/2CQD9CU>).

Some of the most substantial automatic verifications done with SL have been carried out with the VeriFast tool of Jacobs and colleagues. VeriFast employs a symbolic execution engine like Smallfoot, but integrates a dedicated SL theorem prover with a classical SMT solver for non-heap data. A paper reports on the verification of several industrial case studies, including Java Card programs and device drivers written in C;<sup>35</sup> see VeriFast’s GitHub site for these and many other examples (<https://github.com/verifast/verifast>).

*Interactive verification.* In an automatic verifier like Smallfoot, the proof construction is automatic, given the pre/post annotations plus invariants. In interactive verification the human helps guide the proof search, commonly using a proof assistant such as Coq, HOL4, or Isabelle. Interactive verification can often prove stronger properties than automatic verifiers, but the cost is higher.

Interactive verifiers have been used to prove small, intricate algorithms. A recent paper reports on the verification of low-level concurrent algorithms including a CAS-lock, a ticketed lock, a GC allocator, and a non-blocking stack.<sup>39</sup> An emphasis is placed on reusability; for instance, the stack uses the GC allocator, which in turn uses a lock, but the stack uses the spec of the allocator and the allocator uses the spec rather than the implementation of a lock.

The verifiable C logic<sup>1</sup> has been

used to prove crypto code. For example, OpenSSL’s HMAC authentication code, comprising 134 lines of C, was proven using 2,832 lines of Coq.<sup>4</sup>

A larger example is the FSCQ file system.<sup>14</sup> The code and the proof are both done in Coq, taking up 31k lines of proof+code. This compares to 3k lines of C for a related unverified file system. Although the initial effort, which included development of a program logic framework in Coq, took several person years, experiments show incremental, lower cost when modifying code+proof.

A commercial example concerns key modules of a preemptive OS kernel, the μC/OS-II.<sup>41</sup> Modules verified include the scheduler, interrupt handlers, and message queues. 1.3k lines of C were proven using 216k lines of Coq. It took four person years to develop the framework, one-person year to prove the first module, and then the remaining modules, around 900 lines of C, took six person-months.

*Automatic program analysis.* With a verification-oriented program analysis the annotations that a human would supply to a mostly automatic verifier like Smallfoot— invariants and pre/post specs—are inferred. A tool will be able to prove weaker properties when the human is not supplying annotations, but can more easily be deployed broadly to many programmers.

Program analysis with SL has received a great deal of attention. At first, analysis was formulated for simple linked lists,<sup>20</sup> and progressively researchers moved on to more involved data structures. A practical high point in this line of work was the verification of pointer safety in Linux and Windows device drivers up to 10k LOC by the SpaceInvader program analyzer.<sup>43</sup> SpaceInvader was an academic tool; its sibling, SLAyer,<sup>3</sup> developed in parallel at Microsoft, was used internally to find 10s of memory safety errors in Windows device drivers. SpaceInvader and SLAyer were able to analyze complex, linear data structures: for example, oneWindows driver manipulated five-cyclic doubly linked lists sharing a common header node, three of which had acyclic sublists.

Like much research in verification-oriented program analysis these techniques worked in a whole-program fashion: you start from main() or

other entry points and explore the program graph, perhaps visiting procedure bodies multiple times. This can be expensive. While accurate analysis of 10k LOC can be a leading research achievement, 10k is tiny compared to software found in the wild. A single company can have tens of millions of lines of code. **Progress toward big code called for a radical departure.**

### Bi-Abduction and Facebook Infer

In 2008 Calcagno asked: What is the main obstacle blocking application of SpaceInvader and similar tools to programs in the millions of LOC? O'Hearn answered: **The need for the human to supply preconditions.** He proposed that a “truly modular” analysis based on local reasoning could accept a program component with no human annotations, and generate a pre/post spec where the precondition approximates the footprint. The analysis would then “stitch” these specifications together to obtain results for larger program parts. The analysis would be compositional, in that a **spec for a procedure could be obtained without knowing its callers**, and the hypothesis was that it would scale because procedures could be visited independently. This implied **giving up on whole-program analysis**.

Calcagno, O'Hearn, Distefano and Yang set to work on realizing a truly modular analysis. Yang developed a scheme based on **gleaning information from failed proofs to discover a footprint**. Distefano made a breakthrough on the stitching issue for the modular analysis that involved a new inference problem:

Bi-abduction: given  $A$  and  $B$ , find  $?frame$  and  $?anti-frame$  such that

$$A * ?anti-frame \vdash B * ?frame$$

where  $\vdash$  is read ‘entails’ or ‘implies.’ The inference of  $?frame$  (the leftover part in  $A$  but not  $B$ ) was present in Smallfoot, and is used in many tools. The  $?anti-frame$  part (the missing bit needed to establish  $B$ ), is **abduction, or inference of hypotheses**, an inference problem identified by the philosopher Charles Peirce in his conceptual analysis of the scientific method. As a simple example,

$$(x \mapsto -) * ?anti-frame \vdash (y \mapsto -) * ?frame.$$

can be solved with

$$?anti-frame = y \mapsto - \text{ and } ?frame = x \mapsto -.$$

With bi-abduction we can automate the local reasoning idea by abducing assertions that describe preconditions, and using frame inference to keep specifications small. Let us illustrate with the program we started the paper with. We begin symbolic execution with nothing in the precondition, and we ask a bi-abduction question, using the current state  $\text{emp}$  as the  $A$  part of the bi-abduction query and the  $\text{pre}$  of the small axiom for  $[x] = y$  as  $B$ .

- Execution state:  
 $\{\text{emp}\}[x] = y; [y] = x \{???\}$
- Bi-abduction query:  
 $\text{emp} * ?anti-frame \vdash x \mapsto - * ?frame$
- Solution:  
 $?anti-frame = x \mapsto -; ?frame = \text{emp}.$

Now, we move the abduced anti-frame to the overall precondition, we take one step of symbolic execution using the small axiom for Pointer Write from Figure 2, we install the  $\text{post}$  of the small axiom as the  $\text{pre}$  of the next instruction, and we continue.

- Execution state:  
 $\{x \mapsto -\}[x] = y \{x \mapsto y\} [y] = x \{???\}$
- Bi-abduction query:  
 $x \mapsto y * ?anti-frame \vdash y \mapsto - * ?frame$
- Solution:  
 $?anti-frame = y \mapsto -; ?frame = x \mapsto y.$

The formula  $y \mapsto -$  in the bi-abduction query is the precondition of the small axiom for the pointer write  $[y] = x$ : we abduce it as the anti-frame, and add it to the overall precondition. The frame rule tells us that the inferred frame  $x \mapsto y$  is unaltered by  $[y] = x$ , when it is separately conjoined with  $y \mapsto -$ , and this with the small axiom gives us our overall postcondition in

$$\begin{aligned} &\{x \mapsto - * y \mapsto -\} \\ &[x] = y; \\ &[y] = x \\ &\{x \mapsto y * y \mapsto x\} \end{aligned}$$

So, starting from specifications for primitive statements, **we can infer both a precondition and a postcondition for a compound statement** by repeated applications of bi-abduction and the frame rule. This facility leads to a high degree of automation. Also, note that the precondition here is more general than the one at the start of the paper, because it does not mention 0. Bi-abductive analy-

sis not infrequently finds more general specifications than a top-down analysis that dives into procedures at call sites; finding general specs is important for both scalability and precision.

The main bi-abduction paper<sup>12</sup> contributed proof techniques and algorithms for abduction, and a novel compositional algorithm for generating pre/post specs of program components. Experimental results scaled to hundreds of thousands of lines, **and a part of Linux of 3M lines**. This form of analysis finds preconditions supporting safety proofs of clusters of procedures as well as indicating potential bugs where proofs failed.

This work led to the program proof startup Monoidics, founded by Calcagno, Distefano and O'Hearn in 2009. Monoidics developed and marketed the Infer tool, based on the abductive technique. Monoidics was acquired by Facebook in 2013 at which point Calcagno, Distefano, and O'Hearn moved to Facebook with the Monoidics engineering team ([www.fbinfer.com](http://fbinfer.com)).

The compositional nature of Infer turned out to be a remarkable fit for Facebook's software development process.<sup>11</sup> A codebase with millions of lines is altered thousands of times per day in “code diffs” submitted by the programmers. Instead of doing a whole-program analysis for each diff, Infer analyzes changes (the diffs) compositionally, and reports regressions as a bot participating in the internal code review process. Using bi-abduction, the frame rule picks off (an approximation of) just enough state to analyze a diff, instead of considering the entire global program state. The way that compositional analysis supports incremental diff analysis is even more important than the ability to scale; a linear-time analysis operating on the whole program would usually be too slow for this deployment model. Indeed, Infer has evolved from a standalone SL-based analyzer to a general framework for compositional analyses (<http://fbinfer.com/docs/checkers.html> and appendix; <https://bit.ly/2CQD9CU>).

### Conclusion

Some time during 2001, while sitting together in his back garden, Reynolds turned to me and exclaimed: “The

logic is nice, but it's the model that's really important." My own prejudice for semantics made me agree immediately. We were both beguiled by the fact that this funky species of logic could be described using down-to-earth computer science concepts like RAMs and access bits.

What happened later came as a surprise. The specific heap/RAM model gave way in importance to a more general class of nonstandard models based on fictional rather than down-to-earth separation. And the logic itself, particularly its proof theory, turned out to be extremely useful in automatic verification, leading to many novel research tools and eventually to Facebook Infer.

Still, I expect that in the long run it will be the spirit rather than the letter of SL that is more significant. Concepts of frames, footprints, and separation as a basis for modular reasoning seem to be of fundamental importance, independently of the syntax used to describe them. Indeed, one of the more important directions I see for further work is in theoretical foundations that get at the essence of scalable, modular reasoning in as formalism-independent a way as possible. Theoretical synthesis would be extremely useful for three reasons: To make it easier for people to understand what has been achieved by each new idea; to provide a simpler jumping-off point for future work than the union of the many specific advances; and, to suggest new, unexplored avenues. Hoare has been advancing an abstract, algebraic theory related to CSL, which has components covering semantics, proof theory, and testing,<sup>24</sup> and work along these lines is well worth exploring further.

Other relevant reference points are works on general versions of SL,<sup>13,17</sup> abstract interpretation,<sup>15</sup> and work on "separation without SL" discussed in the appendix (<https://bit.ly/2CQD9CU>). Semantic fundamentals would be crucial to an adequate general foundation, but I stress that proof theoretic and especially algorithmic aspects addressing the central problem of scale should be covered as well.

In conclusion, scalable reasoning about code has come a long way since the birth of SL around the turn of the millennium, but it seems to me that much more is possible both in funda-

mental understanding and in mechanized techniques that help programmers in their daily work. I hope that scientists and engineers will continue to innovate on the fascinating problems in this area.

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