

ASSIGNMENT PRESENTATION

TOPIC : STATIC AND DYNAMIC

FLIP FLOP

BATCH MEMBERS:

20L141-SHEENA

21L408-MERUSHETHA

21L415-SANGAVI

21L416-SANGEETHA

INTRODUCTION

- Memory elements are of two types:
- Static memory:

They preserve the state as long as the power is turned on

They are built using positive feedback

- Dynamic memory:

They are based on the temporary charge storage on the parasitic capacitors associated with MOS device.

STATIC FLIP FLOP

- They are Master Slave Edge Triggered Flip Flops which are multiplexer based Latches
- Using Transmission Gates D Flip Flop can be designed

POWER AND AREA ANALYSIS

APPLICATION

SIMULATION

DYNAMIC FLIP FLOP

POWER AND AREA ANALYSIS

APPLICATION

SIMULATION