

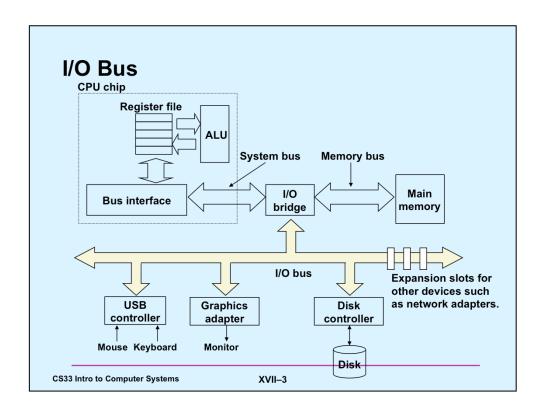
Most of the slides in this lecture are either from or adapted from slides provided by the authors of the textbook "Computer Systems: A Programmer's Perspective," 2nd Edition and are provided from the website of Carnegie-Mellon University, course 15-213, taught by Randy Bryant and David O'Hallaron in Fall 2010. These slides are indicated "Supplied by CMU" in the notes section of the slides.

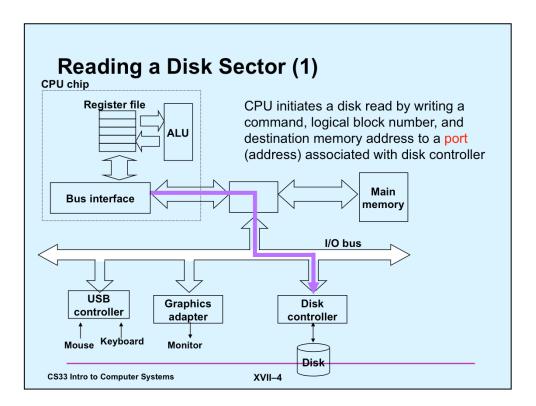
Logical Disk Blocks

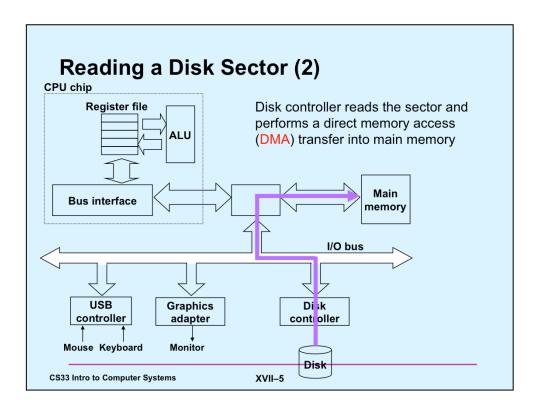
- Modern disks present a simpler abstract view of the complex sector geometry:
 - the set of available sectors is modeled as a sequence of b-sized logical blocks (0, 1, 2, ...)
- Mapping between logical blocks and actual (physical) sectors
 - maintained by hardware/firmware device called disk controller
 - converts requests for logical blocks into (surface, track, sector) triples
- Allows controller to set aside spare cylinders for each zone
 - accounts for the difference in "formatted capacity" and "maximum capacity"

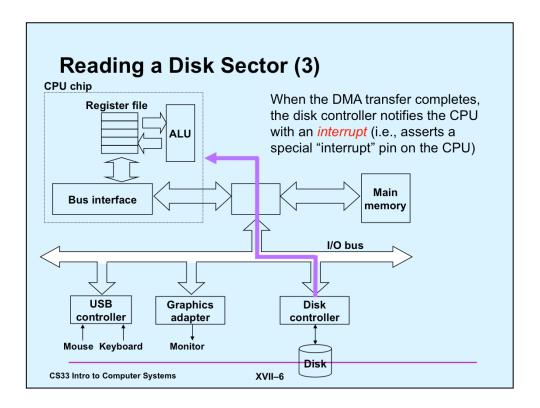
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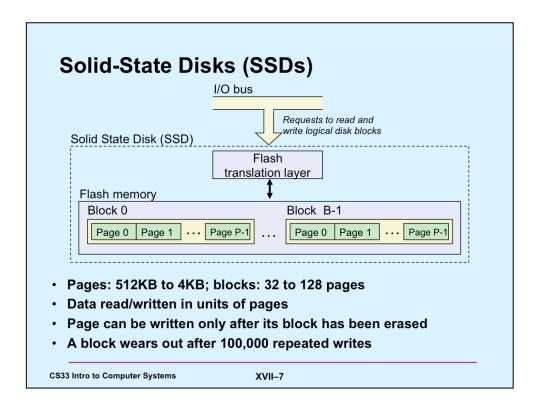
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SSD Performance Characteristics

Sequential read tput250 MB/sSequential write tput170 MB/sRandom read tput140 MB/sRandom write tput14 MB/sRandom read access30 usRandom write access300 us

- · Why are random writes so slow?
 - erasing a block is slow (around 1 ms)
 - modifying a page triggers a copy of all useful pages in the block
 - » find a used block (new block) and erase it
 - » write the page into the new block
 - » copy other pages from old block to the new block

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SSD Tradeoffs vs Rotating Disks

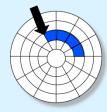
- Advantages
 - no moving parts → faster, less power, more rugged
- Disadvantages
 - have the potential to wear out
 - » mitigated by "wear-leveling logic" in flash translation layer
 - » e.g. Intel X25 guarantees 1 petabyte (10¹⁵ bytes) of random writes before they wear out
 - in 2010, about 100 times more expensive per byte
- Applications
 - MP3 players, smart phones, laptops
 - beginning to appear in desktops and servers

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Reading a File on a Rotating Disk

- Suppose the data of a file are stored on consecutive disk sectors on one track
 - this is the best possible scenario for reading data quickly
 - » single seek required
 - » single rotational delay
 - » all sectors read in a single scan



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Quiz 1

We have two files on the same (rotating) disk. The first file's data resides in consecutive sectors on one track, the second in consecutive sectors on another track. It takes a total of t seconds to read all of the first file then all of the second file.

Now suppose the files are read concurrently, perhaps a sector of the first, then a sector of the second, then the first, then the second, etc. Compared to reading them sequentially, this will take

- a) less time
- b) about the same amount of time
- c) more time

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Quiz 2

You've replaced the rotating disk on your computer with a solid-state disk. The data of the two files are again in consecutive locations. Suppose it still takes a total of t seconds to read the first file then the second. Suppose it took u seconds to read the two files concurrently on the rotating disk. It takes v seconds to read them concurrently on the SSD.

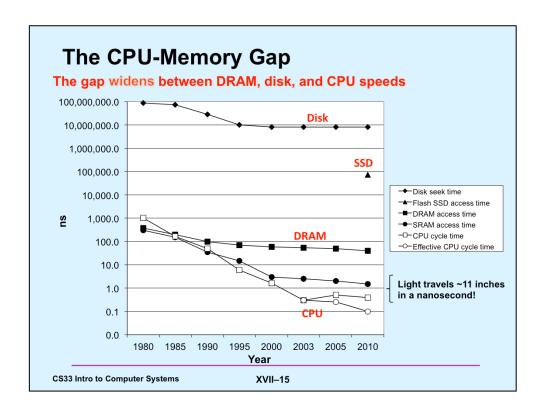
- a) v < u (faster on the SSD)
- b) $v \approx u$ (about the same)
- c) v > u (slower on the SSD)

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Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB access (ns)	19,200 300	2,900 150	320 35	256 15	100 3	75 2	60 1.5	320 200
DRAM Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB access (ns) typical size (MB)	8,000 375 0.064	880 200 0.256	100 100 4	30 70 16	1 60 64	0.1 50 2,000	0.06 40 8,000	130,000 9 125,000
Disk								
Metric	1980	1985	1990	1995	2000	2005	2010	2010:1980
\$/MB access (ms) typical size (MB)	500 87 1	100 75 10	8 28 160	0.30 10 1,000	0.01 8 20,000	0.005 4 160,000	0.0003 3 1,500,000	1,600,000 29 1,500,000
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CPU Clock Rates					Inflection point in computer history when designers hit the "Power Wall"			
	1980	1990	1995	2000	2003	2005	2010	2010:1980
CPU	8080	386	Pentium	P-III	P-4	Core 2	Core i7	
Clock rate (MHz)) 1	20	150	600	3300	2000	2500	2500
Cycle time (ns)	1000	50	6	1.6	0.3	0.50	0.4	2500
Cores	1	1	1	1	1	2	4	4
Effective cycle time (ns)	1000	50	6	1.6	0.3	0.25	0.1	10,000

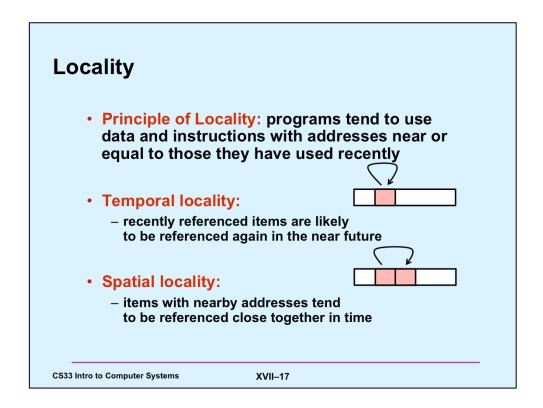


Locality to the Rescue!

The key to bridging this CPU-Memory gap is a fundamental property of computer programs known as locality

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Locality Example

```
sum = 0;
for (i = 0; i < n; i++)
    sum += a[i];</pre>
return sum;
```

Data references

- reference array elements in succession (stride-1 reference pattern)

Spatial locality

- reference variable sum each iteration Temporal locality

Instruction references

- reference instructions in sequence.

Spatial locality

- cycle through loop repeatedly

Temporal locality

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Qualitative Estimates of Locality

- Claim: being able to look at code and get a qualitative sense of its locality is a key skill for a professional programmer
- Question: does this function have good locality with respect to array a?

```
int sum_array_rows(int a[M][N]) {
   int i, j, sum = 0;

   for (i = 0; i < M; i++)
        for (j = 0; j < N; j++)
            sum += a[i][j];
   return sum;
}</pre>
```

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Quiz 3

Does this function have good locality with respect to array a?

- a) yes
- b) no

```
int sum_array_cols(int a[M][N]) {
   int i, j, sum = 0;

for (j = 0; j < N; j++)
   for (i = 0; i < M; i++)
       sum += a[i][j];
   return sum;
}</pre>
```

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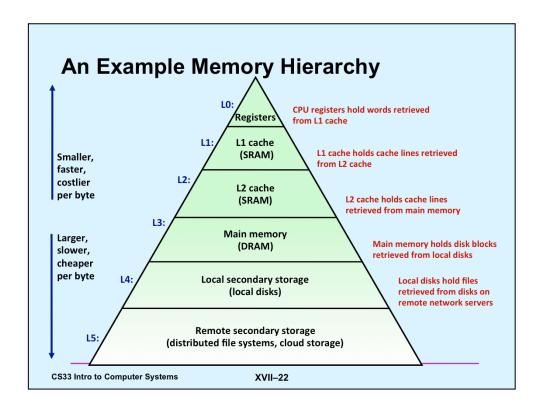
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Memory Hierarchies

- Some fundamental and enduring properties of hardware and software:
 - fast storage technologies cost more per byte, have less capacity, and require more power (heat!)
 - the gap between CPU and main memory speed is widening
 - well written programs tend to exhibit good locality
- These fundamental properties complement each other beautifully
- They suggest an approach for organizing memory and storage systems known as a memory hierarchy

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Putting Things Into Perspective ...

- · Reading from:
 - ... the L1 cache is like grabbing a piece of paper from your desk (3 seconds)
 - ... the L2 cache is picking up a book from a nearby shelf (14 seconds)
 - ... main system memory is taking a 4-minute walk down the hall to talk to a friend
 - ... a hard drive is like leaving the building to roam the earth for one year and three months

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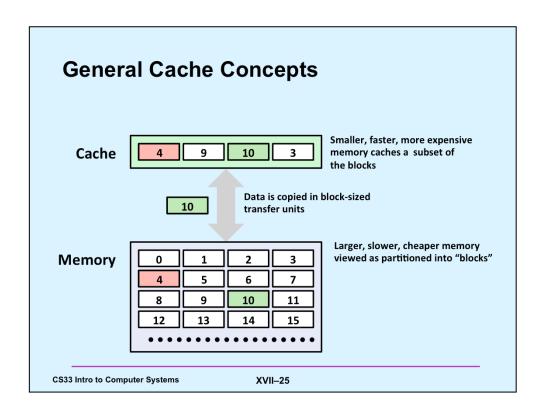
This analogy is from http://duartes.org/gustavo/blog/post/what-your-computer-does-while-you-wait (definitely worth reading!).

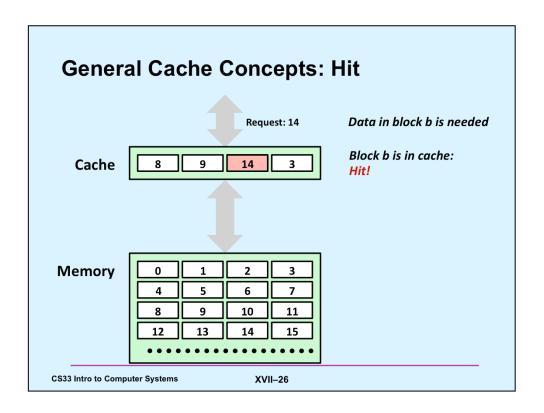
Caches

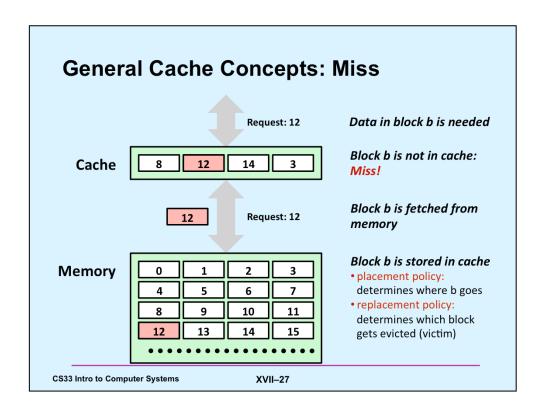
- Cache: A smaller, faster storage device that acts as a staging area for a subset of the data in a larger, slower device
- Fundamental idea of a memory hierarchy:
 - for each k, the faster, smaller device at level k serves as a cache for the larger, slower device at level k+1
- Why do memory hierarchies work?
 - because of locality, programs tend to access the data at level k more often than they access the data at level k+1.
 - thus, the storage at level k+1 can be slower, and thus larger and cheaper per bit
- Big Idea: the memory hierarchy creates a large pool of storage that costs as much as the cheap storage near the bottom, but that serves data to programs at the rate of the fast storage near the top

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General Caching Concepts: Types of Cache Misses

- · Cold (compulsory) miss
 - cold misses occur because the cache is empty
- Conflict miss
 - most caches limit blocks at level k+1 to a small subset (sometimes a singleton) of the block positions at level k
 - » e.g., block i at level k+1 must be placed in block (i mod 4) at level k
 - conflict misses occur when the level k cache is large enough, but multiple data objects all map to the same level k block
 - » e.g., referencing blocks 0, 8, 0, 8, 0, 8, ... would miss every time
- Capacity miss
 - occurs when the set of active cache blocks (working set) is larger than the cache

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Examples of Caching in the Hierarchy							
Cache Type	What is Cached?	Where is it Cached?	Latency (cycles)	Managed By			
Registers	4-8 bytes words	CPU core	0	Compiler			
TLB	Address translations	On-Chip TLB	0	Hardware			
L1 cache	64-bytes block	On-Chip L1	1	Hardware			
L2 cache	64-bytes block	On/Off-Chip L2	10	Hardware			
Virtual Memory	4-KB page	Main memory	100	Hardware + O			
Buffer cache	Parts of files	Main memory	100	os			
Disk cache	Disk sectors	Disk controller	100,000	Disk firmware			
Network buffer cache	Parts of files	Local disk	10,000,000	CIFS/NFS client			
Browser cache	Web pages	Local disk	10,000,000	Web browser			
Web cache	Web pages	Remote server disks	1,000,000,000	Web proxy server			

Summary

- The speed gap between CPU, memory, and mass storage continues to widen
- Well written programs exhibit a property called locality
- Memory hierarchies based on caching close the gap by exploiting locality

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