

1 Readout

Foreseen is a readout with the TOFPET ASIC by PETsys Electronics. However there have been no significant developments towards implementing the readout system for the B-ToF detector.

The readout system consists of two main stages. The first stage consists of an assembly of small analog boards as seen in Fig. 1, the FEB/A, which is part of the *front end readout system*, holding the TOFPET ASIC, which converts the incoming signal into digital charge and timing information.

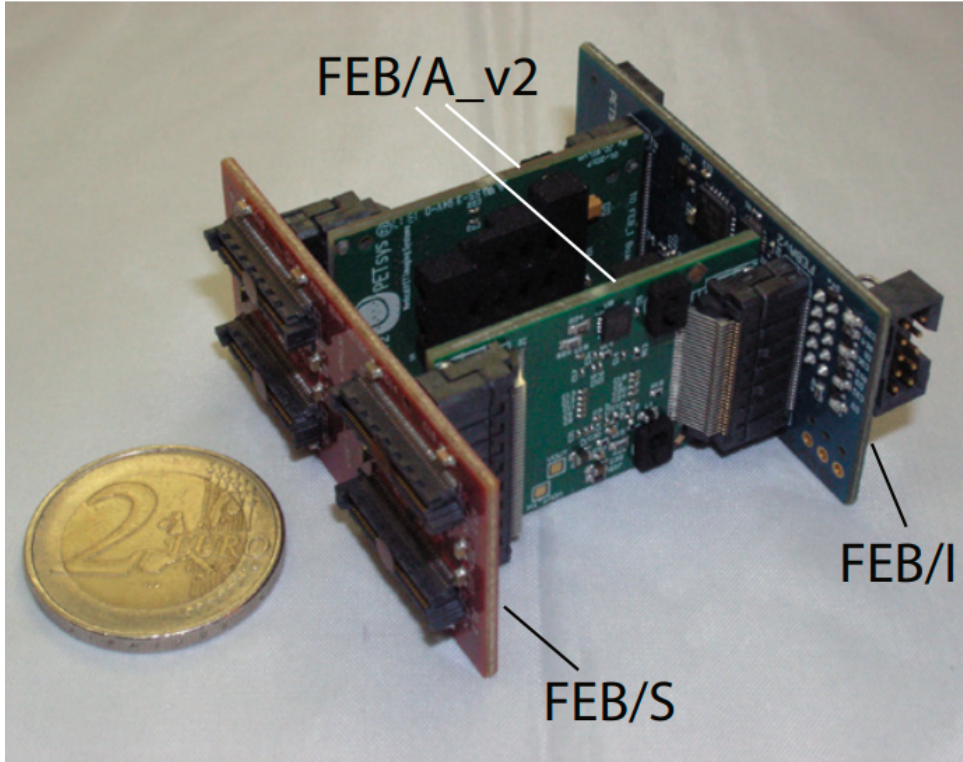


Figure 1: Connection of the FEB/A to the FEB/S and FEB/I.

The second stage is a digital board, the FEB/D, to which up to 8 FEB/A boards connect to and which can be daisy-chained with other FEB/D boards for simple control.

Due to the small dimensions of the available space in the detector slot the boards for the detector need to be redesigned.