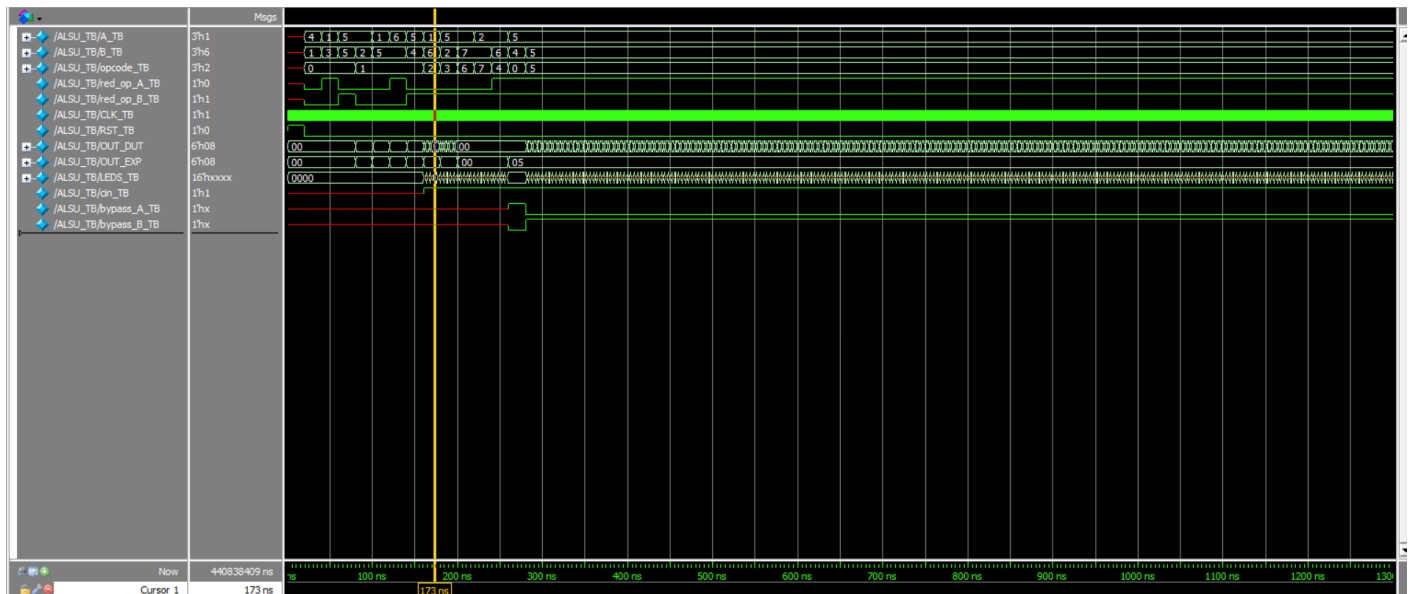


# **Digital Elec. WorkShop**

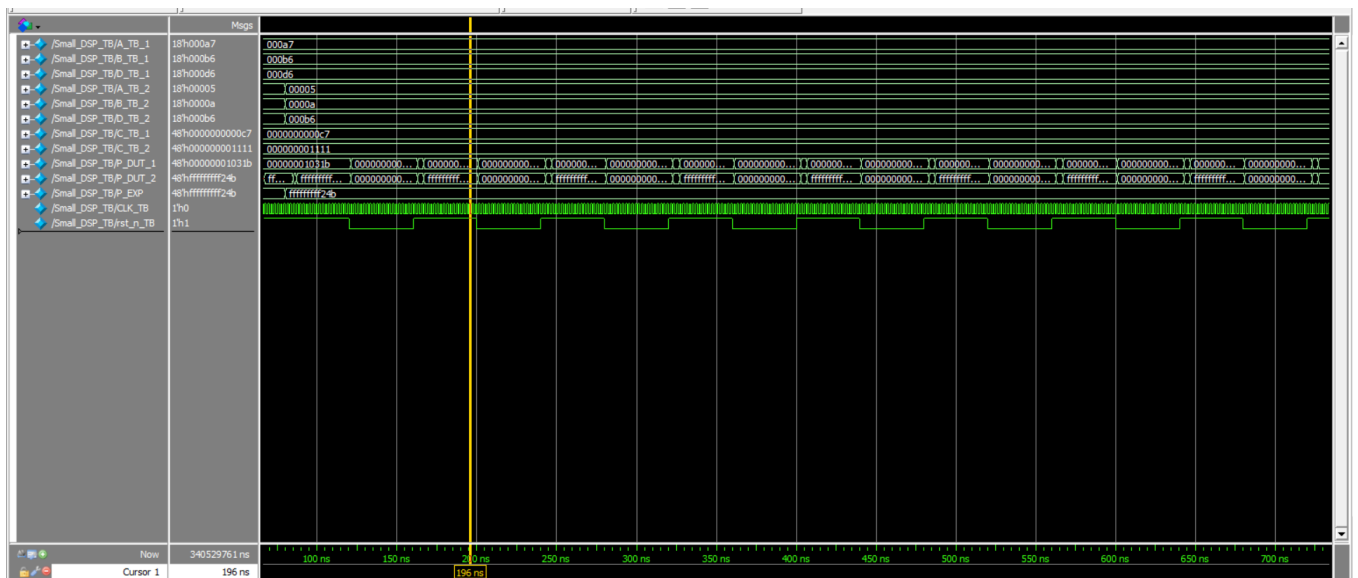
## **Final Project**

**By: Shehab El-Din Tarek**

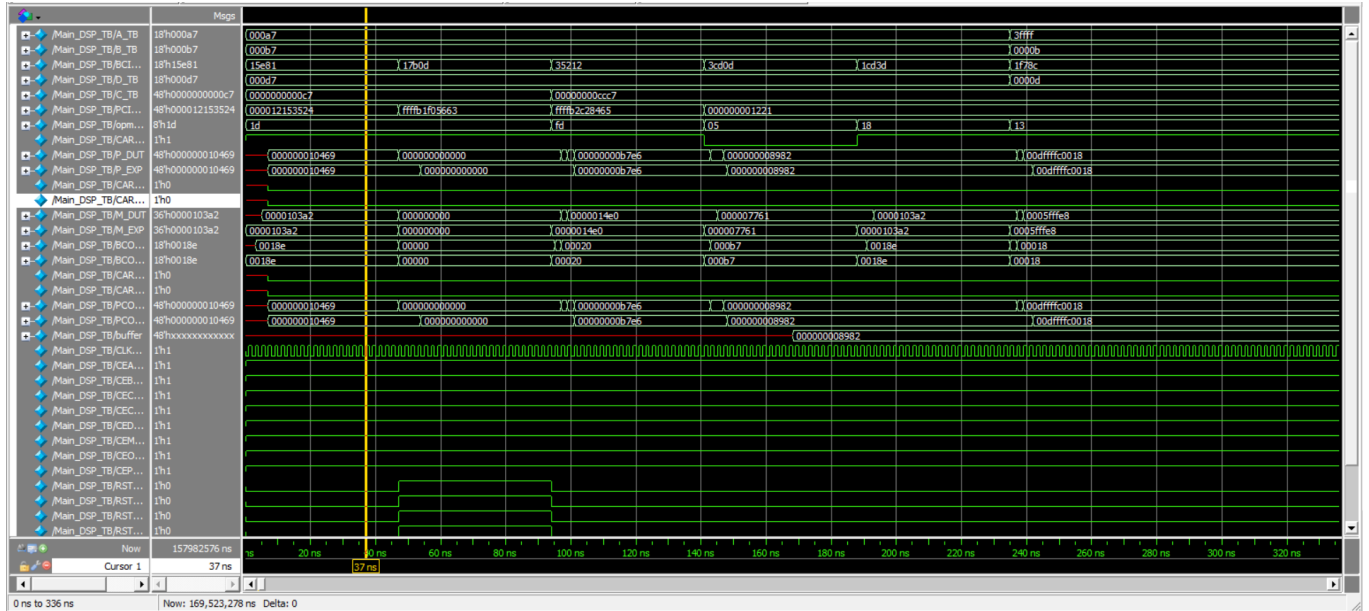
## • ALSU Signals Simulation:



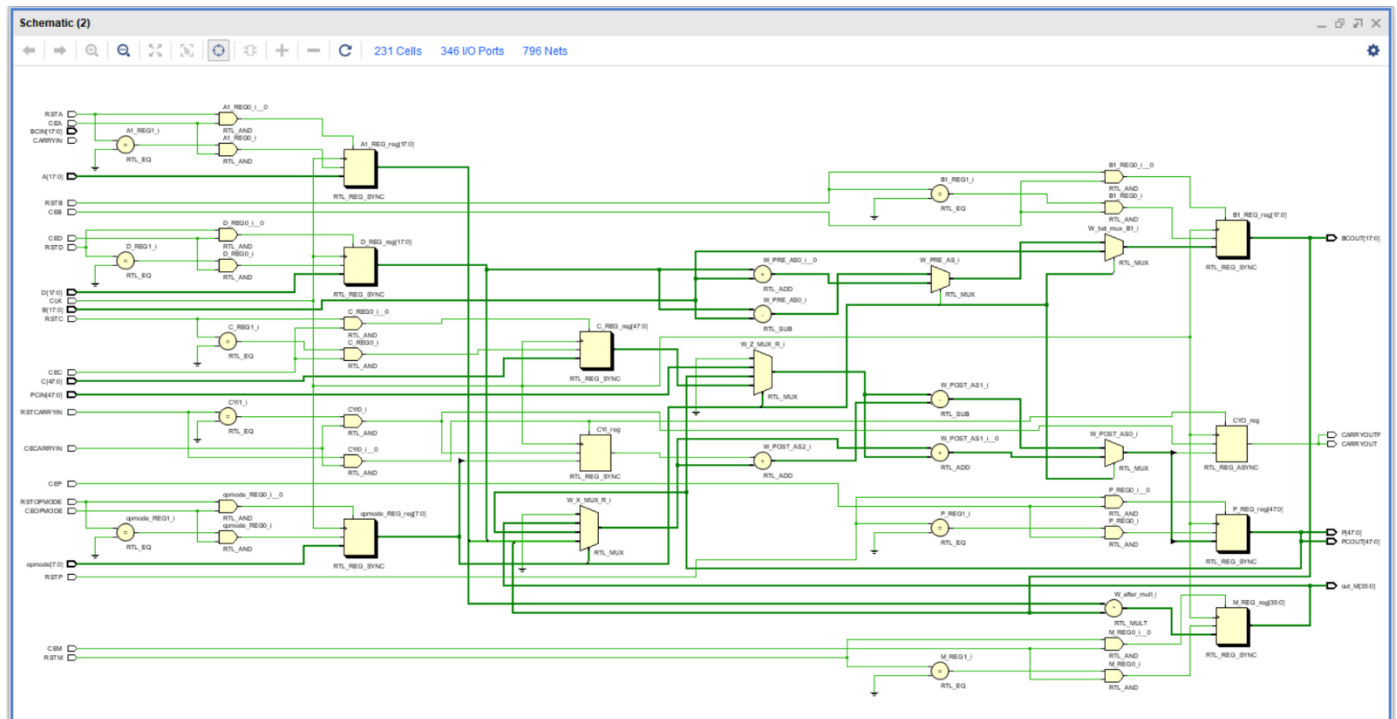
## • Small DSP Signals Simulation:



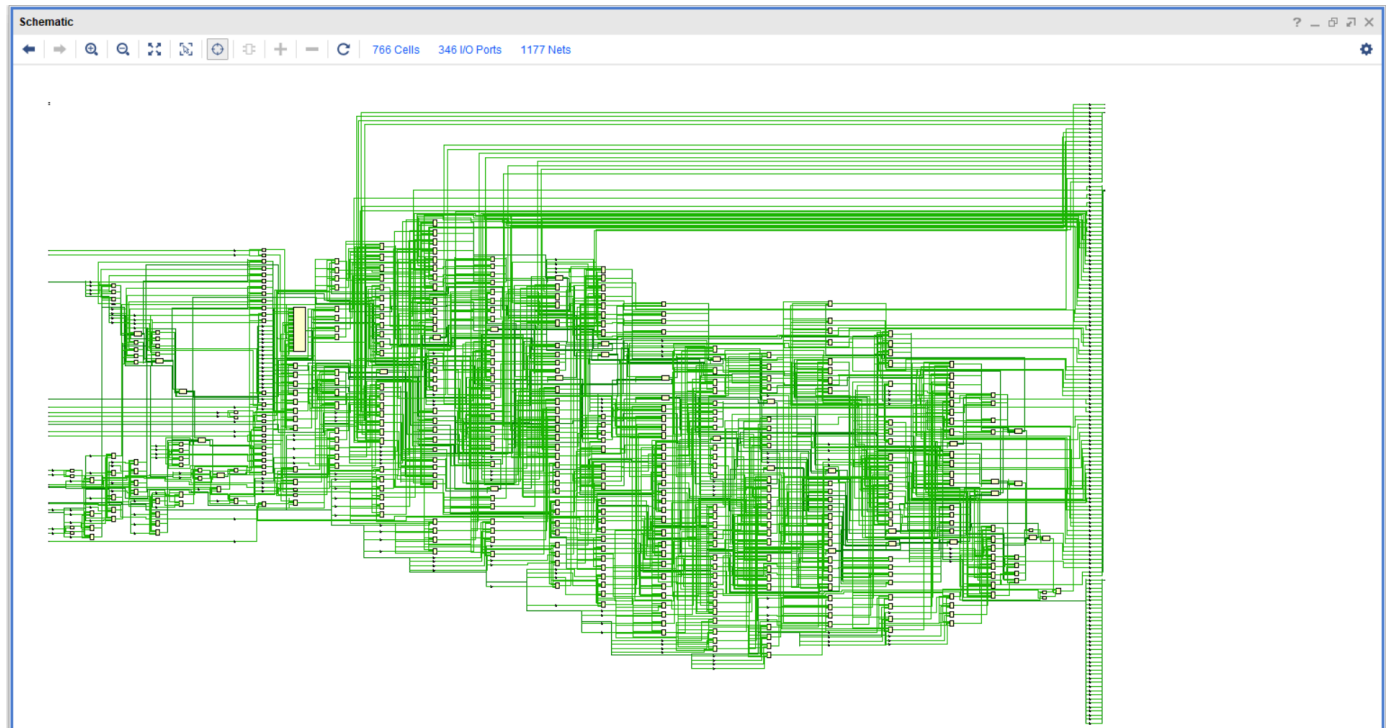
- **Main DSP Signals Simulation:**



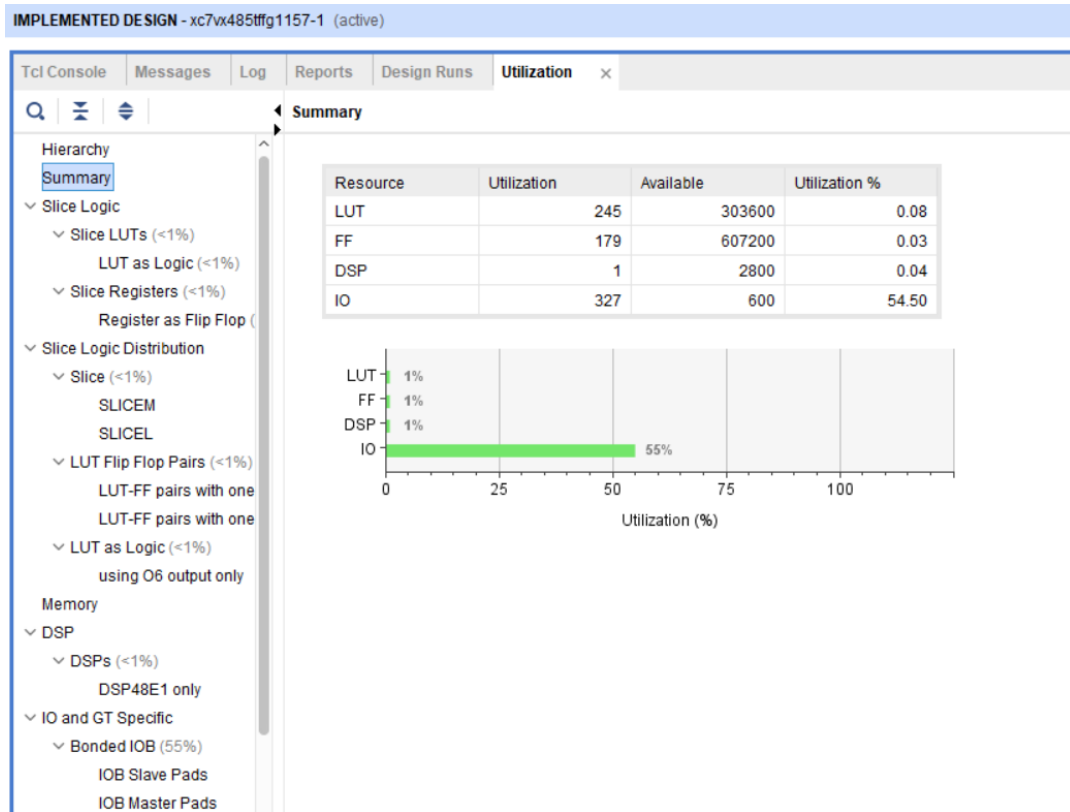
- **Main DSP Elaboration:**



- **Main DSP Synthesis:**



- **Main DSP Utilization report:**



- Main DSP Implementation:

