

# Flip-flops & Race-around Problems

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# Checkpoint

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# Before We Start!!!

- Before we dive into the discussions of Flip-flops, let's go for a quick introduction of it!
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- Putting it more formally in case of circuits... present output depends on present input.
- **But output of sequential circuits depend on past output too!!!**



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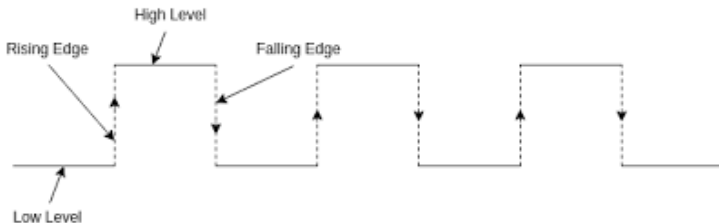
- That brings an important point: we need to **store** the past output somewhere.
- For that, we need storage devices.
- And here is where **Flip-flop** comes in play.
- This whole thing is also known as **storing the state of our circuit**.

# Two Important Questions

- ① What does Flip-flop do?
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- 1 What does Flip-flop do?
  - It stores and updates the state of our circuit
- 2 How does it know when to update the state?
  - It updates upon receiving a signal, whom we refer to as **clock**



# Checkpoint

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# Types of Flip-flops

- SR (Set-Reset FF)
- D (Delay FF)
- T (Toggle FF)



# Types of Flip-flops

- SR (Set-Reset FF)
- D (Delay FF)
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- **JK (Jack Kilby FF)**

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# Truth Table for JK Flip-flop

J	K	$Q_{n-1}$	$Q_n$	Mode
0	0	0	0	Hold/Store
0	0	1	1	
0	1	0	0	Reset
0	1	1	0	
1	0	0	1	Set
1	0	1	1	
1	1	0	1	Toggle
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# Some Trouble Waiting?

**When  $J = K = 1$ , an interesting event may occur depending on the clock mechanism. This event is known as Race Around Condition**



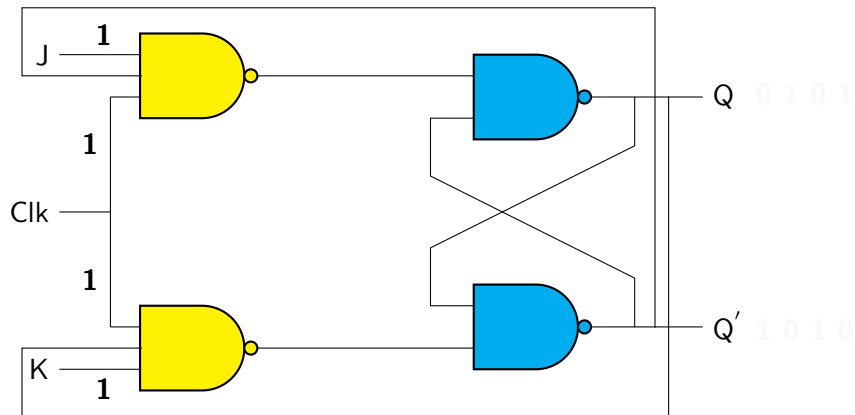
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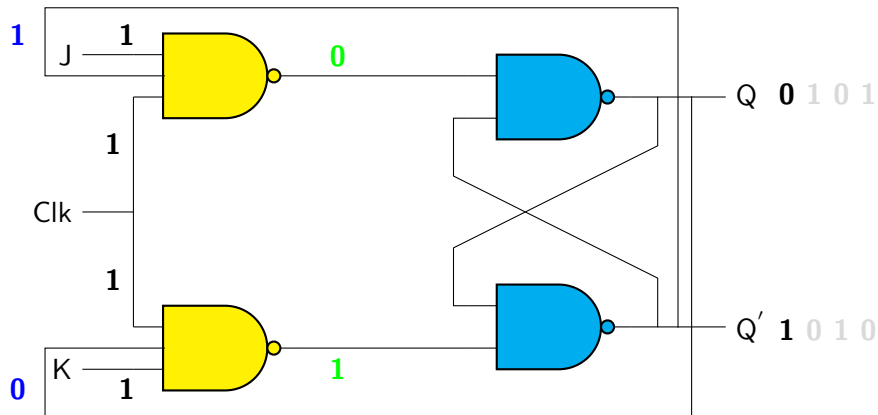
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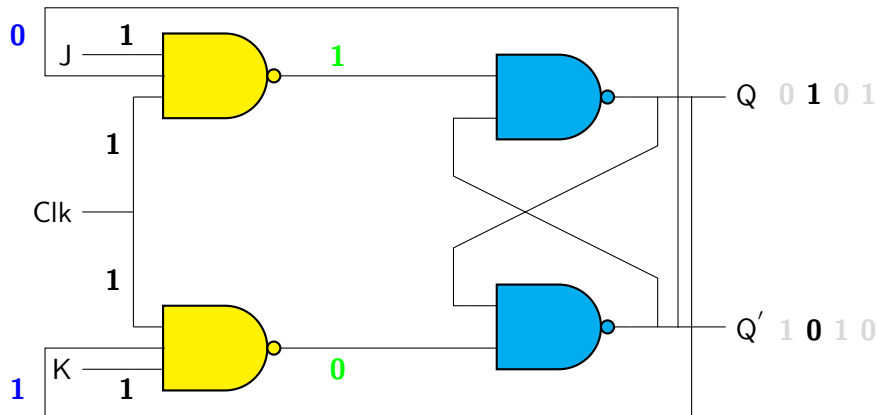
# Circuit Diagram for $J = K = 1$



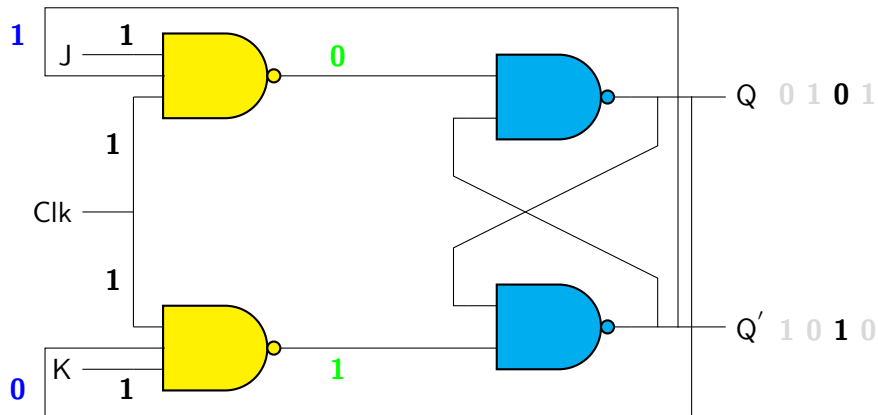
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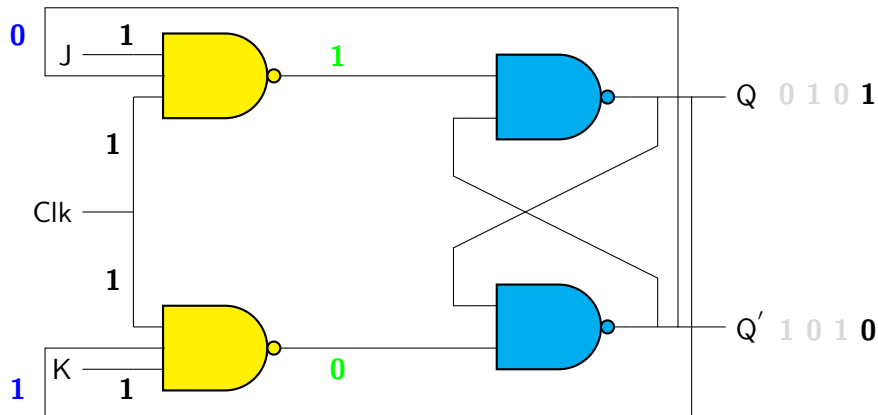
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# Some Misconception!!

## Fallacy

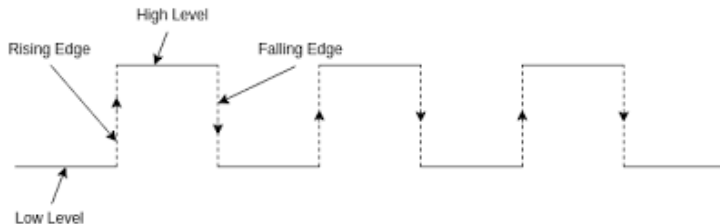
Toggling & Race Condition is same

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# Solution

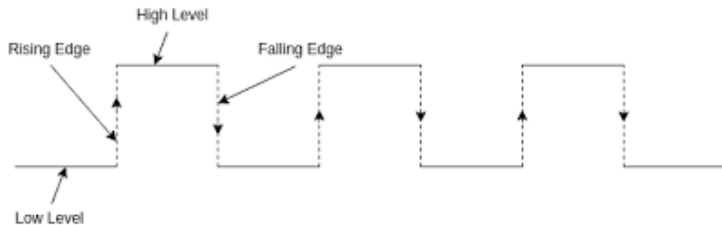
- 1 Use edge triggering instead of level triggering



- 2 Use Master-Slave JK Flip-Flop

# Solution

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