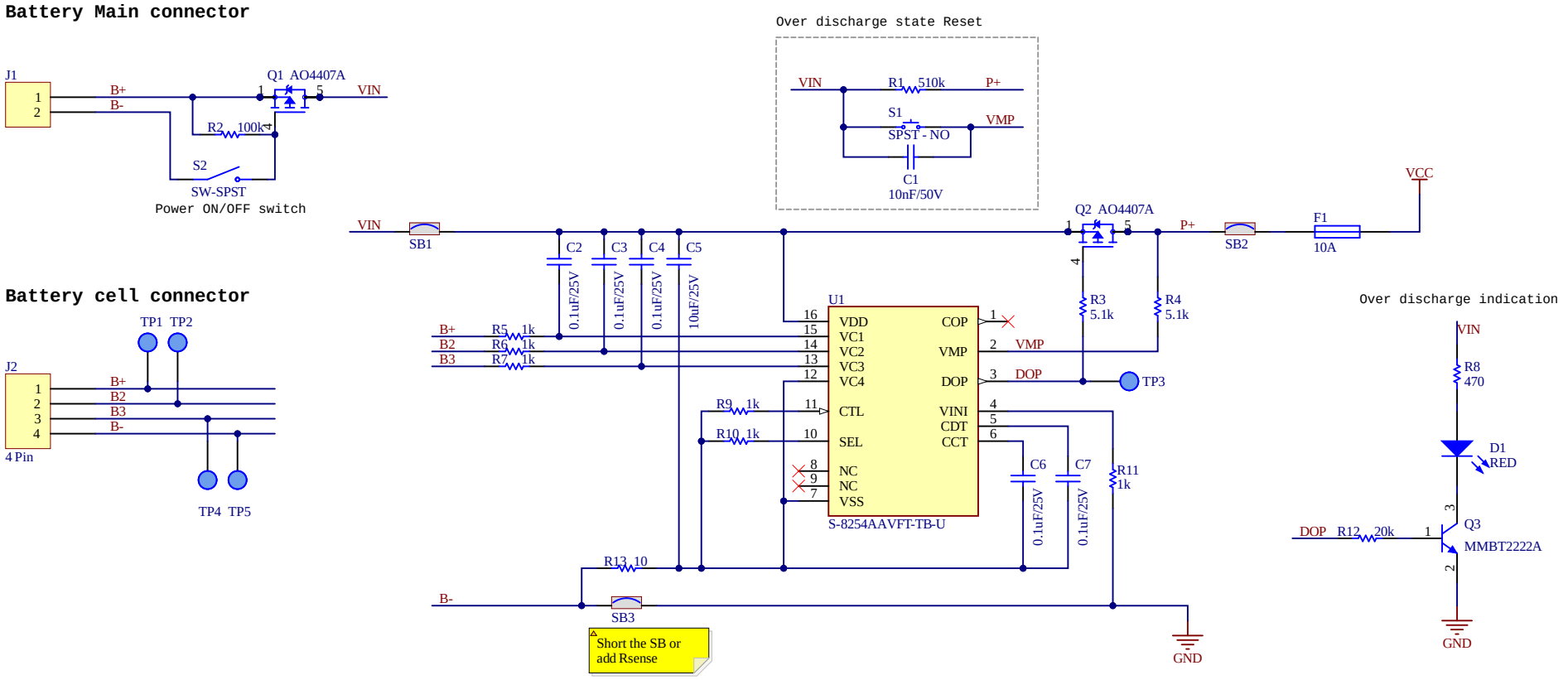
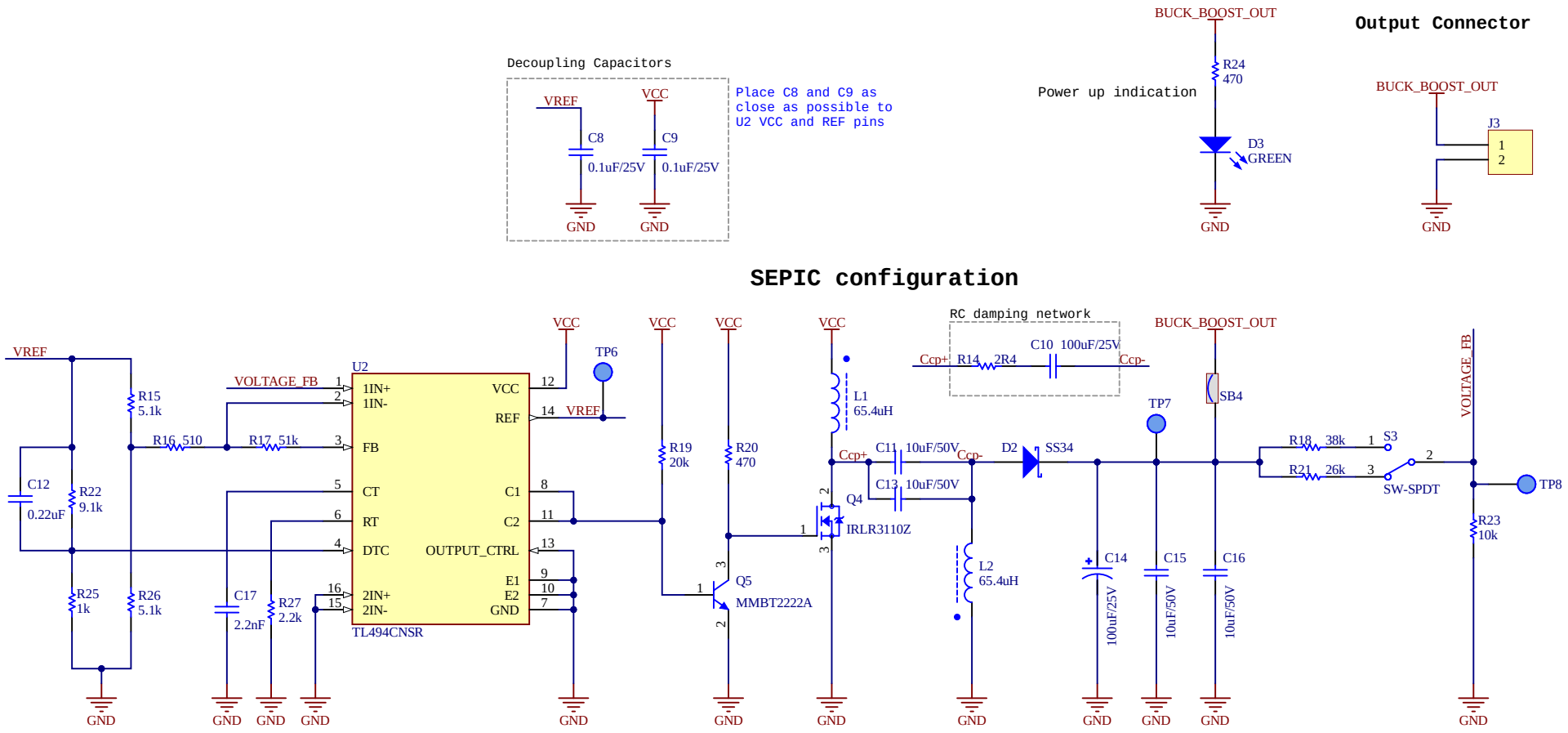


Title : *DC2DC_Buck_Boost_Converter*			
Page : **Under voltage detection**			ENTC Electronic and Telecommunication Engineering
Size: A4	Number:01	Revision:1.0	
Date: 10/25/2024 Time: 8:06:04 PM	Sheet1 of 4		
Sheet Name : 01.Under_voltage_detection.SchDoc			Drawn By : Batch20_ENTC_UOM



Title : *DC2DC_Buck_Boost_Converter*		
Page : **Power Path**		
Size: A4	Number:02	Revision:1.0
Date: 10/25/2024 Time: 8:06:05 PM	Sheet2 of 4	
Sheet Name : 02.Power_path.SchDoc		Drawn By : Batch20_ENTC_UOM

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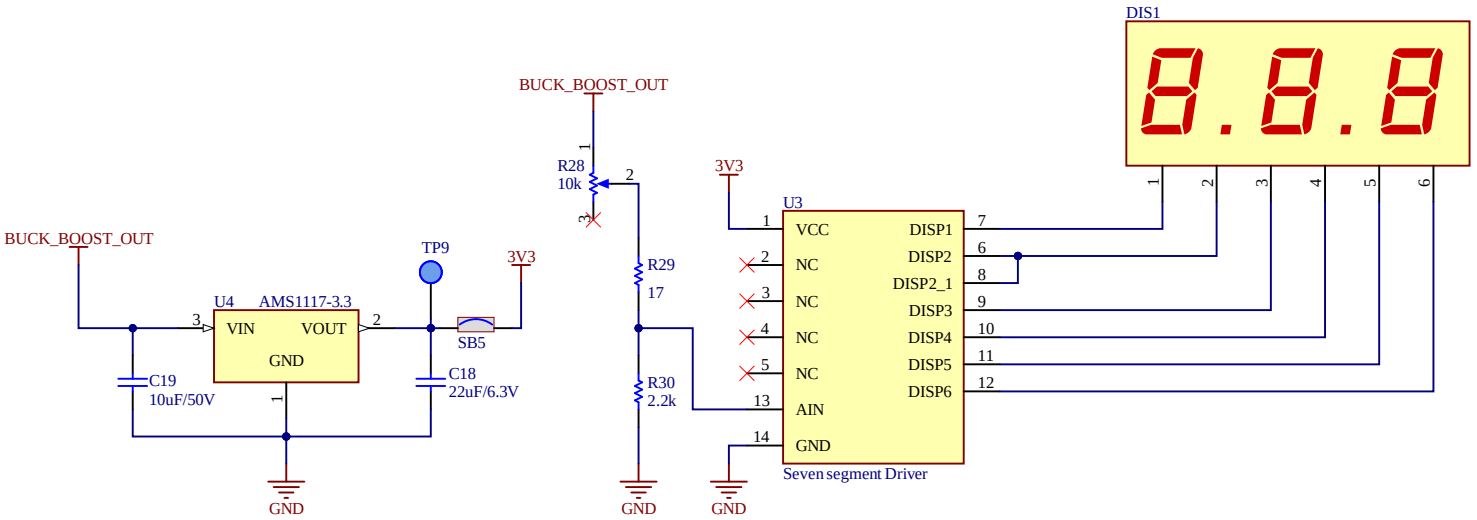
3 Digit 0.28" Seven Segment Display



Title : ***DC2DC_Buck_Boost_Converter***			
Page : **Output voltage display**			
Size: A4	Number:03	Revision:1.0	
Date: 10/25/2024 Time: 8:06:05 PM	Sheet3 of 4		
Sheet Name : 03.Display.SchDoc			Drawn By : Batch20_ENTC_UOM

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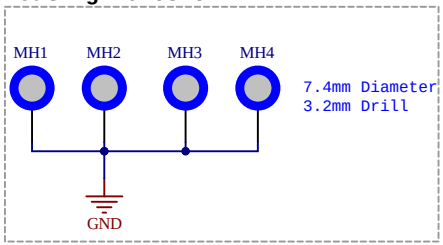
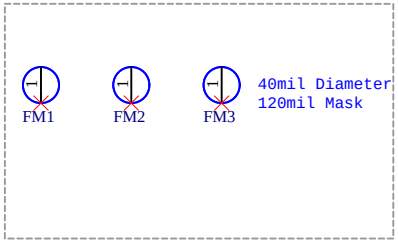
Mouting Holes 3mm x 4



Fiducial Marks x 3



Test point - Reference

Title : *DC2DC_Buck_Boost_Converter*			
Page : **Mechanical Information**			
Size: A4	Number:04	Revision:1.0	
Date: 10/25/2024 Time: 8:06:05 PM		Sheet4 of 4	
Sheet Name : 04.Mechanical.SchDoc			Drawn By : Batch20_ENTC_UOM

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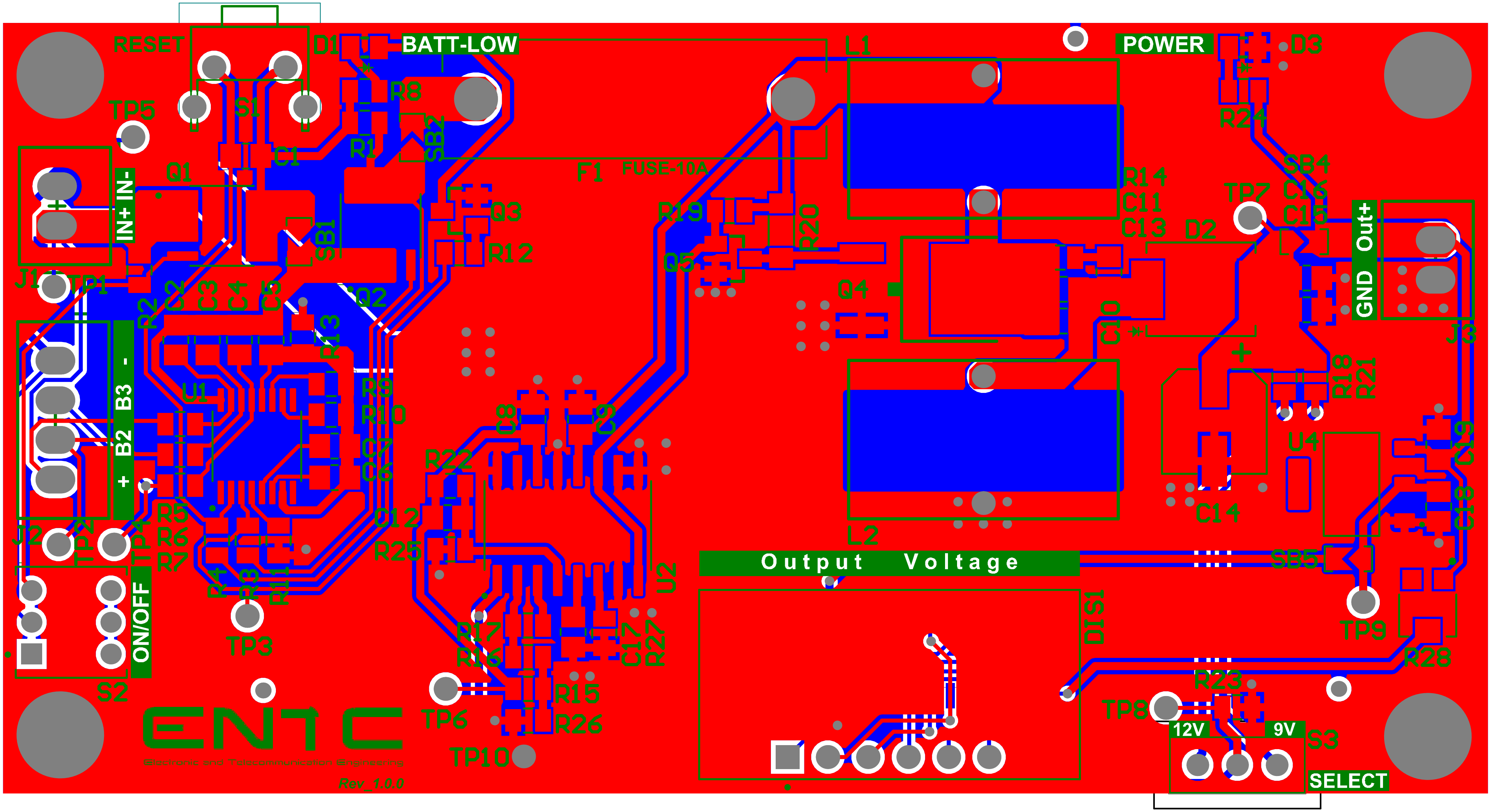
RESET
BATT-LOW
POWER
FUSE-10A
F1
L1
L2
IN+ IN-
J1
J2
J3
S1
S2
S3
- B3 B2 +
Out+ GND
ON/OFF
Output Voltage
DIS1
12V
9V
SELECT
U1
U2
U4
Q1
Q2
Q3
Q4
Q5
D1
D2
D3
SB1
SB2
SB4
SB5
TP1
TP2
TP3
TP4
TP5
TP6
TP7
TP8
TP9
TP10
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