

# Week 3 Task - Post-Synthesis GLS & STA Fundamentals

## Objective

To understand and perform **Gate-Level Simulation (GLS)** after synthesis, validate functionality, and get introduced to **Static Timing Analysis (STA)** concepts with practical experiments using OpenSTA.

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## Part 2 – Fundamentals of STA (Static Timing Analysis)

- If STA is new to you, first go through this free foundational course (**Free for next 4 days starting today**):  
[STA Fundamentals – Udemy](#)
- <https://www.udemy.com/course/vlsi-academy-sta-checks/?couponCode=F960AEDD365E0CD12546>
- Focus on:
  - Setup and Hold checks
  - Slack
  - Clock definitions
  - Path-based analysis

## Deliverable:

A one-page summary or key notes from the STA course (bullet points are fine).

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