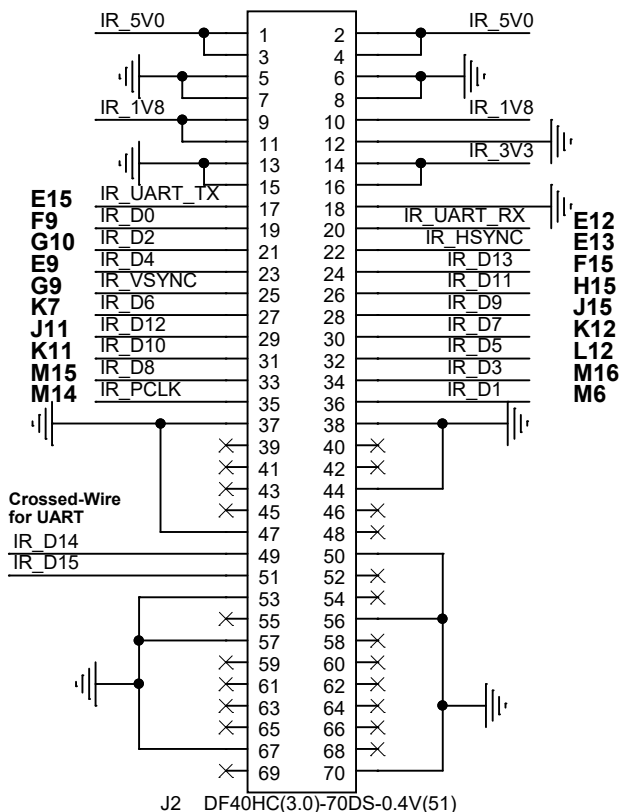


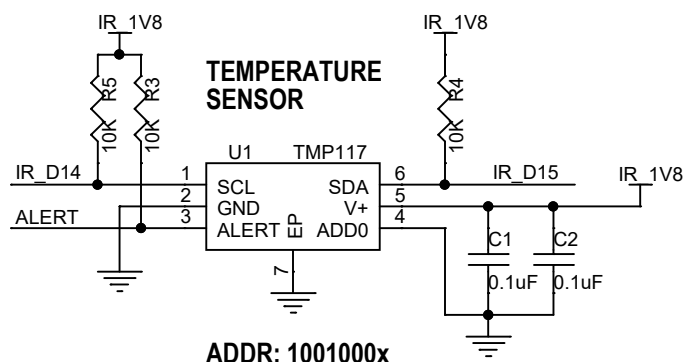
ATTENTION !!!  
PCLK MUST BE ROUTED TO FPGA CLK IN PIN  
FOR USING AS A CLOCK SIGNAL

## Yantai InfiRay Lite640x512 Infrared Image Sensor

### CDS3 1.8V IO



### TEMPERATURE SENSOR



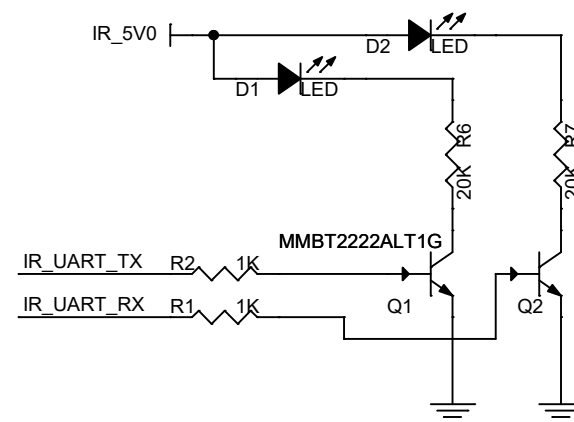
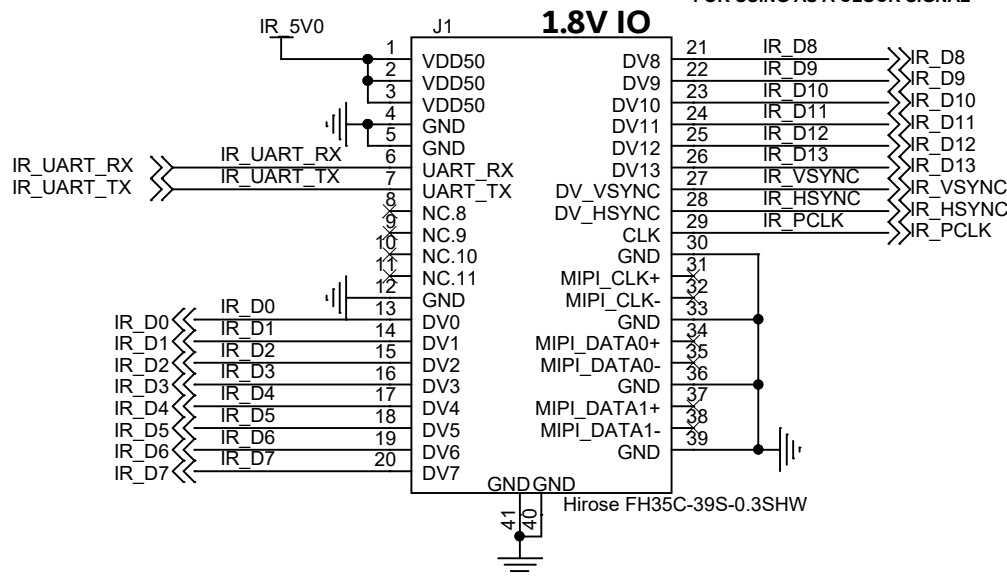
## Yantai InfiRay MicrolIII 256L Infrared Image Sensor

256\*192@14bits

CDS2 Interface 1.8V IO: PCLK, VSYNC, HSYNC, DATA[0:13] (Pixel + Temperature)

350mW

ATTENTION !!!  
PCLK MUST BE ROUTED TO FPGA CLK IN PIN  
FOR USING AS A CLOCK SIGNAL



Title  
Adapter for connecting IR-256x192(FPC-39) to IR-640x512(Hirose-70)

Size  
A Document Number  
<Doc>

Rev  
A1

Date: Thursday, January 15, 2026

Sheet 1 of 1