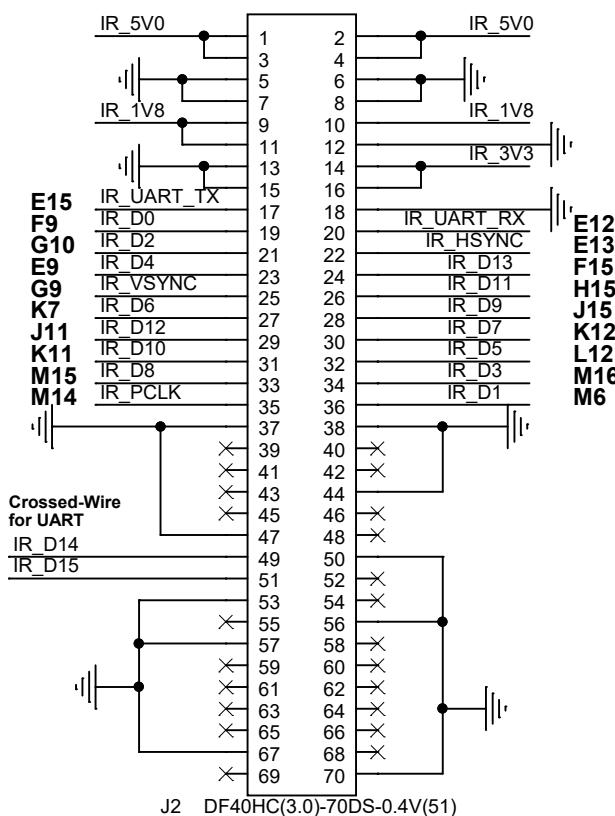


**ATTENTION !!!**  
PCLK MUST BE ROUTED TO FPGA CLK IN PIN  
FOR USING AS A CLOCK SIGNAL

### Yantai InfiRay Lite640x512 Infrared Image Sensor

#### CDS3 1.8V IO

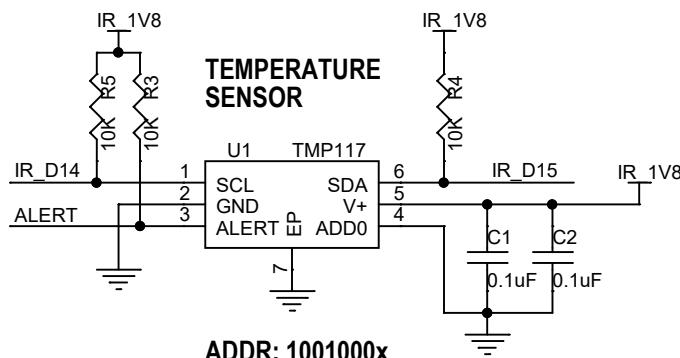
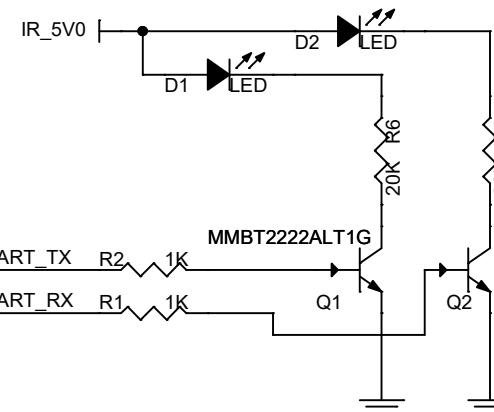
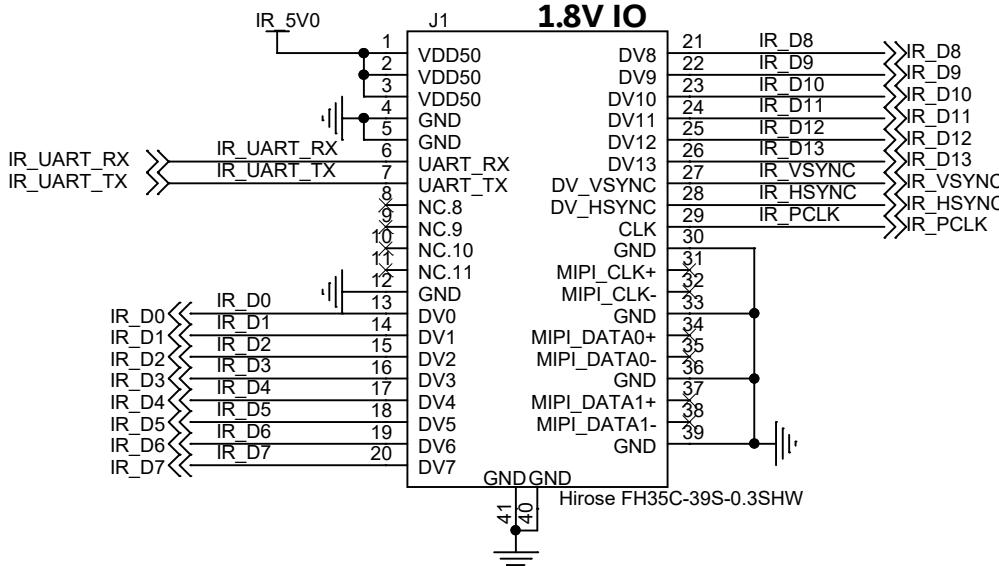


### Yantai InfiRay MicrollI 256L Infrared Image Sensor

256\*192@14bits  
CDS2 Interface 1.8V IO: PCLK, VSYNC, HSYNC, DATA[0:13] (Pixel + Temperature)  
350mW

**ATTENTION !!!**  
PCLK MUST BE ROUTED TO FPGA CLK IN PIN  
FOR USING AS A CLOCK SIGNAL

#### 1.8V IO



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Adapter for connecting IR-256x192(FPC-39) to IR-640x512(Hirose-70)		A1
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