

client service process

allocate buffer & structure

create threads & FIFO

```
create DMA download thread
```

```
create socket receive thread
```

```
create cycle FIFO
```

```

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async DMA download thread|

```

sync cycle FIFO

```

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async socket receive thread|

```

thread start

allocate buffer & structures

check exit flag ?

NO

Is device writable ?

is FIFO not empty?

YES




```
free buffer & structures
```

thread end

FIFO Control Structure

thread start

allocate buffer & structures

check exit flag ?



NO

Is socket readable ?

Page 10 of 10

free buffer & structures

thread end

is FIFO not full?

YES

