ChipSee-SOM335X(RevA1)
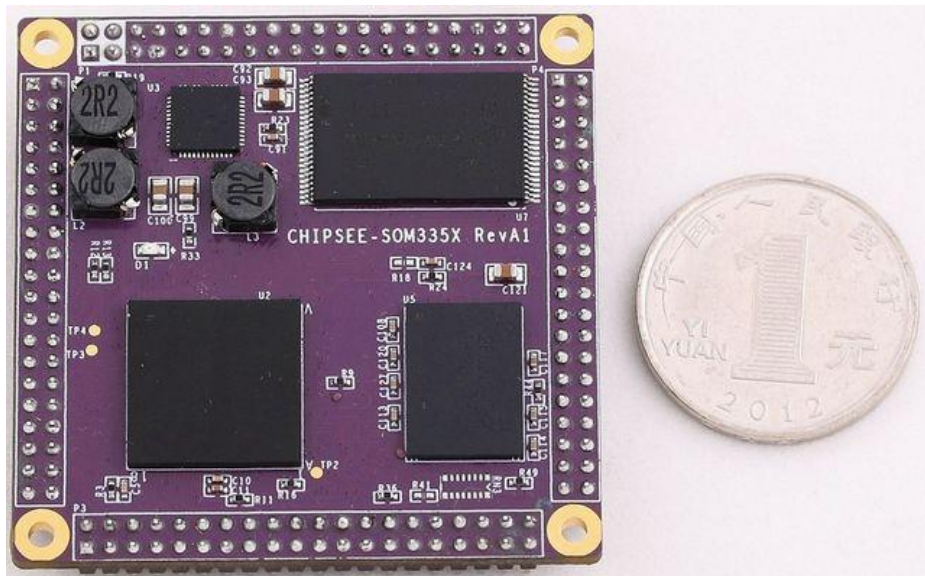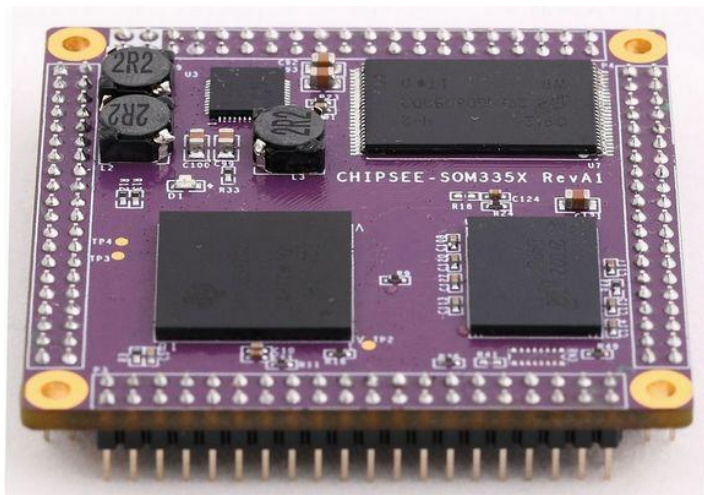
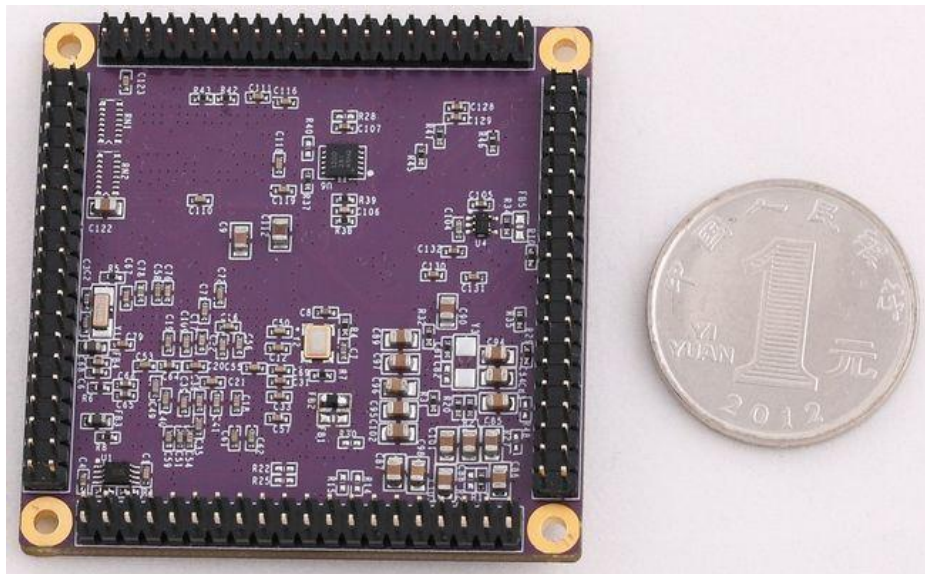**HardWare:**

Processor： AM3359，720MHz;

RAM：512MB DDR3，303MHz;

FLASH：NAND FLASH, 512MB;

PMIC：TPS65910;

Dimension：5.1*5.1*1.3cm;

Connector：2.0mm DIP connector，4*40。

ChipSee-EVM335X

ChipSee-EVM335X is a baseboard which designed to work with ChipSee-SOM335X.

HardWare:

LCD: 7 Inch,800*480Pixel

Touch: 4-Wire Resistive or 5 Point Capacitive Touch

USB HOST: 4   USB OTG: 1

 RS232: 1   uSD card : 1

Audio Out : 1   Audio In : 1(Mic In,Can changed to Line in by remove resistors)

 LAN : 1(100M),   WIFI:1   JTAG:1

Three- Aixal Accerlerometer   Digital Compass

SoftWare:

1) Android 4.1

2) Linux(TI DVSDK)

3) Windows Embedded Compact 7

Android 4.1

Linux(TI DVSDK)

ChipSee-SOM335X Pin Definition

Connector P1

PIN	Function	CPU PIN	PIN	Function	CPU PIN
1	VBAT	**Input**	2	VBAT	**Input**
3	VBACKUP	**Input**	4	GPMC_BEn1	U18
5	GPMC_A11	V17	6	GPMC_A10	T16
7	GPMC_A9	U16	8	GPMC_A8	V16
9	GPMC_A7	T15	10	GPMC_A6	U15
11	GPMC_A5	V15	12	GPMC_A4	R14
13	GPMC_A3	T14	14	GPMC_A2	U14
15	GPMC_A1	V14	16	GPMC_A0	R13
17	ECAP0_IN_PWM0_OUT	C18	18	EXP_PB_POWERON	
19	VMMC	**Output**	20	EXT_WAKEUP	C5
21	GPMC_CSn1	U9	22	PMIC_GPIO	
23	GPMC_CSn2	V9	24	GPMC_AD8	U10
25	GPMC_CSn3	T13	26	GPMC_AD15	U13
27	GPMC_AD10	T11	28	GPMC_AD14	V13
29	GPMC_CLK	V12	30	GPMC_AD11	U12
31	GPMC_AD12	T12	32	GPMC_AD9	T10
33	GPMC_AD13	R12	34	GPMC_ADVn_ALE	R7
35	GPMC_OEn_REn	T7	36	GPMC_BE0n_CLE	T6
37	GPMC_WAIT	T17	38	GPMC_WEn	U6
39	GPMC_CSn0	V6	40	GPMC_WPn	U17

Connector P2

PIN	Function	CPU PIN	PIN	Function	CPU PIN
1	GND		2	GND	
3	USB1_DRVVBUS	F15	4	USB1_VBUS	T18
5	USB0_DRVVBUS	F16	6	USB0_VBUS	P15
7	USB1_ID	P17	8	USB0_ID	P16
9	USB1_DM	R18	10	USB1_DP	R17
11	USB0_DM	N18	12	USB0_DP	N17
13	NMIn	B18	14	GMII1_RXD0	M16
15	GMII1_RXD1	L15	16	GMII1_TXD2	K15
17	GMII1_RXD2	L16	18	GMII1_MDIO_DATA	M17
19	GMII1_RXD3	L17	20	GMII1_MDIO_CLK	M18
21	GMII1_TXD1	K16	22	GMII1_TXD0	K17
23	GMII1_RXCLK	L18	24	GMII1_TXCLK	K18
25	GMII1_RXDV	J17	26	GMII1_TXD3	J18
27	GMII1_RXERR	J15	28	GMII1_TXEN	J16

29	GMII1_REFCLK	H18	30	GMII1_COL	H16
31	MMC0_CMD	G18	32	GMII1_CRS	H17
33	MMC0_CLK	G17	34	MMC0_D1	G15
35	MMC0_D2	F18	36	MMC0_D0	G16
37	MMC0_D3	F17	38	MMC0_SDCD	C15
39	UART0_RTSn	E17	40	UART0_CTSn	E18

Connector P3

PIN	Function	CPU PIN	PIN	Function	CPU PIN
1	UART0_TXD	E16	2	UART0_RXD	E15
3	I2C0_SDA	C17	4	UART1_CTSn	D18
5	I2C0_SCL	C16	6	UART1_RTSn	D17
7	SPI0_D0	B17	8	SPI0_SCLK	A17
9	SPI0_D1	B16	10	SPI0_CS0	A16
11	UART1_RXD	D16	12	UART1_TXD	D15
13	XDMA_EVENT_INTR0	A15	14	XDMA_EVENT_INTR1	D14
15	MCASP0_AHCLKX	A14	16	MCASP0_ACLKR	B12
17	MCASP0_ACLKX	A13	18	MCASP0_FSR	C13
19	MCASP0_AXR1	D13	20	MCASP0_FSX	B13
21	MCASP0_AHCLKR	C12	22	MCASP0_AXR0	D12
23	AIN0	B6	24	AIN1	C7
25	AIN2	B7	26	AIN3	A7
27	AIN4	C8	28	AIN5	B8
29	AIN6	A8	30	AIN7	C9
31	TCK	A12	32	TDI	B11
33	TDO	A11	34	TMS	C11
35	EMU0	C14	36	TRSTn	B10
37	EMU1	B14	38	WARMRSTn	A10
39	GND		40	GND	

Connector P4

PIN	Function	CPU PIN	PIN	Function	CPU PIN
1	VAUX33	**Output**	2	LCD_EN	R6
3	LCD_HSYNC	R5	4	LCD_VSYNC	U5
5	LCD_DATA3	R4	6	LCD_DATA4	T1
7	LCD_DATA1	R2	8	LCD_DATA2	R3
9	LCD_DATA10	U3	10	LCD_DATA0	R1
11	LCD_DATA8	U1	12	LCD_DATA9	U2
13	LCD_DATA6	T3	14	LCD_DATA7	T4
15	LCD_DATA15	T5	16	LCD_DATA5	T2
17	LCD_DATA13	V3	18	LCD_DATA14	V4
19	LCD_DATA11	U4	20	LCD_DATA12	V2

21	GND		22	LCD_PCLK	V5
23	GND		24	GND	
25	GND		26	GND	
27	GND		28	GND	
29	GND		30	GND	
31	GND		32	GND	
33	GND		34	GND	
35	GND		36	GND	
37	GND		38	GND	
39	GND		40	GND	

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