

3.10) RSB performs subtraction differently:

SUB C, A, B $\Rightarrow C = A - B$

$$\text{RSB } C, A, B \Rightarrow C = B - A$$

3.11) R3 = 000000000111000100000000
000001101

060001101

3.12) a. $R3 = 0000FFFF_{16}$

b. $R3 = 0000\ 0000\ 0F\&F16$

c. $R_3 = 0000 \ 0000 \ 0000 \ 0FFf_{16}$

d. R3 = 0000 0000 0000 0000 0FFF₁₆

3.13) a. RO = 0000 FF, b.

b. $RO = 0000\ 0000FF_{16}$

C. RD =

3.14) shift the value of R0 by 32 bits to the right, moving it to R1

[illegible]

B/C RD, RI