Sherry (Xiangyu) Wu

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EDUCATION

University of Illinois at Urbana-Champaign   May 2020
Bachelor of Science in Computer Engineering   GPA: 3.78/ 4.00
Minor: Business

- Dean's List (Fall 2016- Present), James Scholar (Spring 2016-Present), HKN (Fall 2017-Present)

COURSEWORK

Data Structures (C++)	Database System (SQL-In Porgress)	Artificial Intelligence (In Progress)
Reinforcement Learning (In Progress)	Digital Systems Laboratory (SystemVerilog)	Computer System Engineering
Digital Signal Processing	Linear Algebra	

PROJECTS & CONTESTS

X68 Kernel   Spring 2019

- Constructed a Kernel with customized operating system from scratch with C and x86 assembly
- Setup interrupt descriptor table and basic paging support
- Wrote I/O interface: file descriptors, buffering, control operations, memory mapping
- Supported multi-terminal, basic scheduling, system call interface and file system
- Implemented multiple device drivers: real time clock, programmable interval timer, PS/2 keyboard, and PC speaker

Pac-Man Arcade Game   Fall 2018

- Created a modified version of Pac-Man using FPGA
- Wrote game algorithm in SystemVerilog and C
- Adding sound effects to on-chip memory
- Design and test USB keyboard protocol and a standard VGA protocol using SystemVerilog and FPGA

PURE Research Program   Sept 2017- May 2018
Research Assistant

- Worked as research assistant advised by Ph.D. student Wei Yang
- Researched in adversarial Machine Learning field and performed adversarial attack on an open source plate recognition system

Deloitte Touche Tohmatsu CPA Co., Ltd. Beijing Branch   July 2016
Intern at Enterprise Risk System

- Translated and collected information; created PowerPoints to show companies' financial systems
- Conducted interview with HR from different companies to help create a framework for responding to critical enterprise risks

EXPERIENCES

Digital Systems Laboratory Undergrad Teaching Assistant   Champaign, IL
- Hold office hours and open lab for students   Spring 2019 - Present
- Attend weekly lab section to assist students regarding questions for TTL, FPGA, and SystemVerilog

Techniques for Engineering Decision Course Staff   Champaign, IL
- Grading Homework and Exams   Spring 2018 - Present

SKILLS

- Advanced: C++, C, SytsemVerilog, x86 Assembly, Linux environment, SQL
- Intermediate: Python, MatLab, html, css, Javascript
- Fluent in Cantonese, Mandarin