



# Vidyavardhini's College of Engineering and Technology

## Department of Artificial Intelligence & Data Science

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<b>Class/Sem:</b>	SE/IV
<b>Experiment No.:</b>	9
<b>Title:</b>	Develop a program to implement dynamic partitioning placement algorithms.
<b>Date of Performance:</b>	
<b>Date of Submission:</b>	
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### Experiment No.: 9

**Aim:** Memory Management: Virtual Memory

**Objective:**

To study and implement page replacement policy FIFO, LRU, OPTIMAL

**Theory:**

**Demand Paging**

A demand paging system is quite similar to a paging system with swapping where processes reside in secondary memory and pages are loaded only on demand, not in advance. When a context switch occurs, the operating system does not copy any of the old program's pages out to the disk or any of the new program's pages into the main memory. Instead, it just begins executing the new program after loading the first page and fetches that program's pages as they are referenced.

**Page Replacement Algorithm**

Page replacement algorithms are the techniques using which an Operating System decides which memory pages to swap out, write to disk when a page of memory needs to be allocated.

**Reference String**

The string of memory references is called reference string. Reference strings are generated artificially or by tracing a given system and recording the address of each memory reference.

**First In First Out (FIFO)**

This is the simplest page replacement algorithm. In this algorithm, the OS maintains a queue that keeps track of all the pages in memory, with the oldest page at the front and the most recent page at the back.

When there is a need for page replacement, the FIFO algorithm, swaps out the page at the front of the queue, that is the page which has been in the memory for the longest time.

**Least Recently Used (LRU)**

Least Recently Used page replacement algorithm keeps track of page usage over a short period of time. It works on the idea that the pages that have been most heavily used in the past are most likely to be used heavily in the future too.

In LRU, whenever page replacement happens, the page which has not been used for the longest amount of time is replaced.



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### Optimal Page Replacement

Optimal Page Replacement algorithm is the best page replacement algorithm as it gives the least number of page faults. It is also known as OPT, clairvoyant replacement algorithm, or Belady's optimal page replacement policy.

In this algorithm, pages are replaced which would not be used for the longest duration of time in the future, i.e., the pages in the memory which are going to be referred farthest in the future are replaced.

#### Program:

// First - Fit algorithm

```
#include<stdio.h>
```

```
void firstFit(int blockSize[], int m, int processSize[], int n)
```

```
{
    int i, j;
    int allocation[n];
    for(i = 0; i < n; i++)
    {
        allocation[i] = -1;
    }

    for (i = 0; i < n; i++)
    {
        for (j = 0; j < m; j++)
        {
            if (blockSize[j] >= processSize[i])
            {
                allocation[i] = j;
                blockSize[j] -= processSize[i];

                break;
            }
        }
    }

    printf("\nProcess No.\tProcess Size\tBlock no.\n");
    for (int i = 0; i < n; i++)
    {
        printf(" %i\t\t\t", i+1);
        printf("%i\t\t\t\t", processSize[i]);
        if (allocation[i] != -1)
            printf("%i", allocation[i] + 1);
        else
            printf("Not Allocated");
        printf("\n");
    }
}
```



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```
}  
  
int main()  
{  
    int m;  
    int n;  
    int blockSize[] = {100, 500, 200, 300, 600};  
    int processSize[] = {212, 417, 112, 426};  
    m = sizeof(blockSize) / sizeof(blockSize[0]);  
    n = sizeof(processSize) / sizeof(processSize[0]);  
  
    firstFit(blockSize, m, processSize, n);  
  
    return 0 ;  
}
```

**Output:**

### Output

/tmp/x9PuTciIz2.o

Process No.	Process Size	Block no.
1	212	2
2	417	5
3	112	2
4	426	Not Allocated

=== Code Execution Successful ===



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### **Conclusion:**

Why do we need page replacement strategies ?

Page replacement strategies are essential in virtual memory management systems to efficiently utilize limited physical memory resources while providing the illusion of a larger memory space to user applications. The primary reason for employing page replacement strategies stems from the concept of virtual memory, which allows the operating system to use a portion of the disk as an extension of physical memory. However, physical memory is finite, and there are often more processes and data than can fit into it simultaneously.

Page replacement strategies come into play when a new page needs to be loaded into physical memory but there is no free space available. In such cases, the operating system must decide which page currently residing in physical memory to evict or replace with the new page. This decision impacts system performance, as it affects factors such as access time, efficiency of memory usage, and overall system throughput.

Effective page replacement strategies aim to minimize the number of page faults, which occur when a requested page is not found in physical memory and must be retrieved from secondary storage. By selecting the most appropriate page to replace based on certain criteria, such as frequency of use or time since last access, these strategies strive to optimize memory usage and maintain a balance between different processes' memory demands.

Additionally, page replacement strategies play a crucial role in supporting multitasking environments, where multiple processes compete for limited physical memory resources. Without efficient page replacement mechanisms, system performance could degrade significantly due to excessive paging activity, leading to increased response times, thrashing, and overall degradation of system performance.

In summary, page replacement strategies are indispensable components of virtual memory management systems, enabling efficient utilization of physical memory resources, supporting multitasking environments, and maintaining acceptable system performance levels. They are vital for achieving the delicate balance between maximizing memory usage and minimizing the overhead associated with managing memory.



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