

1

2

3

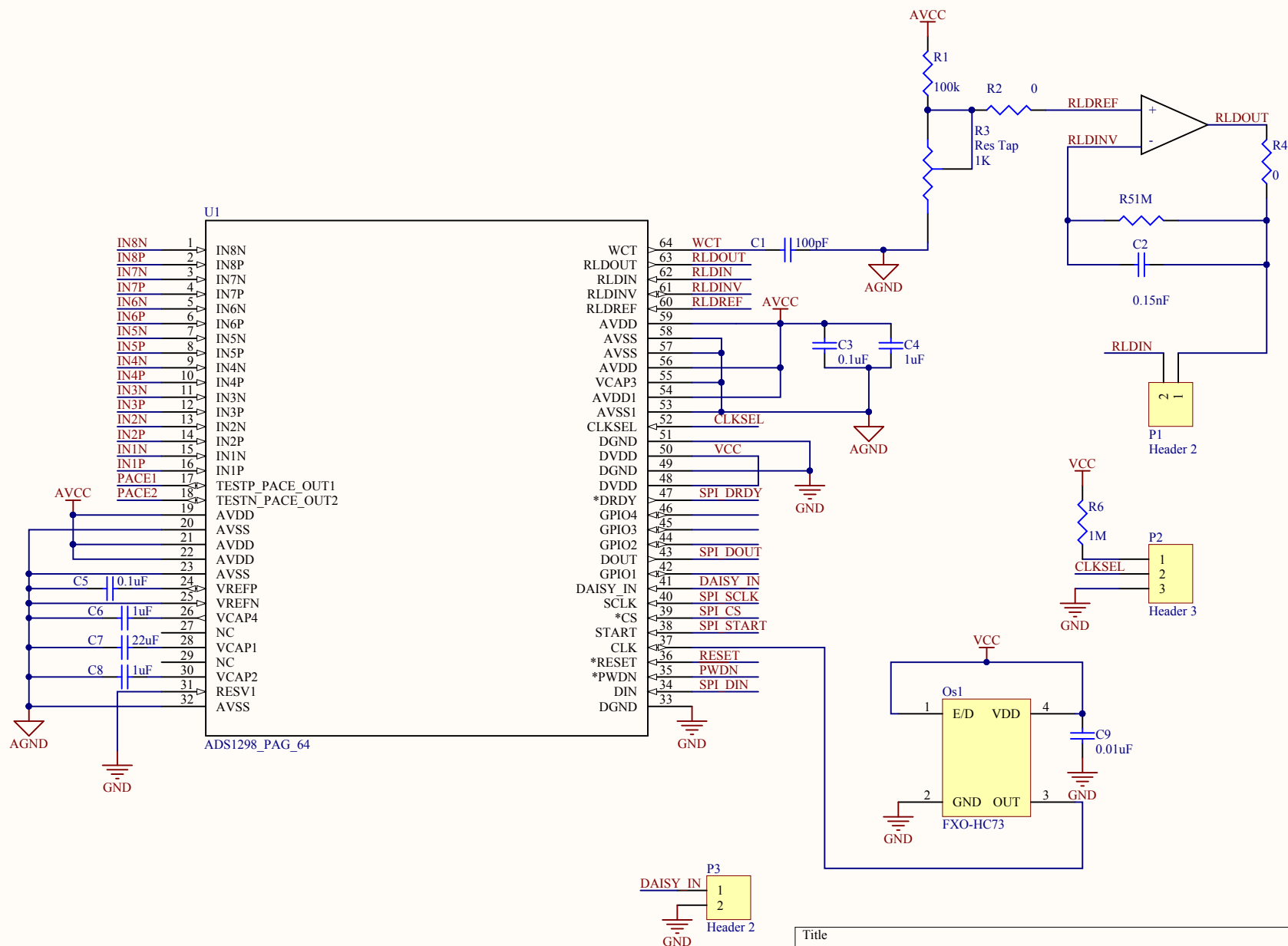
4

A

B

C

D



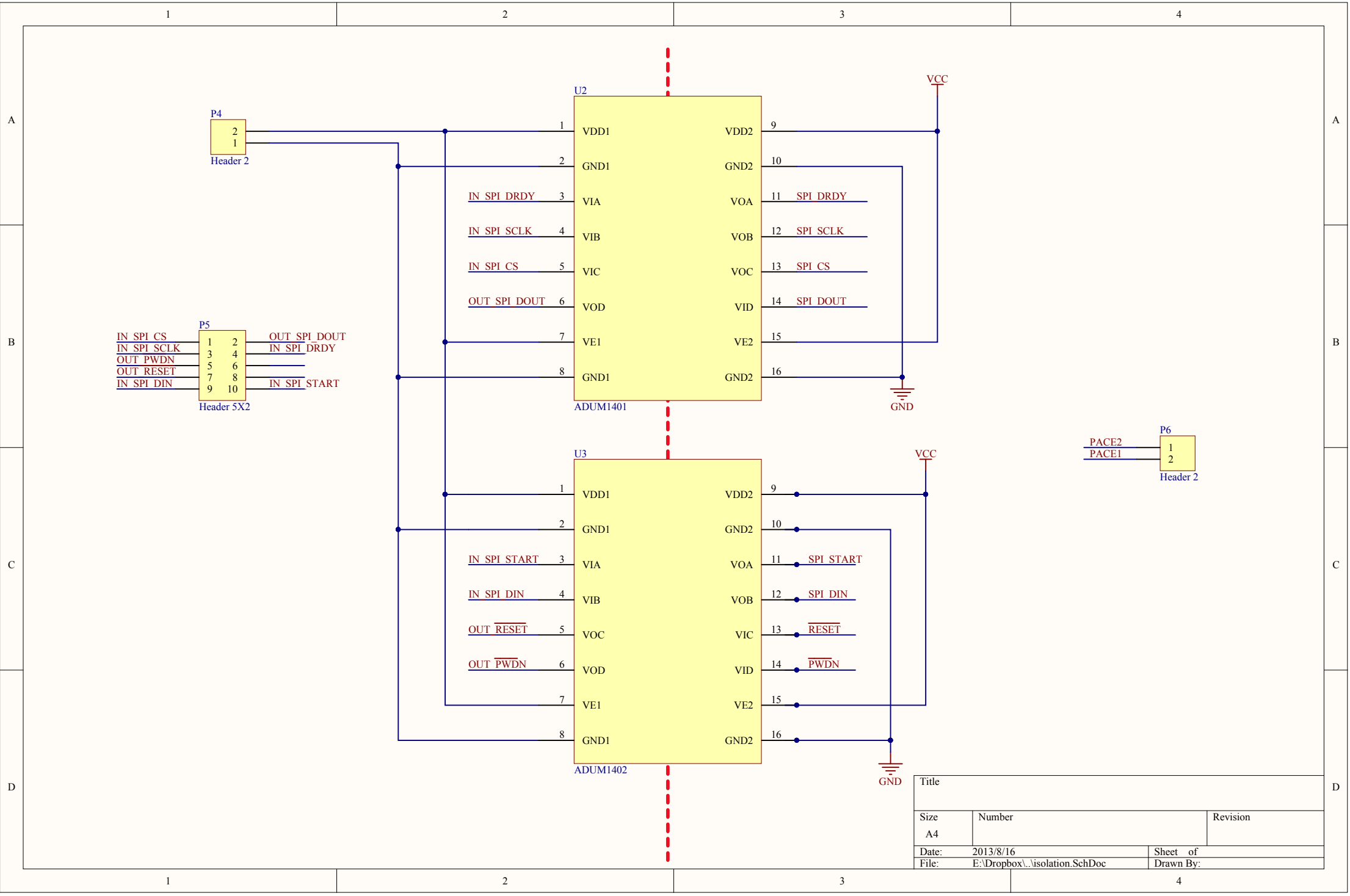
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A4		
Date:	2013/8/16	Sheet of
File:	E:\Dropbox\...\ADC.SchDoc	Drawn By:

1

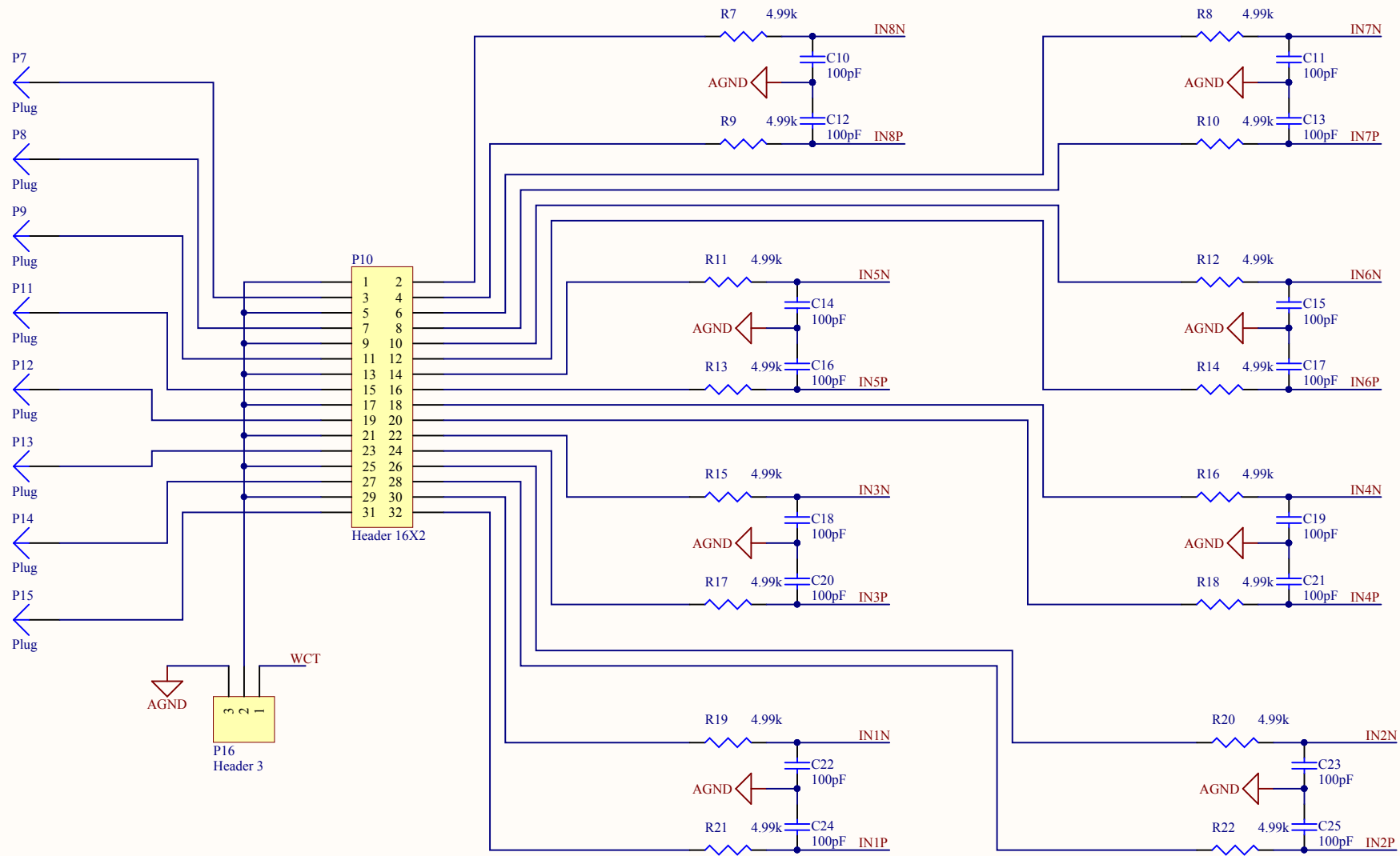
2

3

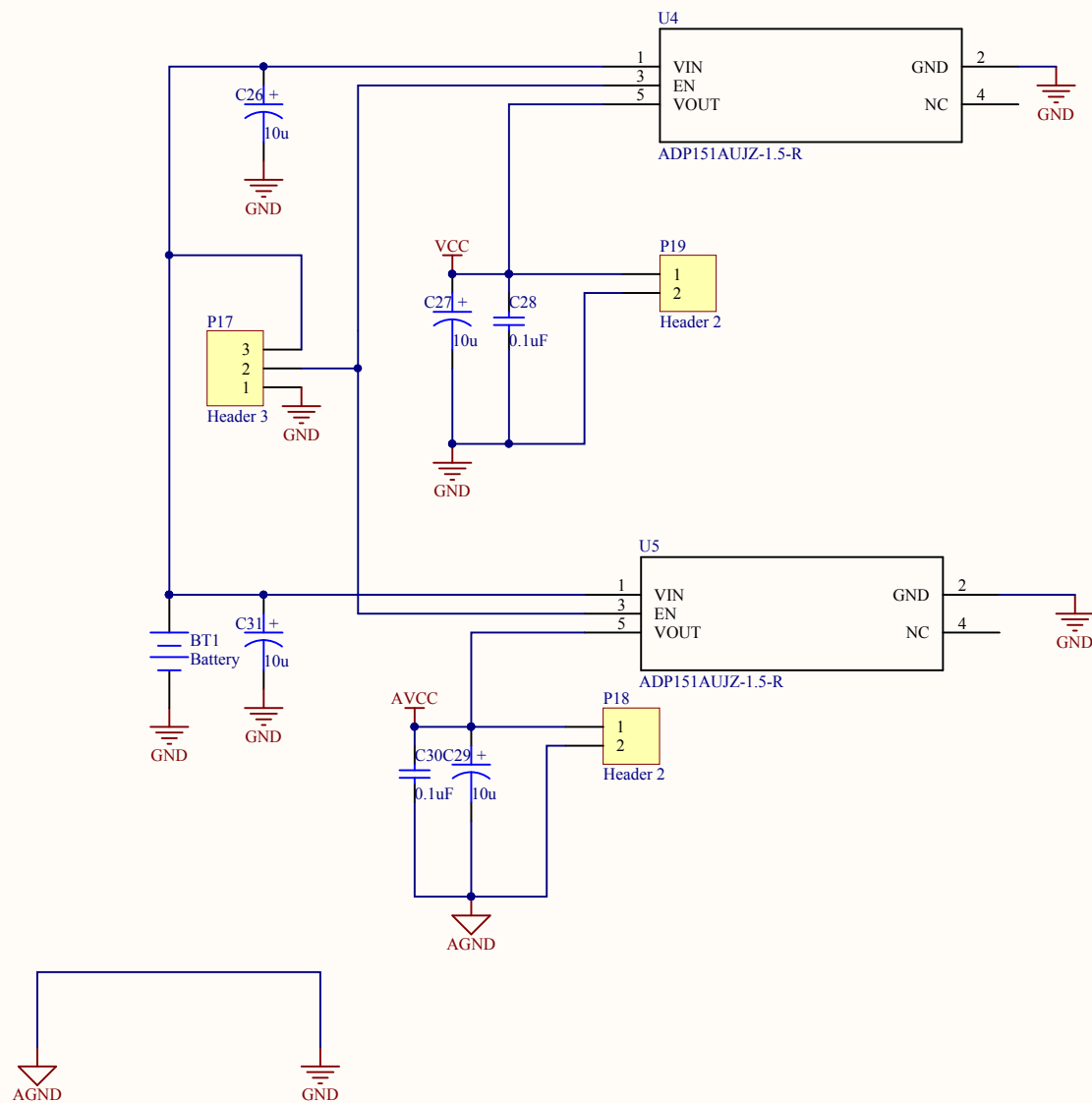
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Title		
Size	Number	Revision
A4		
Date:	2013/8/16	Sheet of
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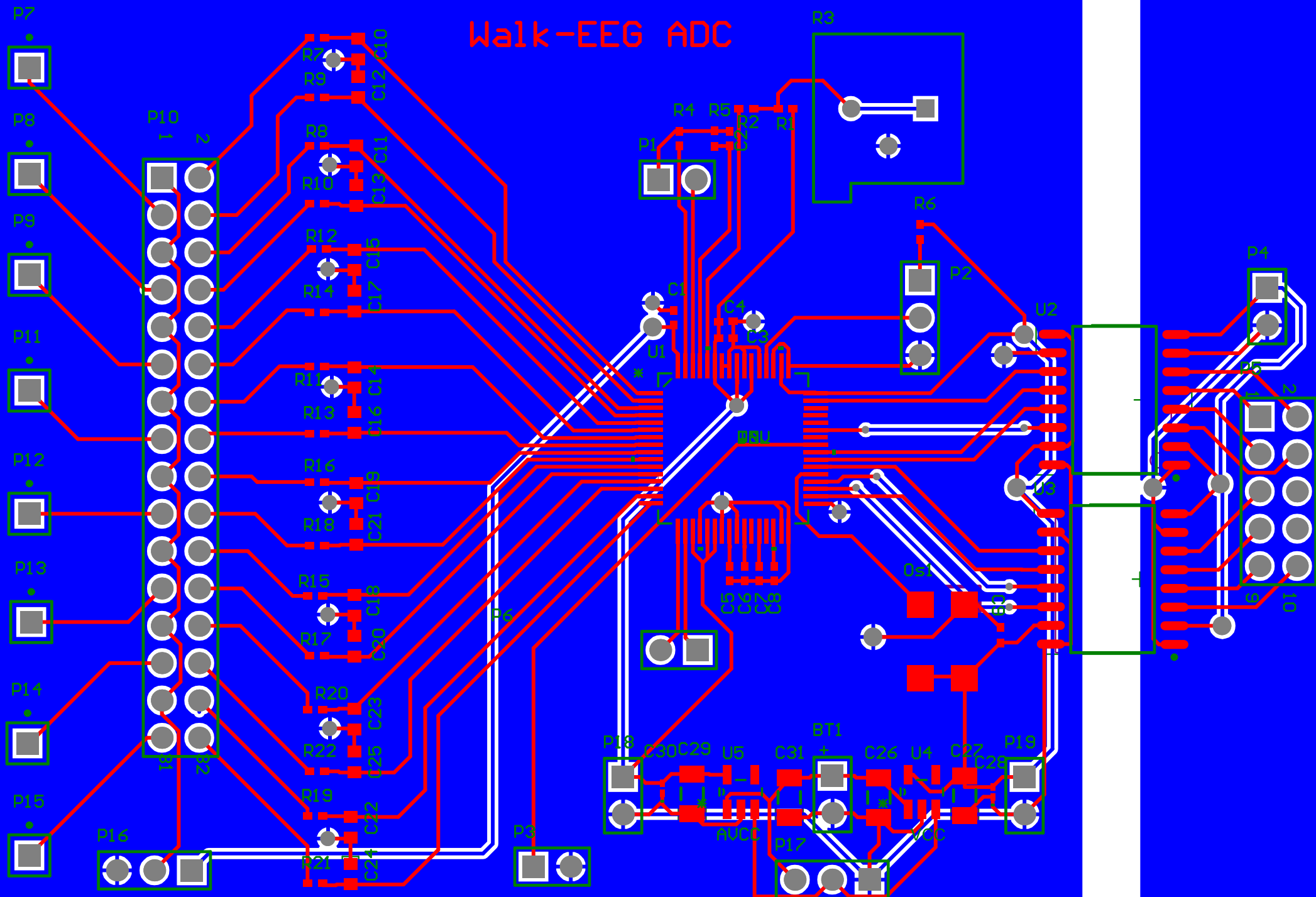


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Size	Number	Revision
A4		
Date:	2013/8/16	Sheet of
File:	E:\Dropbox\...\On board front end.SchDoc	
	Drawn By:	



Title		
Size	Number	Revision
A4		
Date:	2013/8/16	Sheet of
File:	E:\Dropbox\...\PW.SchDoc	Drawn By:

# Walk-EEG ADC



# Bill of Materials

<Parameter Title not found>

Source Data From: walKEEG\_ADC.PrjPCB  
 Project: walKEEG\_ADC.PrjPCB  
 Variant: None

Creation Date: 2013/8/16 21:28:41  
 Print Date: 41502 41502.89497

Footprint	Comment	LibRef	Designator	Description	Quantity
BAT-2	Battery	Battery	BT1	Multicell Battery	1
0402	Cap	Cap	C1, C3, C4, C5, C6, C7, C8, C9	Capacitor	8
0402		Cap Semi, Res3	C2, R1, R2, R4, R5, R6, R7, R8, R9, R10, R11, R12, R13, R14, R15, R16, R17, R18, R19, R20, R21, R22	Capacitor (Semiconductor SIM Model), Resistor	22
1608[0603]	Cap	Cap	C10, C11, C12, C13, C14, C15, C16, C17, C18, C19, C20, C21, C22, C23, C24, C25	Capacitor	16
C1206	10u	CAPACITOR POL	C26, C27, C29, C31	Capacitor	4
0603	Cap	Cap	C28, C30	Capacitor	2
PCBComponent_1	FXO-HC73	FXO-HC73	Os1		1
HDR1X2	Header 2	Header 2	P1, P3, P4, P6, P18, P19	Header, 2-Pin	6
HDR1X3	Header 3	Header 3	P2, P16, P17	Header, 3-Pin	3
HDR2X5	Header 5X2	Header 5X2	P5	Header, 5-Pin, Dual row	1
PIN1	Plug	Plug	P7, P8, P9, P11, P12, P13, P14, P15	Plug	8
HDR2X16	Header 16X2	Header 16X2	P10	Header, 16-Pin, Dual row	1
VR4	Res Tap	Res Tap	R3	Tapped Resistor	1
PAG64	ADS1298_PAG_64	ADS1298_PAG_64	U1	Imported	1
SOIC_W	ADUM1401	ADUM1401	U2	ADI isolation	1
SOIC_W	ADUM1402	ADUM1402	U3		1
UJ_5	ADP151AUJZ-1.5-R	ADP151AUJZ-1.5-R	U4, U5	Imported	2
					79

Approved	Notes