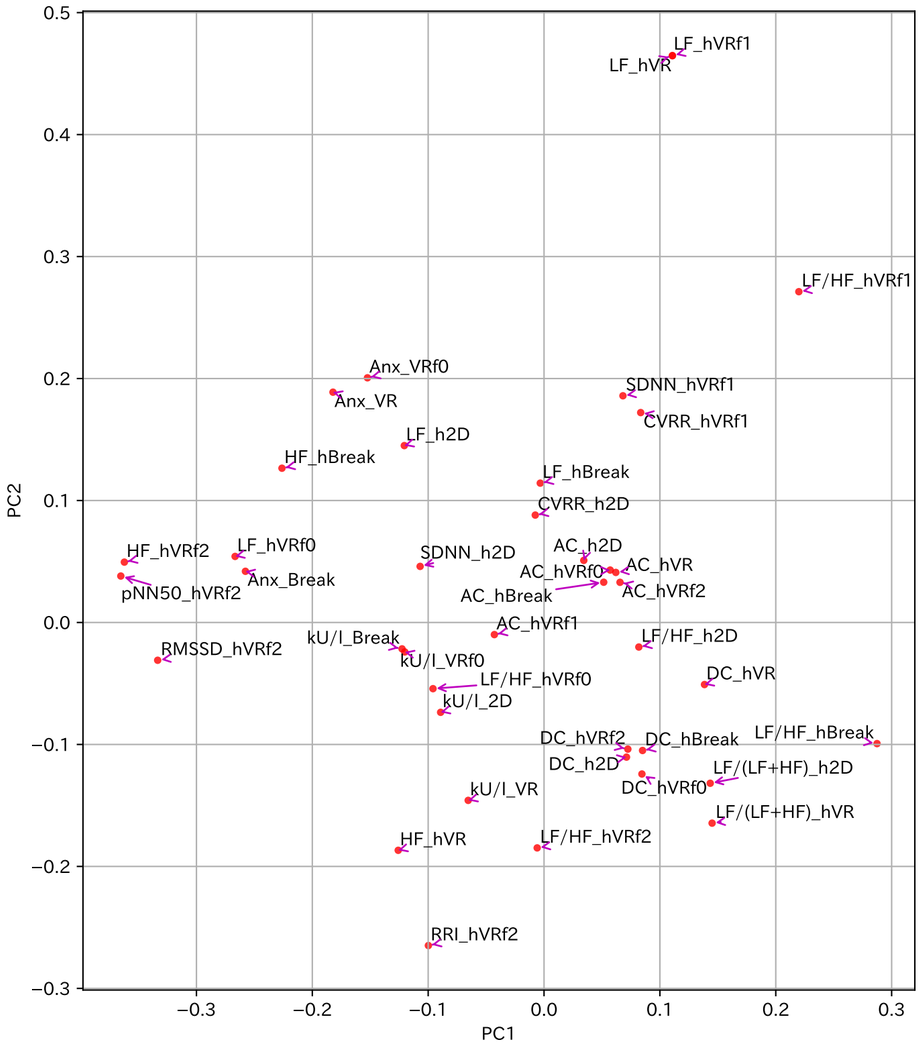
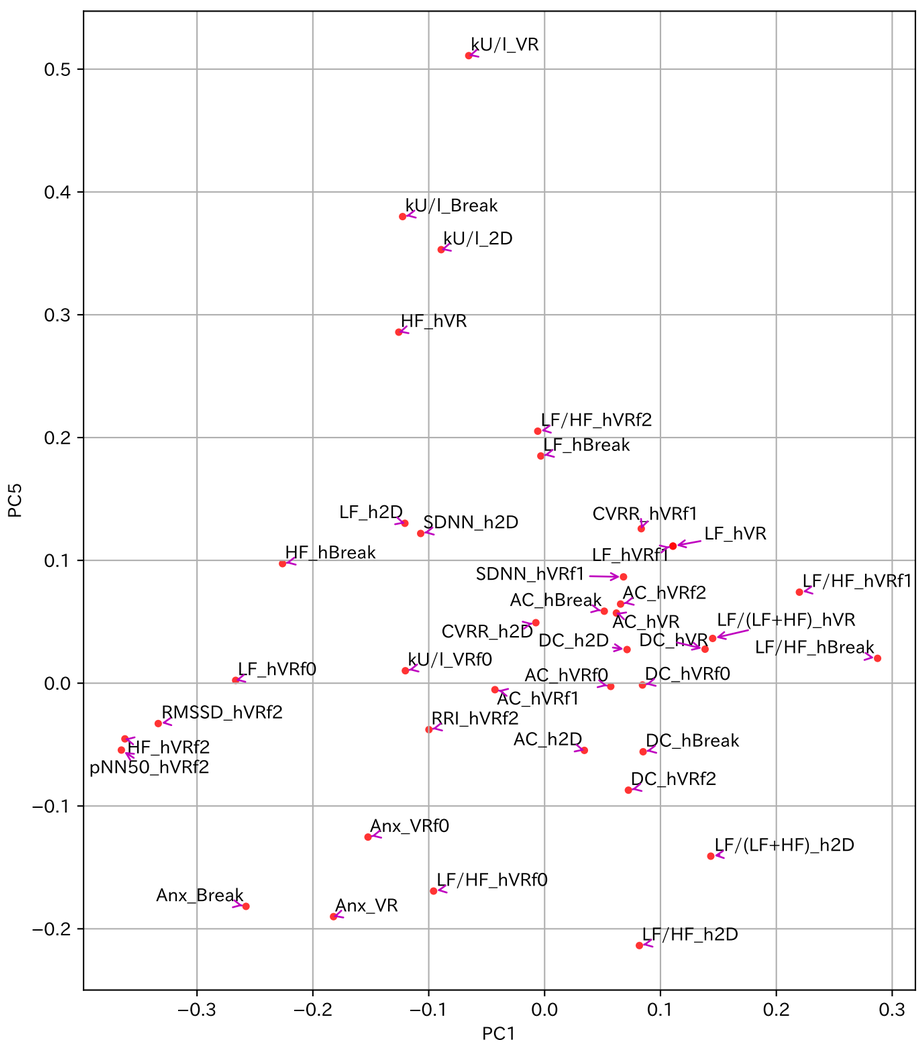
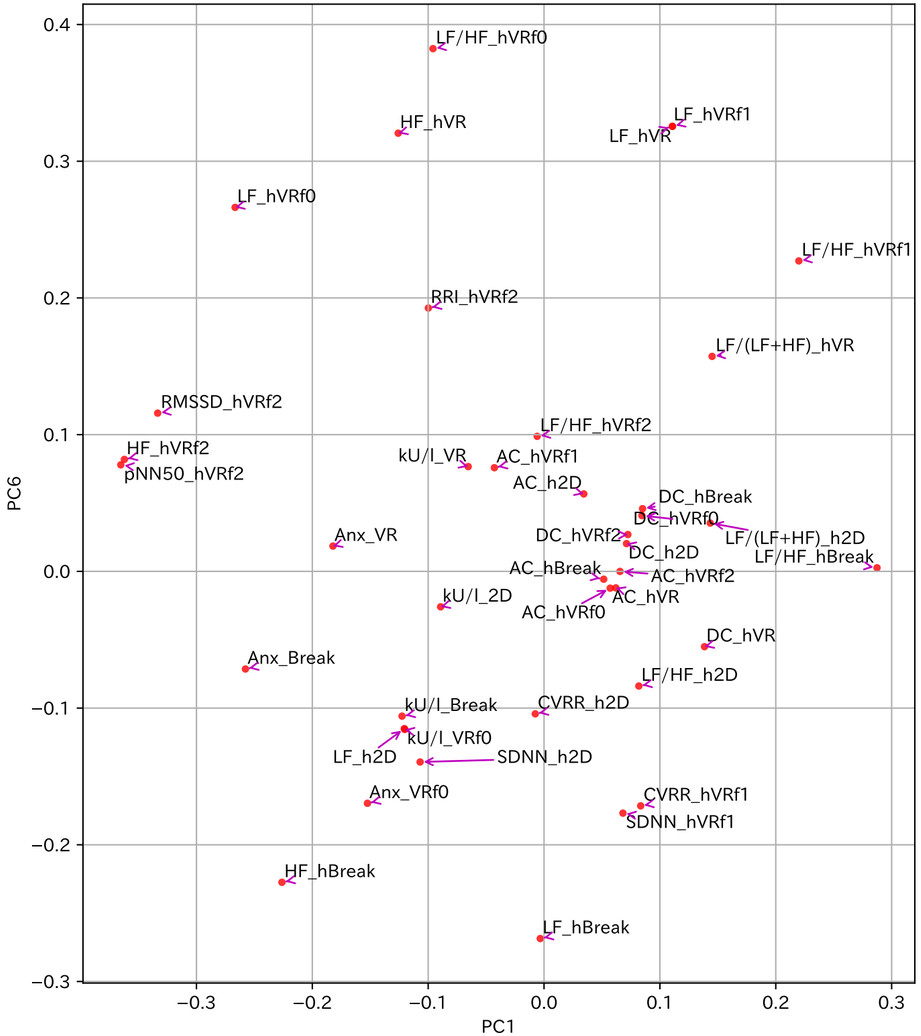
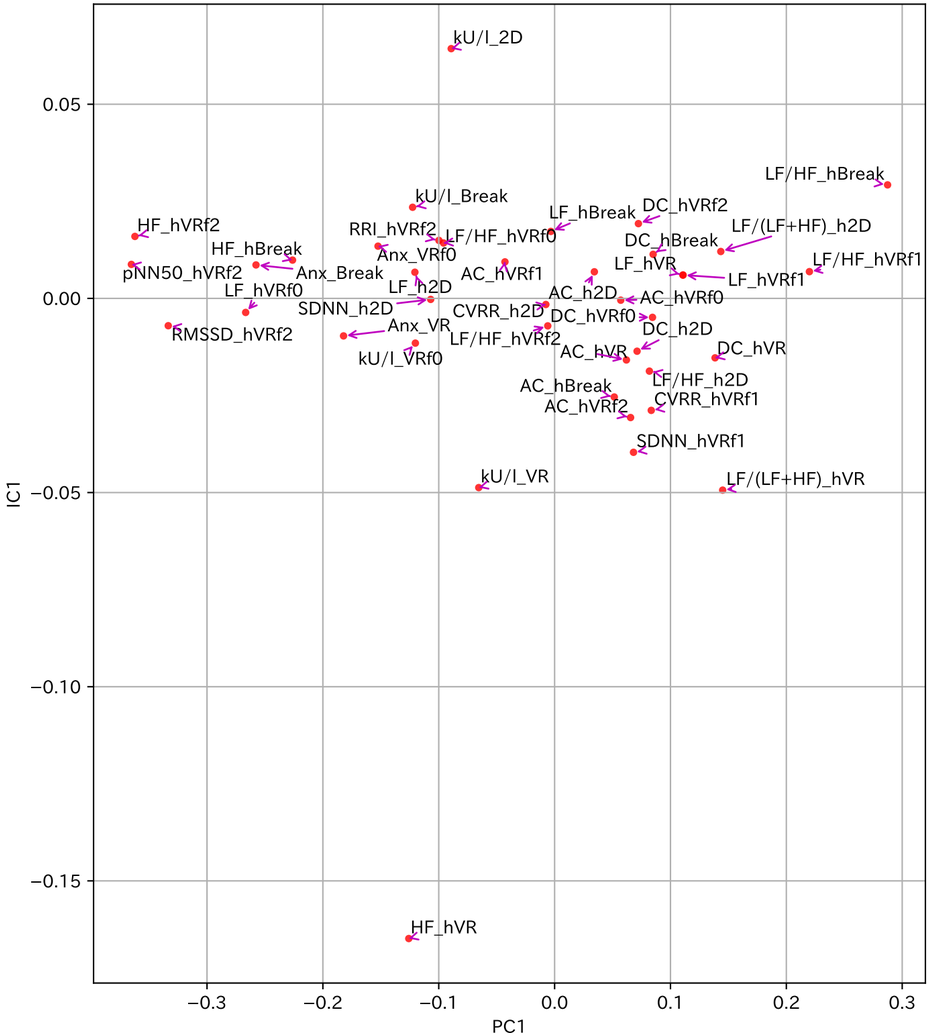
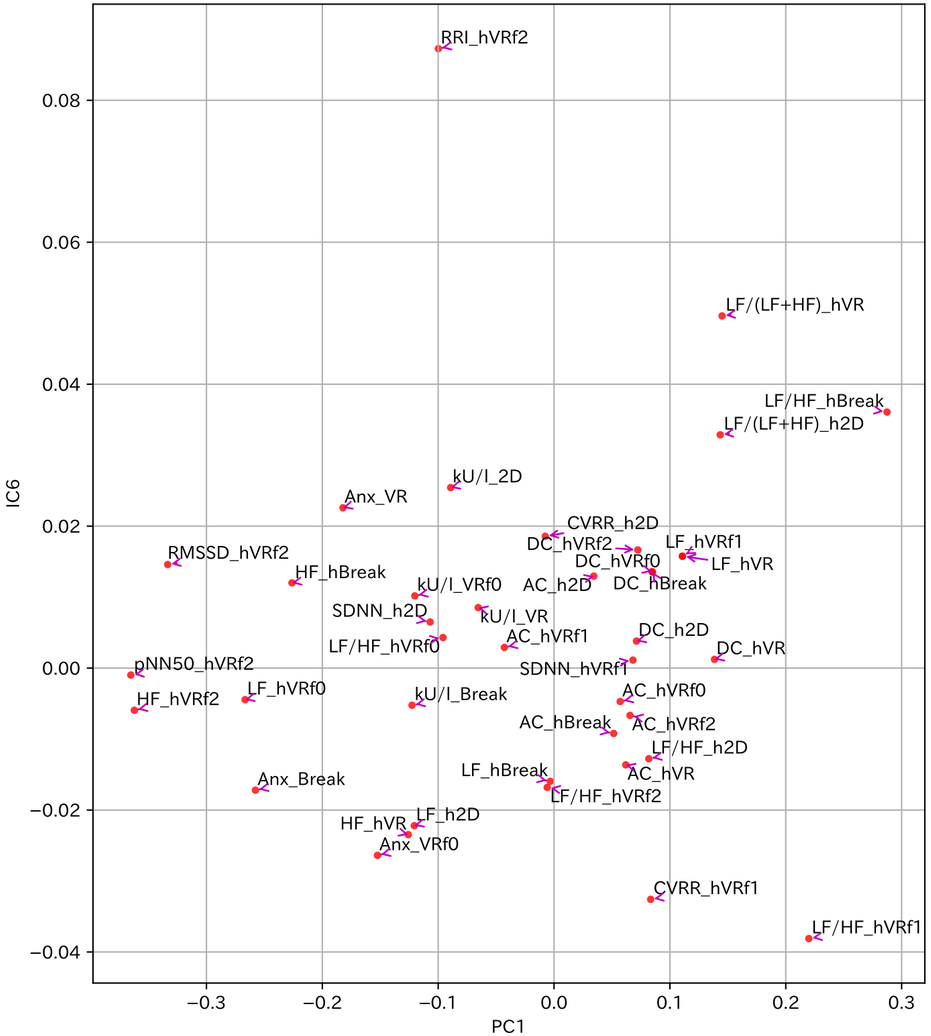
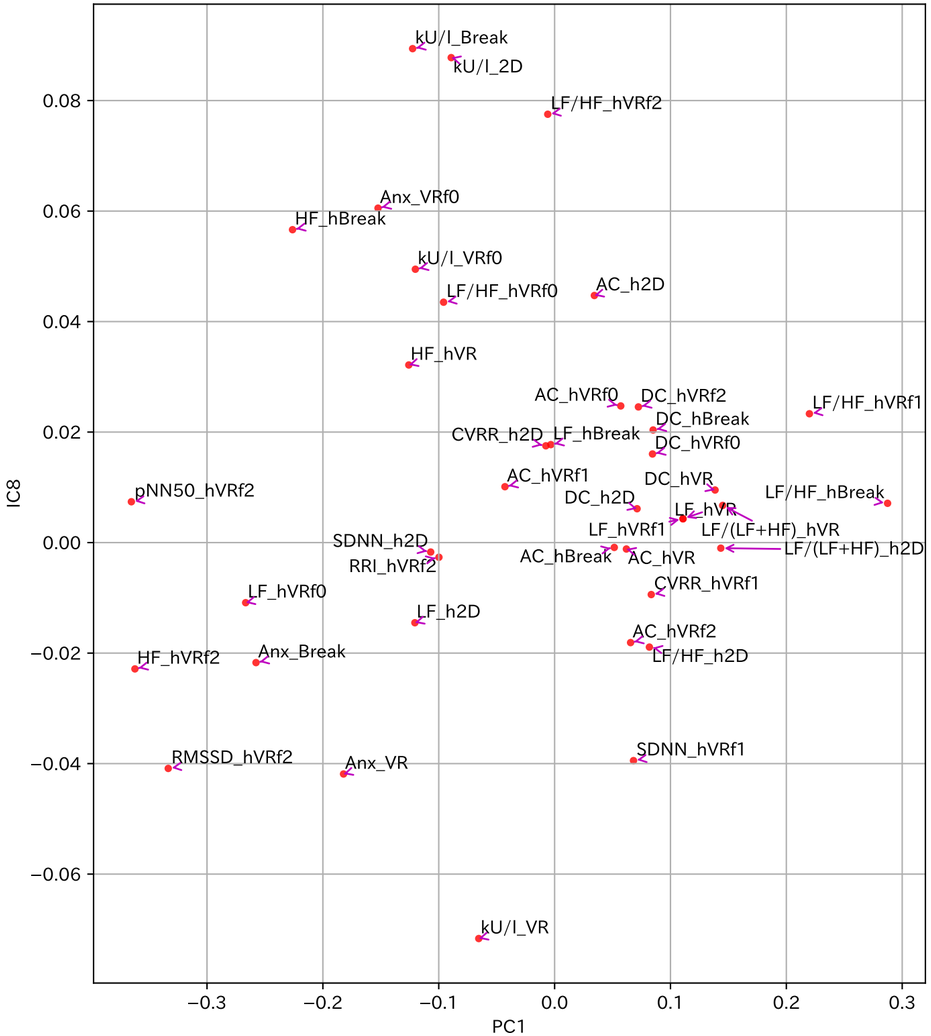

PC1
PC2
LF_hVRf1
LF_hVR
LF/HF_hVRf1
Anx_VRf0
Anx_VR
SDNN_hVRf1
CVRR_hVRf1
LF_h2D
HF_hBreak
LF_hBreak
CVRR_h2D
HF_hVRf2
LF_hVRf0
SDNN_h2D
AC_h2D
AC_hVRf0
AC_hVR
Anx_Break
pNN50_hVRf2
AC_hBreak
AC_hVRf2
AC_hVRf1
kU/I_Break
LF/HF_h2D
RMSSD_hVRf2
kU/I_VRf0
LF/HF_hVRf0
DC_hVR
kU/I_2D
DC_hVRf2
DC_hBreak
LF/HF_hBreak
DC_h2D
LF/(LF+HF)_h2D
kU/I_VR
DC_hVRf0
LF/(LF+HF)_hVR
HF_hVR
LF/HF_hVRf2
RRI_hVRf2


PC1
PC5
kU/I_VR
kU/I_Break
kU/I_2D
HF_hVR
LF/HF_hVRf2
LF_hBreak
LF_h2D
SDNN_h2D
CVRR_hVRf1
LF_hVR
HF_hBreak
LF_hVRf1
SDNN_hVRf1
LF/HF_hVRf1
AC_hVRf2
AC_hBreak
AC_hVR
LF/(LF+HF)_hVR
CVRR_h2D
DC_h2D
DC_hVR
LF/HF_hBreak
LF_hVRf0
kU/I_VRf0
AC_hVRf0
DC_hVRf0
AC_hVRf1
RMSSD_hVRf2
RRI_hVRf2
HF_hVRf2
AC_h2D
DC_hBreak
pNN50_hVRf2
DC_hVRf2
Anx_VRf0
LF/(LF+HF)_h2D
LF/HF_hVRf0
Anx_Break
Anx_VR
LF/HF_h2D


LF/HF_hVRf0
HF_hVR
LF_hVRf1
LF_hVR
LF_hVRf0
LF/HF_hVRf1
RRI_hVRf2
LF/(LF+HF)_hVR
RMSSD_hVRf2
LF/HF_hVRf2
HF_hVRf2
pNN50_hVRf2
kU/I_VR
AC_hVRf1
AC_h2D
DC_hBreak
DC_hVRf0
Anx_VR
DC_hVRf2
LF/(LF+HF)_h2D
DC_h2D
LF/HF_hBreak
AC_hBreak
AC_hVRf2
kU/I_2D
AC_hVR
AC_hVRf0
DC_hVR
Anx_Break
LF/HF_h2D
kU/I_Break
CVRR_h2D
kU/I_VRf0
LF_h2D
SDNN_h2D
Anx_VRf0
CVRR_hVRf1
SDNN_hVRf1
HF_hBreak
LF_hBreak
PC6
PC1

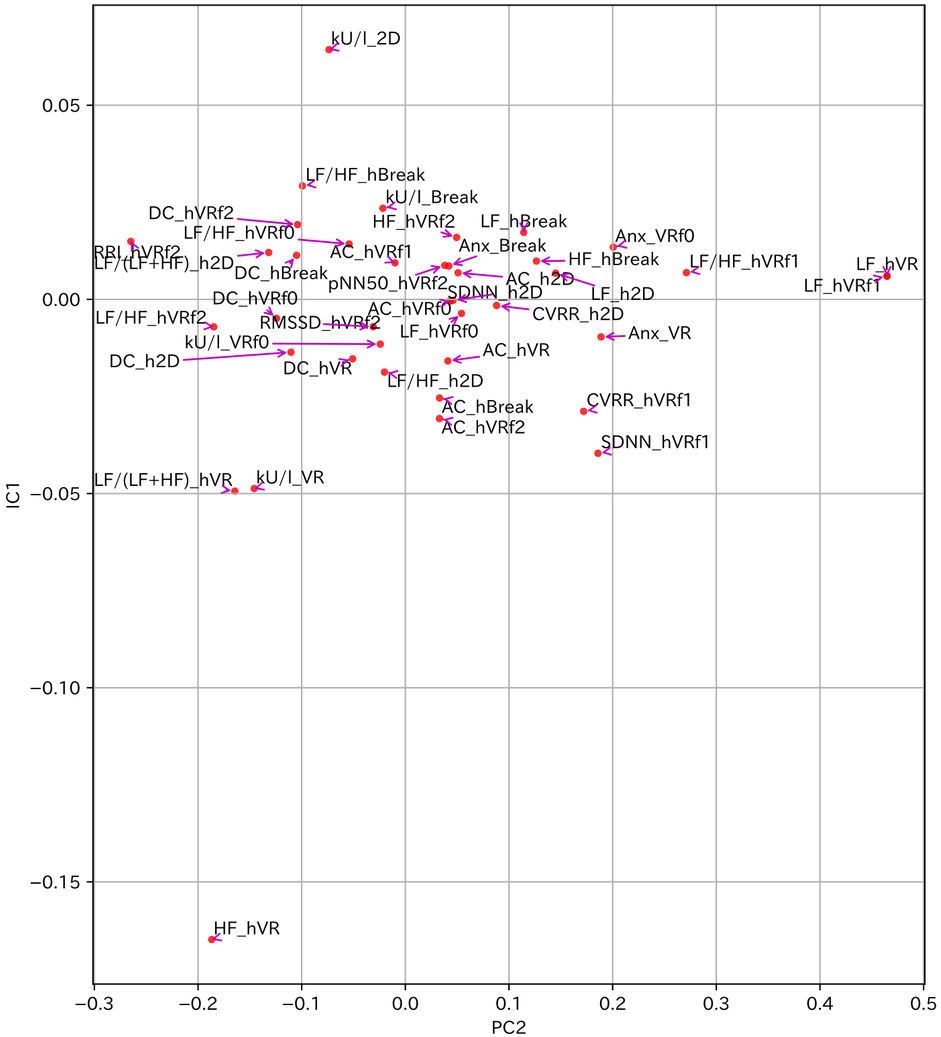

kU/I_2D
LF/HF_hBreak
kU/I_Break
LF_hBreak
DC_hVRf2
HF_hVRf2
RRI_hVRf2
LF/HF_hVRf0
LF/(LF+HF)_h2D
DC_hBreak
HF_hBreak
Anx_VRf0
LF_hVR
LF/HF_hVRf1
pNN50_hVRf2
Anx_Break
AC_hVRf1
LF_hVRf1
LF_hVRf0
LF_h2D
AC_h2D
AC_hVRf0
SDNN_h2D
CVRR_h2D
DC_hVRf0
Anx_VR
DC_h2D
RMSSD_hVRf2
LF/HF_hVRf2
AC_hVR
DC_hVR
kU/I_VRf0
LF/HF_h2D
AC_hBreak
CVRR_hVRf1
AC_hVRf2
SDNN_hVRf1
kU/I_VR
LF/(LF+HF)_hVR
HF_hVR
IC1
PC1

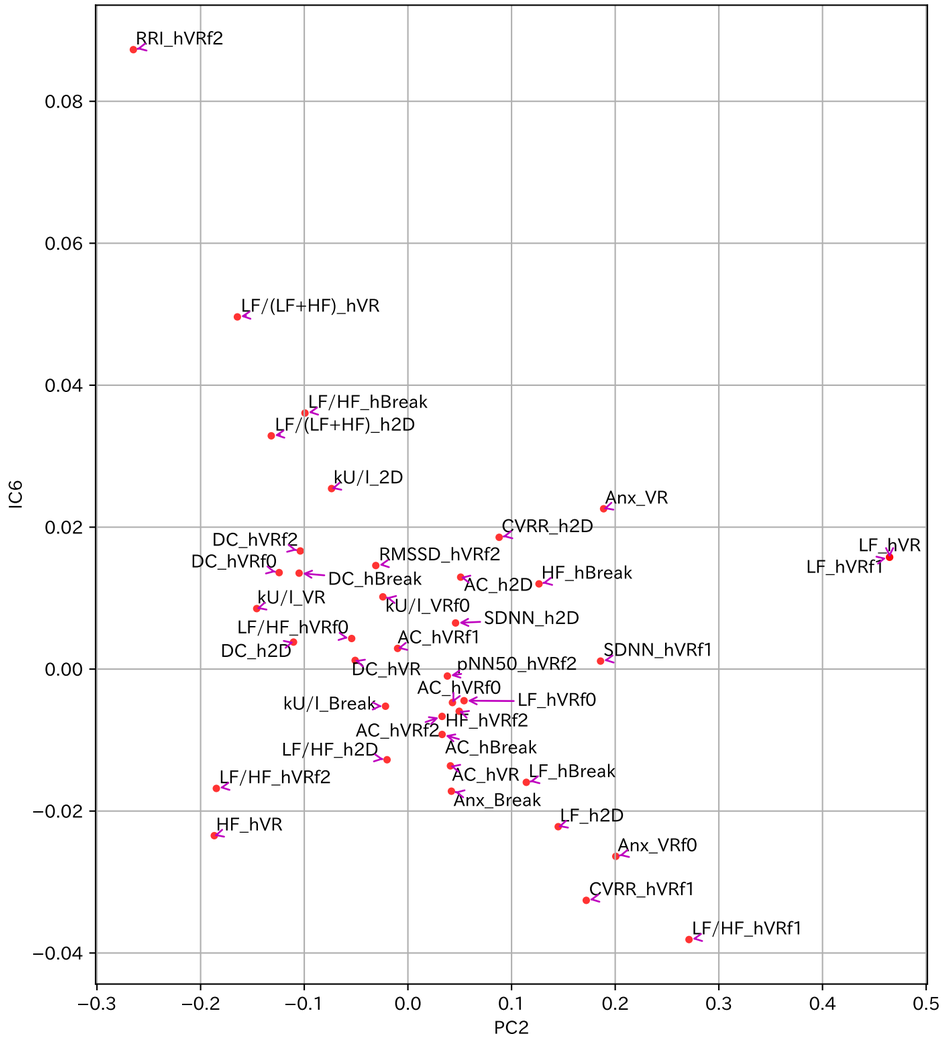

RRI_hVRf2
LF/(LF+HF)_hVR
LF/HF_hBreak
LF/(LF+HF)_h2D
kU/I_2D
Anx_VR
CVRR_h2D
DC_hVRf2
LF_hVRf1
DC_hVRf0
LF_hVR
RMSSD_hVRf2
HF_hBreak
kU/I_VRf0
AC_h2D
DC_hBreak
SDNN_h2D
kU/I_VR
LF/HF_hVRf0
AC_hVRf1
DC_h2D
DC_hVR
pNN50_hVRf2
SDNN_hVRf1
HF_hVRf2
LF_hVRf0
kU/I_Break
AC_hVRf0
AC_hBreak
AC_hVRf2
LF/HF_h2D
Anx_Break
LF_hBreak
AC_hVR
LF/HF_hVRf2
HF_hVR
LF_h2D
Anx_VRf0
CVRR_hVRf1
LF/HF_hVRf1
IC6
PC1

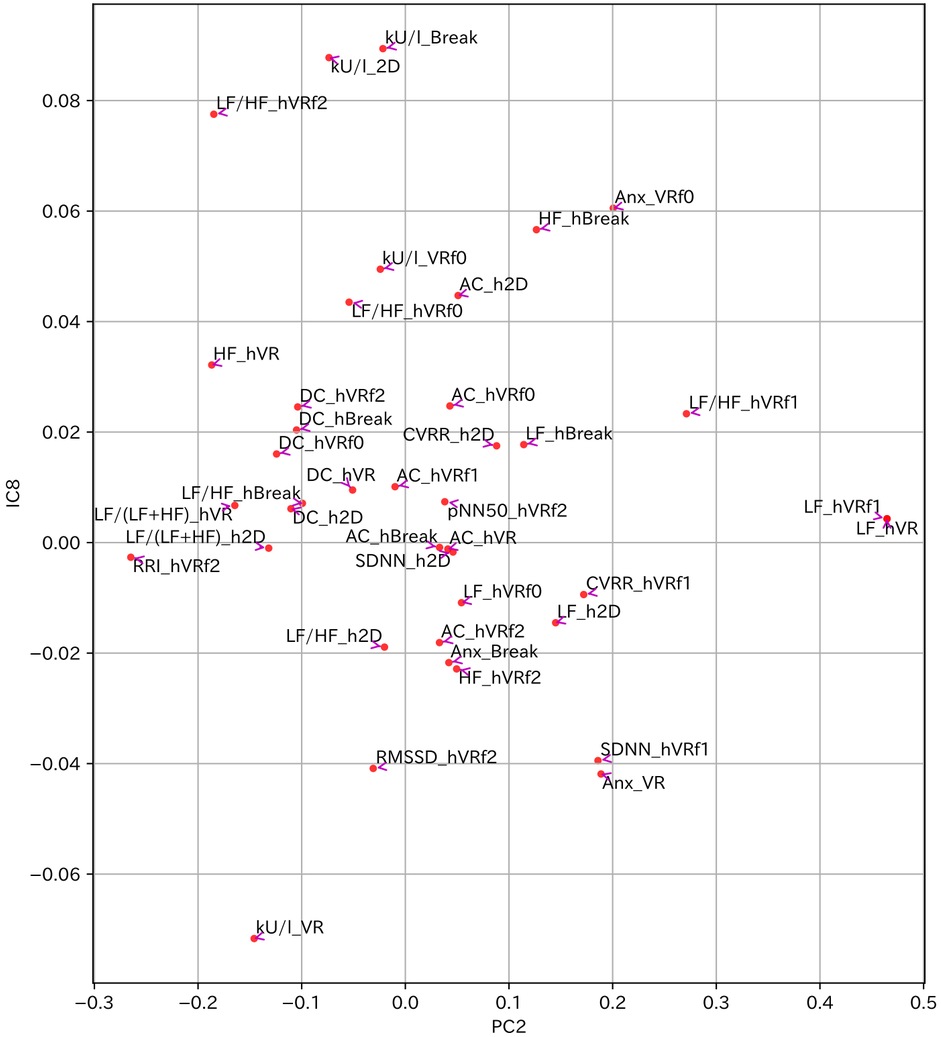

kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
LF/HF_hVRf0
AC_h2D
HF_hVR
AC_hVRf0
DC_hVRf2
LF/HF_hVRf1
DC_hBreak
CVRR_h2D
LF_hBreak
DC_hVRf0
AC_hVRf1
DC_hVR
pNN50_hVRf2
DC_h2D
LF/HF_hBreak
LF_hVR
LF_hVRf1
LF/(LF+HF)_hVR
SDNN_h2D
LF/(LF+HF)_h2D
AC_hBreak
AC_hVR
RRI_hVRf2
CVRR_hVRf1
LF_hVRf0
LF_h2D
AC_hVRf2
HF_hVRf2
Anx_Break
LF/HF_h2D
SDNN_hVRf1
RMSSD_hVRf2
Anx_VR
kU/I_VR
IC8
PC1

kU/I_VR
kU/I_Break
kU/I_2D
HF_hVR
LF/HF_hVRf2
LF_hBreak
SDNN_h2D
LF_h2D
CVRR_hVRf1
LF_hVR
LF_hVRf1
HF_hBreak
AC_hVRf2
SDNN_hVRf1
LF/HF_hVRf1
AC_hVR
LF/(LF+HF)_hVR
AC_hBreak
CVRR_h2D
DC_h2D
DC_hVR
LF/HF_hBreak
kU/I_VRf0
LF_hVRf0
DC_hVRf0
AC_hVRf1
AC_hVRf0
RMSSD_hVRf2
RRI_hVRf2
HF_hVRf2
DC_hBreak
pNN50_hVRf2
AC_h2D
DC_hVRf2
Anx_VRf0
LF/(LF+HF)_h2D
LF/HF_hVRf0
Anx_Break
Anx_VR
LF/HF_h2D
PC5
PC2

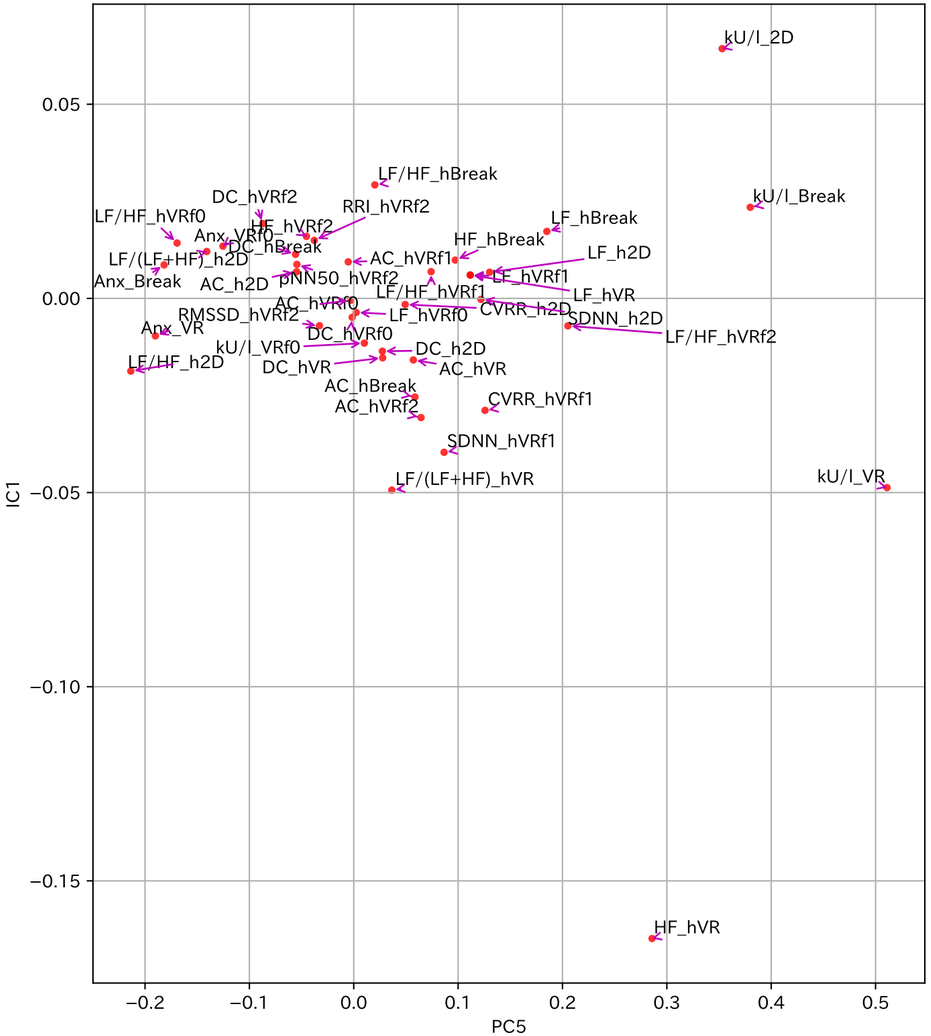
LF/HF_hVRf0
HF_hVR
LF_hVRf1
LF_hVR
LF_hVRf0
LF/HF_hVRf1
RRI_hVRf2
LF/(LF+HF)_hVR
RMSSD_hVRf2
LF/HF_hVRf2
kU/I_VR
pNN50_hVRf2
HF_hVRf2
AC_hVRf1
AC_h2D
DC_hVRf0
DC_hBreak
LF/(LF+HF)_h2D
DC_hVRf2
Anx_VR
DC_h2D
AC_hVRf2
LF/HF_hBreak
AC_hBreak
AC_hVRf0
kU/I_2D
AC_hVR
DC_hVR
Anx_Break
LF/HF_h2D
kU/I_Break
CVRR_h2D
kU/I_VRf0
LF_h2D
SDNN_h2D
CVRR_hVRf1
Anx_VRf0
SDNN_hVRf1
HF_hBreak
LF_hBreak
PC6
PC2

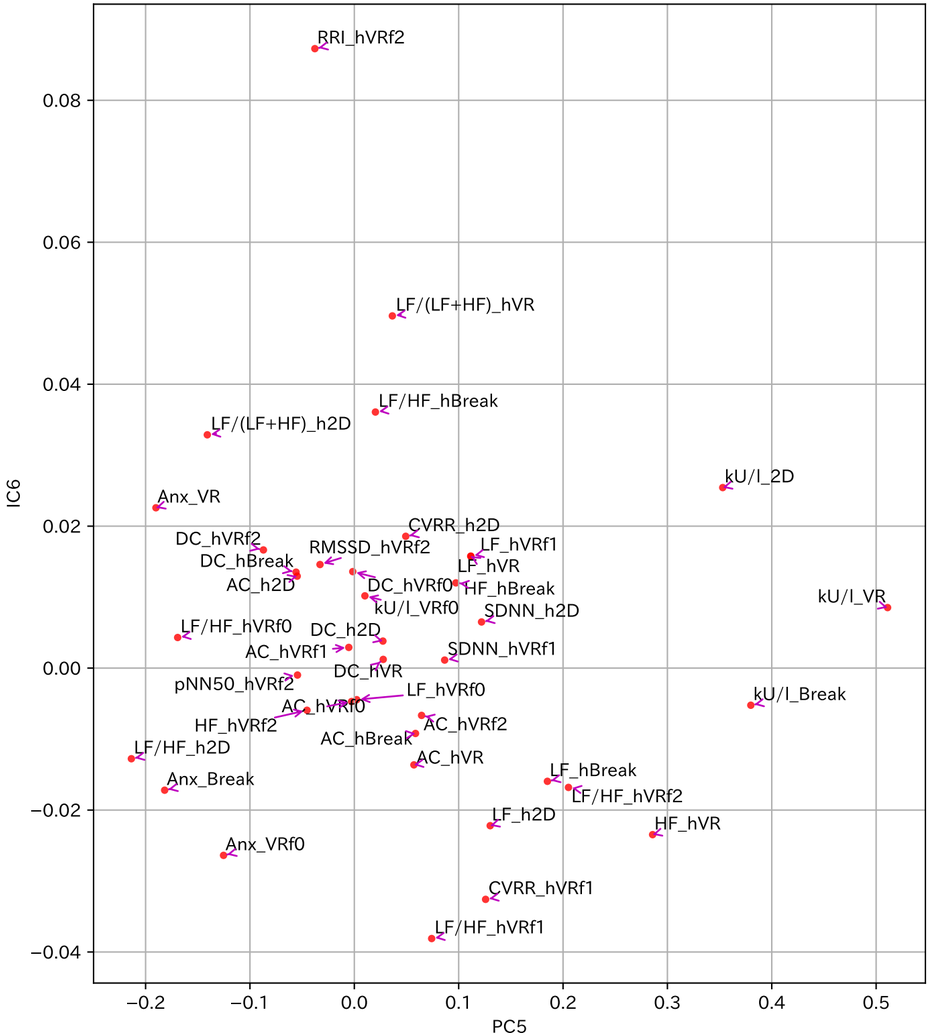

kU/I_2D
LF/HF_hBreak
kU/I_Break
DC_hVRf2
HF_hVRf2
LF_hBreak
LF/HF_hVRf0
Anx_VRf0
Anx_Break
RRI_hVRf2
AC_hVRf1
HF_hBreak
LF/HF_hVRf1
LF_hVR
LF/(LF+HF)_h2D
DC_hBreak
pNN50_hVRf2
AC_h2D
LF_hVRf1
DC_hVRf0
SDNN_h2D
LF_h2D
AC_hVRf0
CVRR_h2D
LF/HF_hVRf2
RMSSD_hVRf2
LF_hVRf0
Anx_VR
kU/I_VRf0
AC_hVR
DC_h2D
DC_hVR
LF/HF_h2D
CVRR_hVRf1
AC_hBreak
AC_hVRf2
SDNN_hVRf1
LF/(LF+HF)_hVR
kU/I_VR
HF_hVR
IC1
PC2

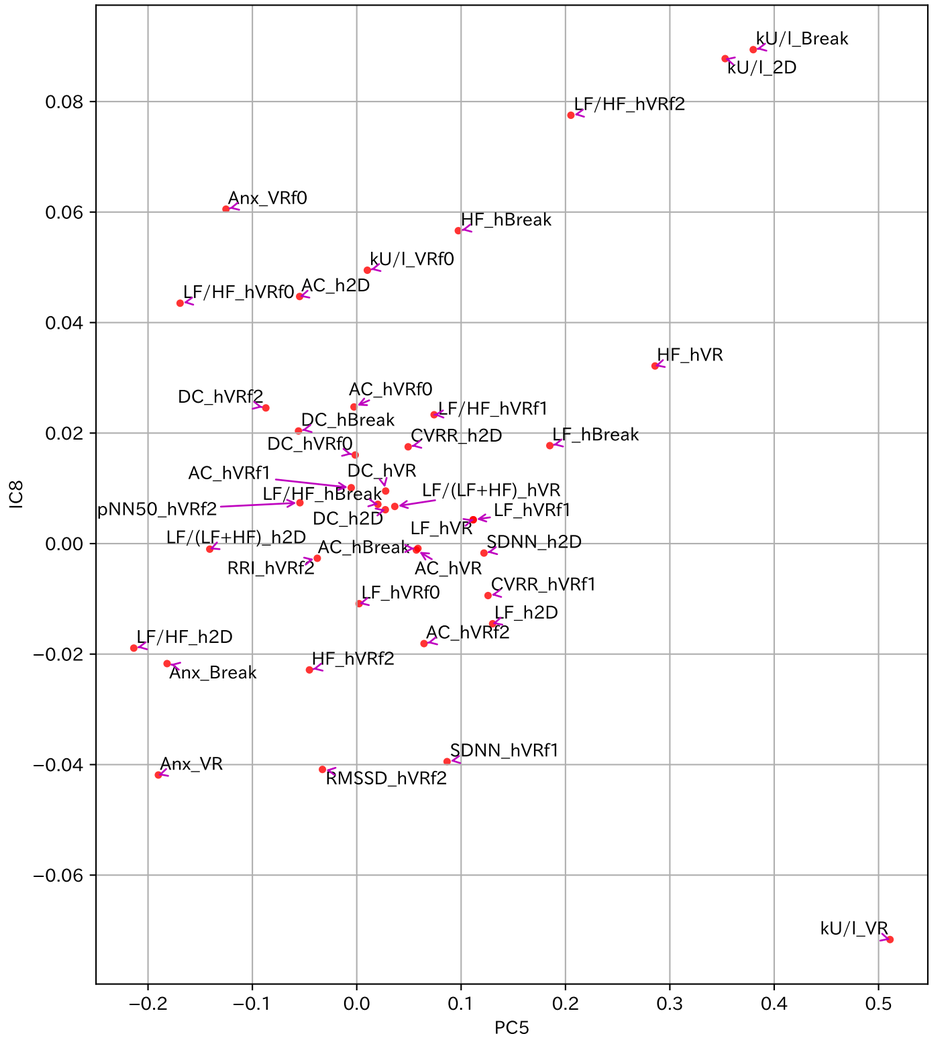

RRI_hVRf2
LF/(LF+HF)_hVR
LF/HF_hBreak
LF/(LF+HF)_h2D
kU/I_2D
Anx_VR
CVRR_h2D
DC_hVRf2
LF_hVR
DC_hVRf0
RMSSD_hVRf2
LF_hVRf1
DC_hBreak
HF_hBreak
AC_h2D
kU/I_VR
kU/I_VRf0
SDNN_h2D
LF/HF_hVRf0
AC_hVRf1
DC_h2D
SDNN_hVRf1
DC_hVR
pNN50_hVRf2
AC_hVRf0
kU/I_Break
LF_hVRf0
HF_hVRf2
AC_hVRf2
AC_hBreak
LF/HF_h2D
AC_hVR
LF_hBreak
LF/HF_hVRf2
Anx_Break
LF_h2D
HF_hVR
Anx_VRf0
CVRR_hVRf1
LF/HF_hVRf1
IC6
PC2

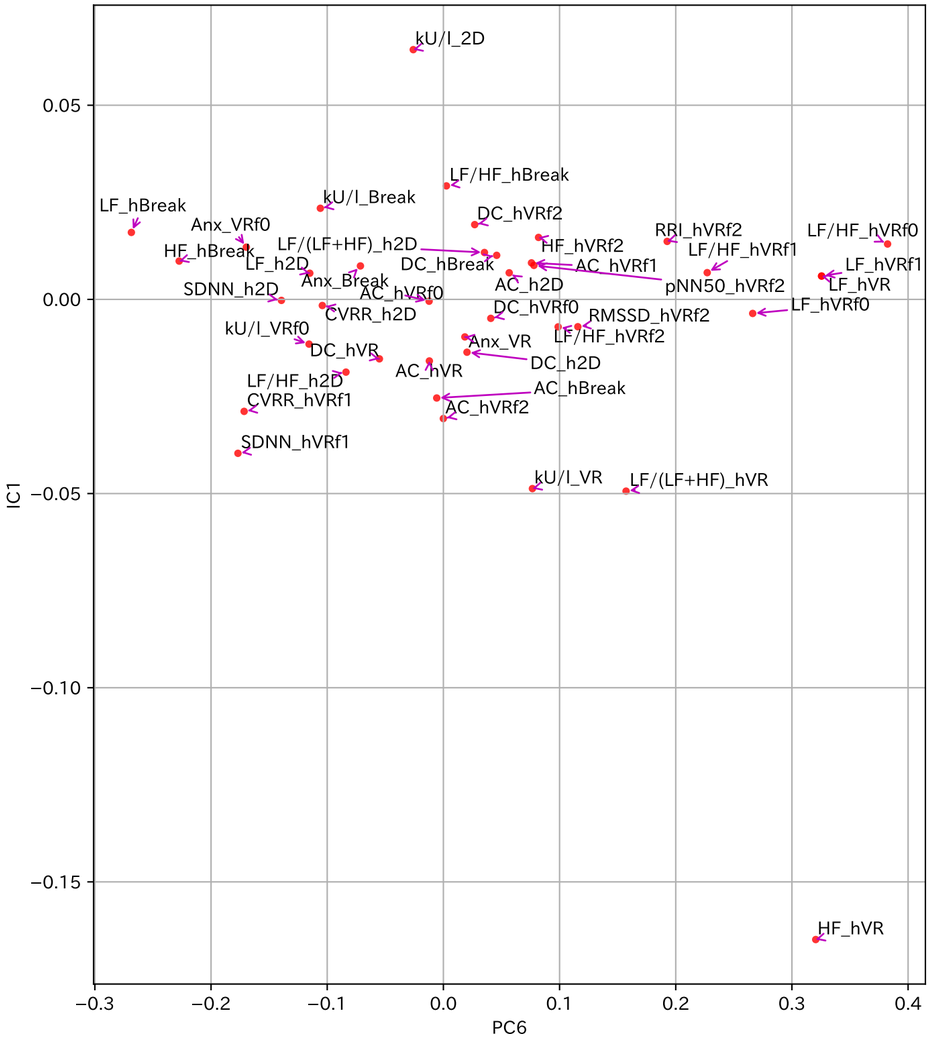

kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
AC_h2D
LF/HF_hVRf0
HF_hVR
DC_hVRf2
AC_hVRf0
LF/HF_hVRf1
DC_hBreak
CVRR_h2D
LF_hBreak
DC_hVRf0
DC_hVR
AC_hVRf1
LF/HF_hBreak
LF/(LF+HF)_hVR
DC_h2D
pNN50_hVRf2
LF_hVRf1
LF_hVR
LF/(LF+HF)_h2D
AC_hBreak
AC_hVR
SDNN_h2D
RRI_hVRf2
LF_hVRf0
CVRR_hVRf1
LF_h2D
LF/HF_h2D
AC_hVRf2
Anx_Break
HF_hVRf2
RMSSD_hVRf2
SDNN_hVRf1
Anx_VR
kU/I_VR
IC8
PC2

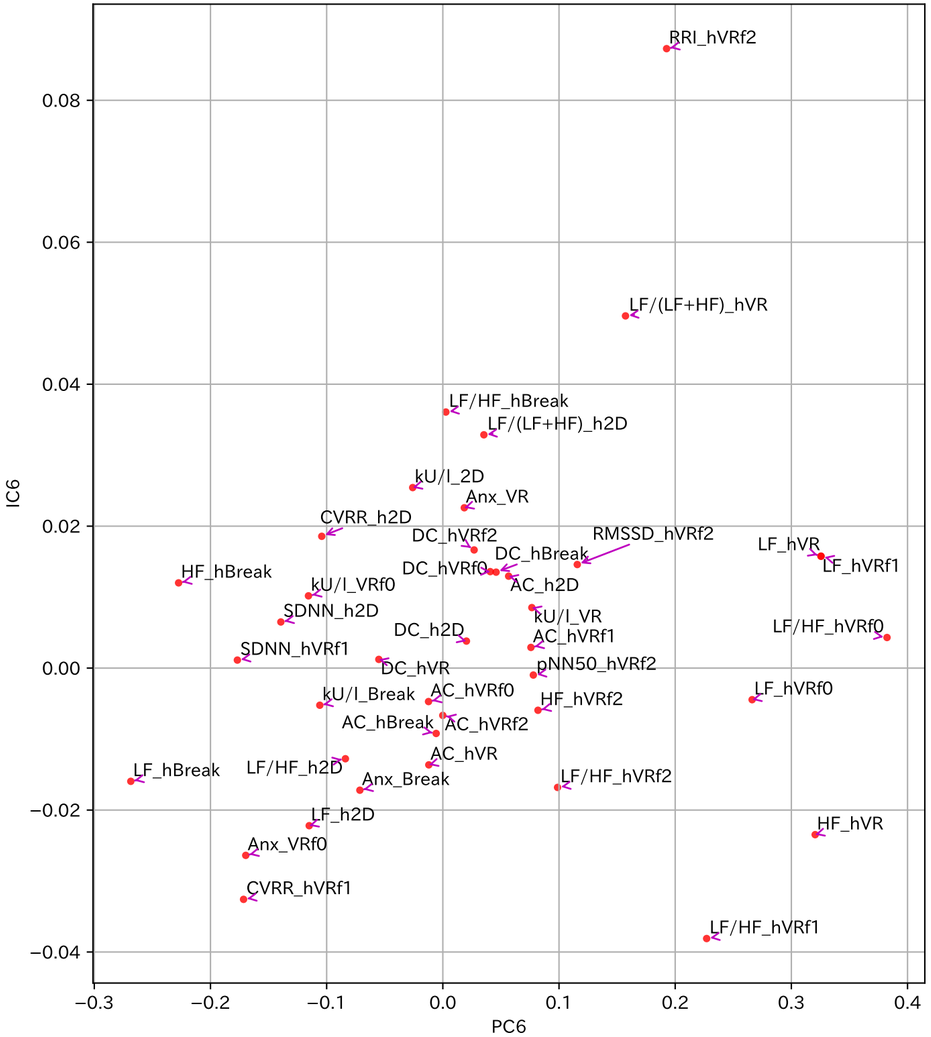
LF/HF_hVRf0
LF_hVRf1
HF_hVR
LF_hVR
LF_hVRf0
LF/HF_hVRf1
RRI_hVRf2
LF/(LF+HF)_hVR
RMSSD_hVRf2
LF/HF_hVRf2
kU/I_VR
pNN50_hVRf2
HF_hVRf2
AC_hVRf1
AC_h2D
DC_hVRf0
DC_hBreak
LF/(LF+HF)_h2D
DC_h2D
Anx_VR
DC_hVRf2
AC_hVRf2
LF/HF_hBreak
AC_hBreak
AC_hVRf0
AC_hVR
kU/I_2D
DC_hVR
Anx_Break
LF/HF_h2D
CVRR_h2D
kU/I_Break
kU/I_VRf0
LF_h2D
SDNN_h2D
Anx_VRf0
CVRR_hVRf1
SDNN_hVRf1
HF_hBreak
LF_hBreak
PC6
PC5

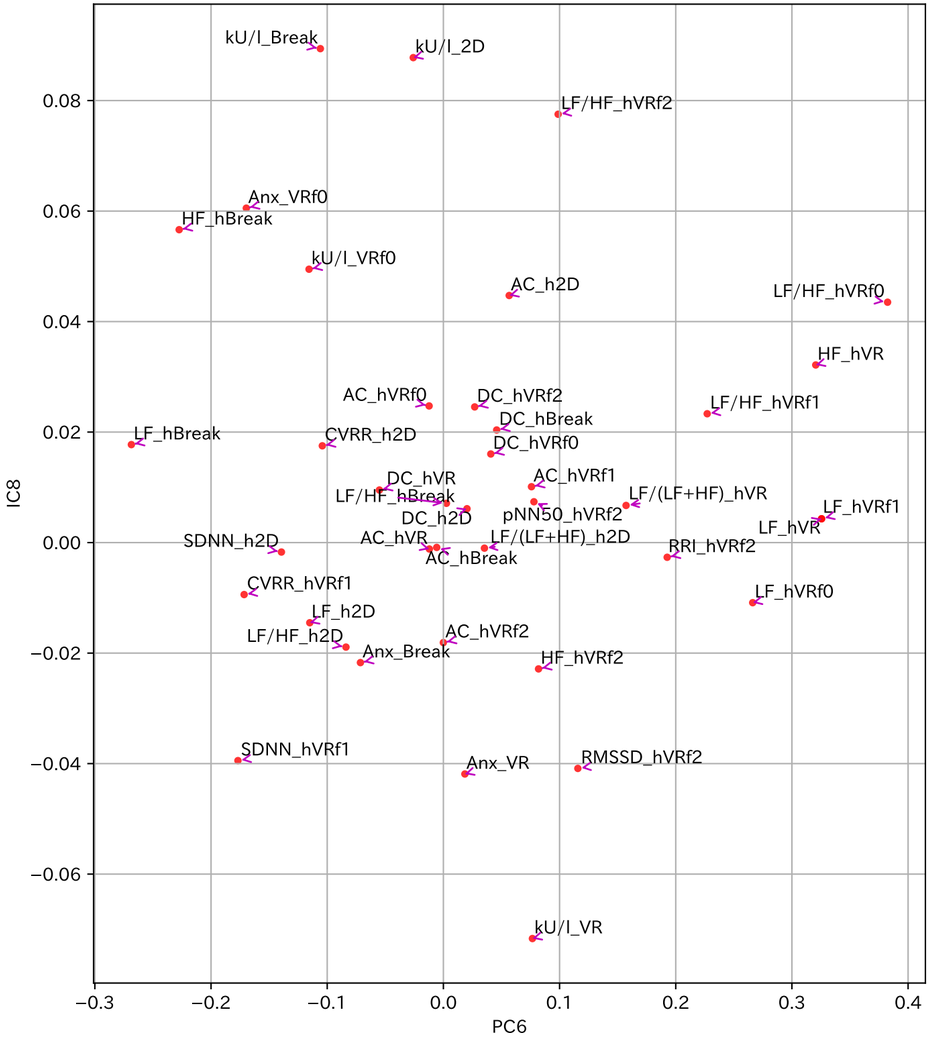

kU/I_2D
LF/HF_hBreak
kU/I_Break
DC_hVRf2
RRI_hVRf2
LF/HF_hVRf0
LF_hBreak
Anx_VRf0
HF_hVRf2
HF_hBreak
DC_hBreak
LF_h2D
LF/(LF+HF)_h2D
AC_hVRf1
Anx_Break
AC_h2D
pNN50_hVRf2
LF_hVRf1
LF/HF_hVRf1
LF_hVR
AC_hVRf0
CVRR_h2D
RMSSD_hVRf2
LF_hVRf0
SDNN_h2D
Anx_VR
DC_hVRf0
LF/HF_hVRf2
kU/I_VRf0
DC_h2D
LF/HF_h2D
DC_hVR
AC_hVR
AC_hBreak
CVRR_hVRf1
AC_hVRf2
SDNN_hVRf1
LF/(LF+HF)_hVR
kU/I_VR
HF_hVR
IC1
PC5

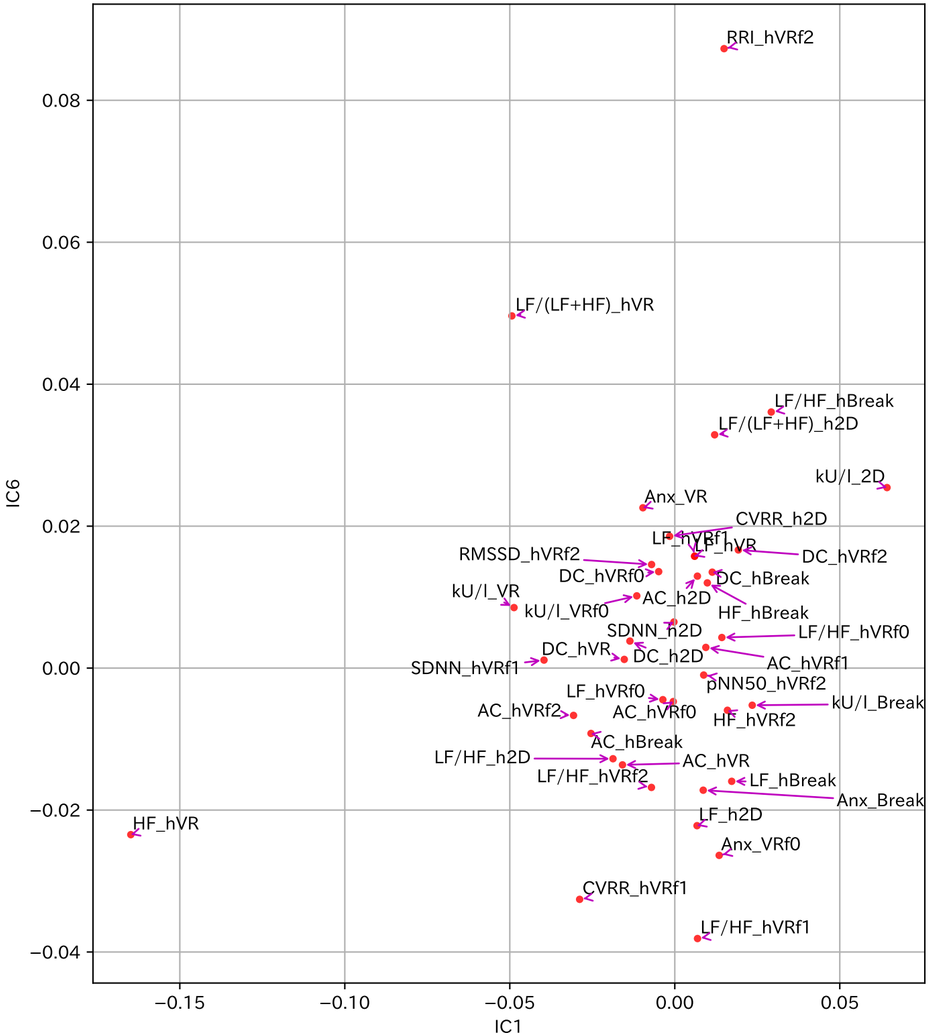

RRI_hVRf2
LF/(LF+HF)_hVR
LF/HF_hBreak
LF/(LF+HF)_h2D
kU/I_2D
Anx_VR
CVRR_h2D
DC_hVRf2
RMSSD_hVRf2
LF_hVRf1
DC_hBreak
LF_hVR
AC_h2D
DC_hVRf0
HF_hBreak
kU/I_VR
kU/I_VRf0
SDNN_h2D
LF/HF_hVRf0
DC_h2D
AC_hVRf1
SDNN_hVRf1
DC_hVR
pNN50_hVRf2
LF_hVRf0
kU/I_Break
AC_hVRf0
HF_hVRf2
AC_hVRf2
AC_hBreak
LF/HF_h2D
AC_hVR
LF_hBreak
Anx_Break
LF/HF_hVRf2
LF_h2D
HF_hVR
Anx_VRf0
CVRR_hVRf1
LF/HF_hVRf1
IC6
PC5


kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
LF/HF_hVRf0
AC_h2D
HF_hVR
DC_hVRf2
AC_hVRf0
LF/HF_hVRf1
DC_hBreak
DC_hVRf0
CVRR_h2D
LF_hBreak
AC_hVRf1
DC_hVR
LF/HF_hBreak
LF/(LF+HF)_hVR
pNN50_hVRf2
DC_h2D
LF_hVRf1
LF_hVR
LF/(LF+HF)_h2D
AC_hBreak
SDNN_h2D
RRI_hVRf2
AC_hVR
CVRR_hVRf1
LF_hVRf0
LF_h2D
LF/HF_h2D
AC_hVRf2
HF_hVRf2
Anx_Break
SDNN_hVRf1
Anx_VR
RMSSD_hVRf2
kU/I_VR
IC8
PC5


kU/I_2D
LF/HF_hBreak
LF_hBreak
kU/I_Break
DC_hVRf2
Anx_VRf0
RRI_hVRf2
LF/HF_hVRf0
HF_hBreak
LF/(LF+HF)_h2D
HF_hVRf2
LF/HF_hVRf1
LF_h2D
DC_hBreak
AC_hVRf1
LF_hVRf1
Anx_Break
AC_h2D
pNN50_hVRf2
LF_hVR
SDNN_h2D
AC_hVRf0
DC_hVRf0
LF_hVRf0
CVRR_h2D
RMSSD_hVRf2
kU/I_VRf0
LF/HF_hVRf2
Anx_VR
DC_hVR
DC_h2D
AC_hVR
LF/HF_h2D
AC_hBreak
CVRR_hVRf1
AC_hVRf2
SDNN_hVRf1
kU/I_VR
LF/(LF+HF)_hVR
HF_hVR
IC1
PC6


RRI_hVRf2
LF/(LF+HF)_hVR
LF/HF_hBreak
LF/(LF+HF)_h2D
kU/I_2D
Anx_VR
CVRR_h2D
DC_hVRf2
RMSSD_hVRf2
DC_hBreak
LF_hVR
LF_hVRf1
HF_hBreak
DC_hVRf0
AC_h2D
kU/I_VRf0
SDNN_h2D
kU/I_VR
DC_h2D
AC_hVRf1
LF/HF_hVRf0
SDNN_hVRf1
DC_hVR
pNN50_hVRf2
kU/I_Break
AC_hVRf0
HF_hVRf2
LF_hVRf0
AC_hBreak
AC_hVRf2
AC_hVR
LF_hBreak
LF/HF_h2D
Anx_Break
LF/HF_hVRf2
LF_h2D
HF_hVR
Anx_VRf0
CVRR_hVRf1
LF/HF_hVRf1
IC6
PC6


kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
AC_h2D
LF/HF_hVRf0
HF_hVR
AC_hVRf0
DC_hVRf2
LF/HF_hVRf1
DC_hBreak
LF_hBreak
CVRR_h2D
DC_hVRf0
DC_hVR
AC_hVRf1
LF/HF_hBreak
LF/(LF+HF)_hVR
DC_h2D
pNN50_hVRf2
LF_hVRf1
LF_hVR
SDNN_h2D
AC_hVR
LF/(LF+HF)_h2D
RRI_hVRf2
AC_hBreak
CVRR_hVRf1
LF_hVRf0
LF_h2D
LF/HF_h2D
AC_hVRf2
Anx_Break
HF_hVRf2
SDNN_hVRf1
Anx_VR
RMSSD_hVRf2
kU/I_VR
IC8
PC6


RRI_hVRf2
LF/(LF+HF)_hVR
LF/HF_hBreak
LF/(LF+HF)_h2D
kU/I_2D
Anx_VR
CVRR_h2D
LF_hVRf1
LF_hVR
RMSSD_hVRf2
DC_hVRf2
DC_hVRf0
DC_hBreak
kU/I_VR
AC_h2D
kU/I_VRf0
HF_hBreak
SDNN_h2D
LF/HF_hVRf0
DC_hVR
DC_h2D
AC_hVRf1
SDNN_hVRf1
pNN50_hVRf2
LF_hVRf0
kU/I_Break
AC_hVRf2
AC_hVRf0
HF_hVRf2
AC_hBreak
LF/HF_h2D
AC_hVR
LF/HF_hVRf2
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HF_hVR
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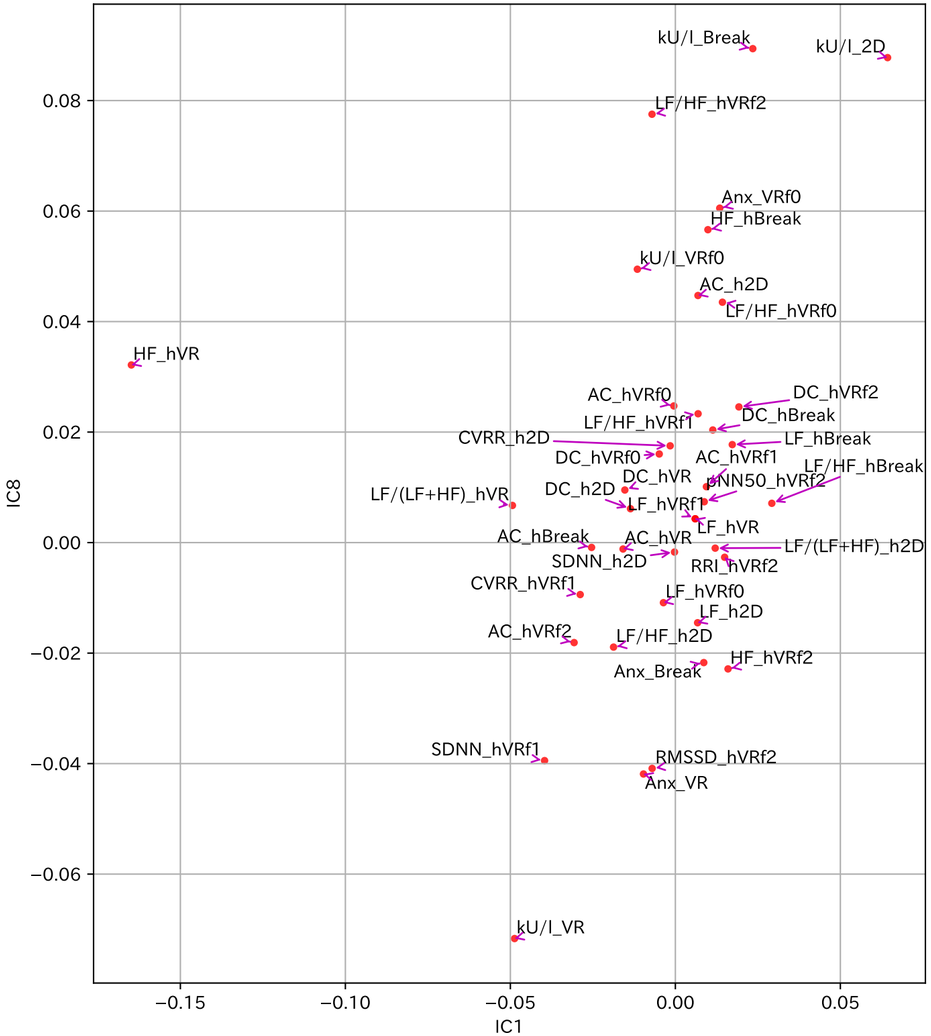
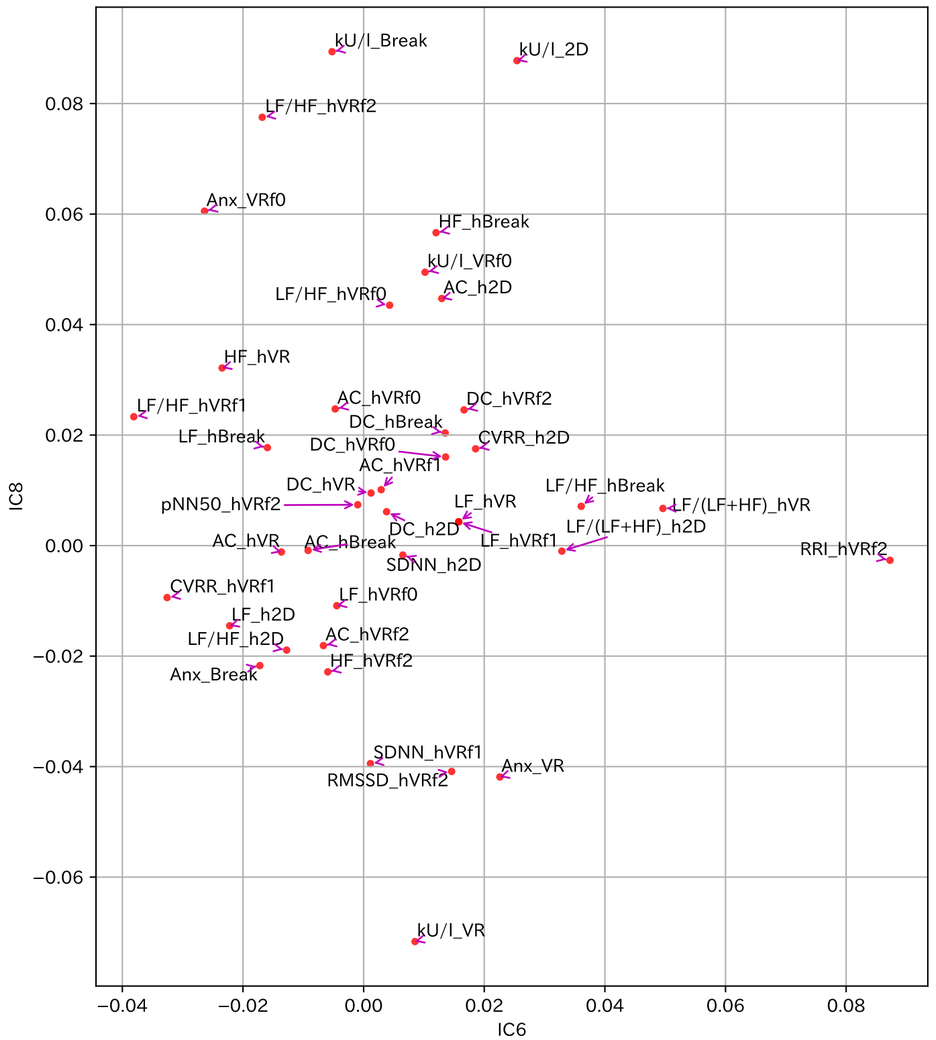
Anx_VRf0
CVRR_hVRf1
LF/HF_hVRf1
IC6
IC1


kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
AC_h2D
LF/HF_hVRf0
HF_hVR
AC_hVRf0
DC_hVRf2
LF/HF_hVRf1
DC_hBreak
CVRR_h2D
LF_hBreak
DC_hVRf0
AC_hVRf1
LF/HF_hBreak
DC_hVR
pNN50_hVRf2
LF/(LF+HF)_hVR
DC_h2D
LF_hVRf1
LF_hVR
AC_hBreak
AC_hVR
LF/(LF+HF)_h2D
SDNN_h2D
RRI_hVRf2
CVRR_hVRf1
LF_hVRf0
LF_h2D
AC_hVRf2
LF/HF_h2D
HF_hVRf2
Anx_Break
SDNN_hVRf1
RMSSD_hVRf2
Anx_VR
kU/I_VR
IC8
IC1


kU/I_Break
kU/I_2D
LF/HF_hVRf2
Anx_VRf0
HF_hBreak
kU/I_VRf0
LF/HF_hVRf0
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HF_hVR
LF/HF_hVRf1
AC_hVRf0
DC_hVRf2
DC_hBreak
LF_hBreak
DC_hVRf0
CVRR_h2D
AC_hVRf1
DC_hVR
LF/HF_hBreak
pNN50_hVRf2
LF_hVR
LF/(LF+HF)_hVR
DC_h2D
LF/(LF+HF)_h2D
AC_hVR
AC_hBreak
LF_hVRf1
RRI_hVRf2
SDNN_h2D
CVRR_hVRf1
LF_hVRf0
LF_h2D
LF/HF_h2D
AC_hVRf2
HF_hVRf2
Anx_Break
SDNN_hVRf1
Anx_VR
RMSSD_hVRf2
kU/I_VR
IC8
IC6
0.08
0.06
0.04
0.02
0.00
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−0.04
−0.06
−0.04
−0.02
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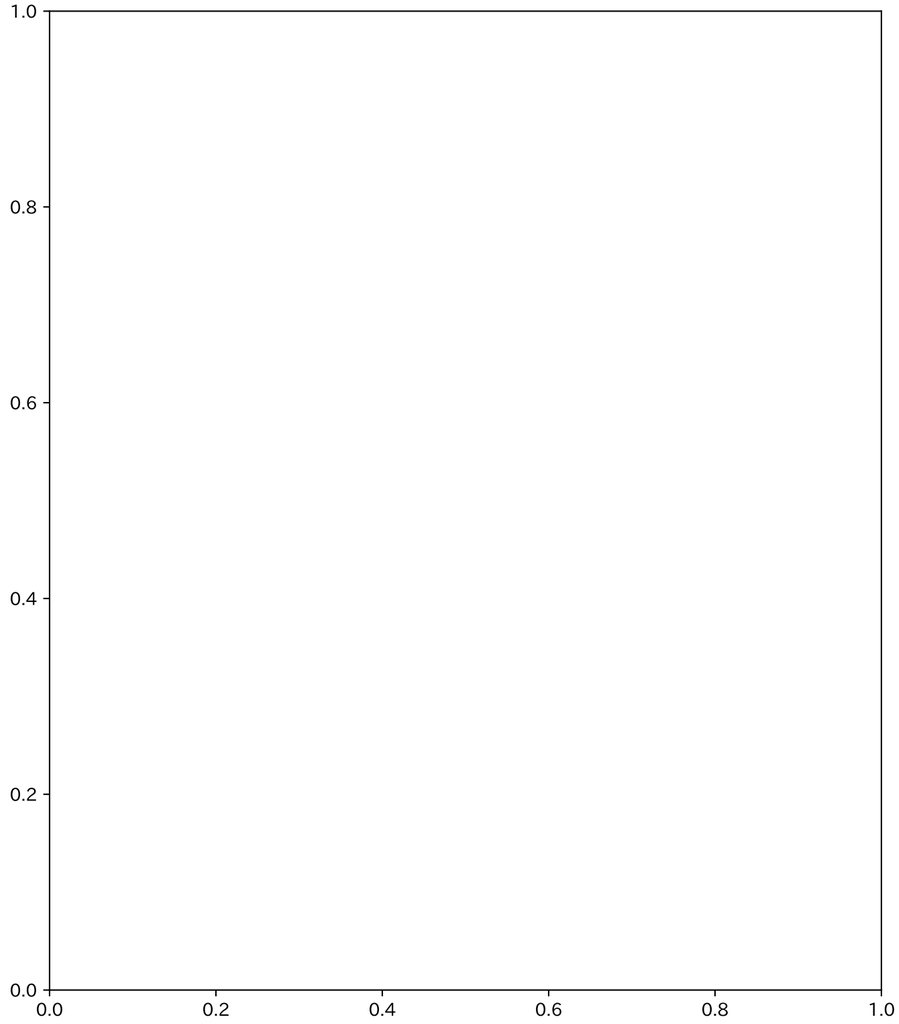
0.08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0.6
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0.2
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0.6
0.8
1.0