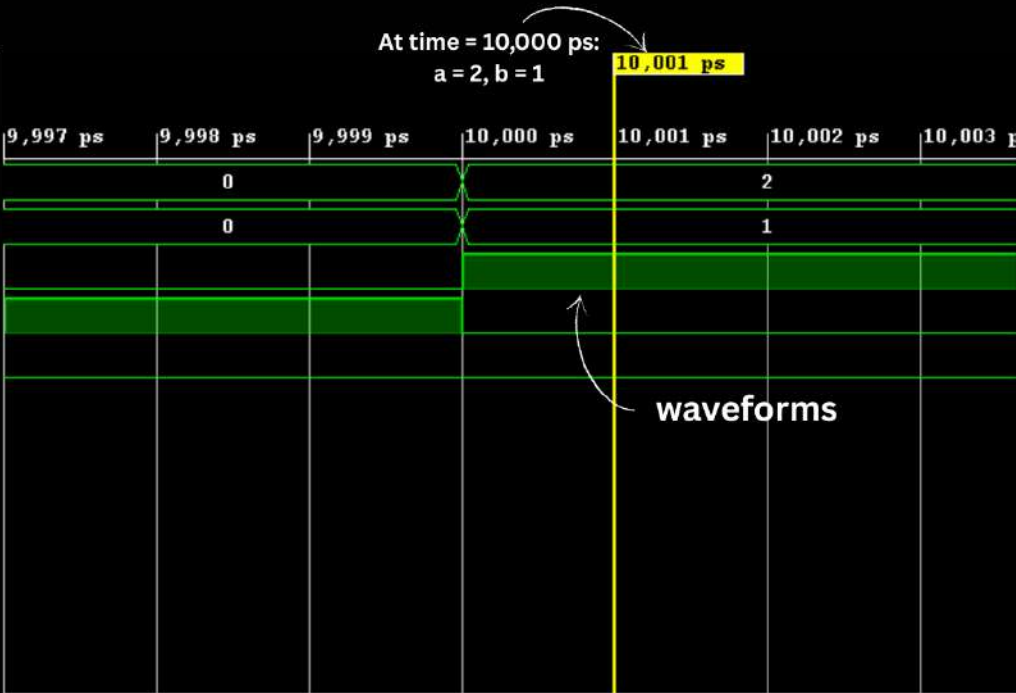


4-bit comparator

a,b	Name	Value
>	a[3:0]	2
>	b[3:0]	1
	agtb	1
	aeqb	0
	altb	0

- Output:
- agtb = 1
 - aeqb = 0
 - altb = 0

This means A > B



RTL schematic auto-generated by Vivado

