## COMPARISON BETWEEN PARALLEL AND SERIAL ALGORITHMS USING OpenMP+SIMD

#### A PROJECT REPORT FOR J COMPONENT

## PARALLEL AND DISRIBUTED COMPUTING (CSE 4001)

PROJECT SUPERVISOR- PROF. SAIRA BANU J

Submitted by

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**School of Computer Science Engineering** 



Nov. 2017

# **School of Computer Science and Engineering**

# **CERTIFICATE**

This is to certify that the project entitled, "Comparision between Parallel and serial algorithms using OpenMP +SIMD" submitted by the students Shikhar Singh=16BCE2316 and Aratrik Paul=16BCE0519 for the course "Parallel and Distibuted Computing(CSE-4001)" at "VIT university", is an authentic work carried out by the members of the team under my supervision and guidance. To the best of my knowledge, the matter embodied in the project has not been submitted to any other University / Institute for any purpose and is unique.

Date:	
Place:	

Signature

## **ACKNOWLEDGEMENTS**

We would like to extend our thanks to the management of VIT University, Vellore and our respected chancellor G. Viswanathan for giving us the opportunity to carry out our project in VIT.

We would also like to extend our gratitude to our faculty, Prof. Saira Banu J for her constant and ceaseless guidance and support through the entire course of this project. We would also want to thank ma'am for guiding us all along the way and encouraging us to think out of the box in an attempt to maximize the project's validity for the society.

Shikhar Singh-16BCE2316 Aratrik Paul – 16BCE0519

# **ABSTRACT**

We the students of VIT, Vellore have made a project on "Comparison of Parallel and serial Algorithms using OpenMP +SIMD" as our Parallel and Distributed Computing project for the fifth semester. Our goal was to give an overview of the Analysis and speedup of Different Algorithms such as Dijkstra's Algorithm and find out how effective it would be if it were parallelized.

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#### 1) INTRODUCTION

**OpenMP** (**Open Multi-Processing**) is an application programming interface (API) that helps us use a multiple platform shared memory multi - processing programming in C, C++ and/or Fortran, on most platforms, the instruction set architectures and operating systems, including Solaris, AIX, HP-UX, Linux, macOS, and Windws. It consists of a set ofdirectives and library routines that influence run-time behavior.

OpenMP is managed by the non for profit technology Company OpenMP Architecture Review Board or OpenMP ARB, jointly defined by a group of major computer hardware and software vendors, including AMD, IBM, Intel, Cray, HP, Fujitsu, Nvidia, NEC, Red Hat, Texas Instruments, Oracle Corporation, and many more.

OpenMP uses a portable and scalable model that gives programmers a simple interface for developing parallel applications for platforms may it be on a computer, Desktop or a Supercomputer.

An app built with the hybrid model of parallel programming can run on a Desktop cluster using both OpenMP and Message Passing Interface (MPI), such that OpenMP is used for parallelism within a multiple core node while MPI is used for parallelism amongst nodes. There have also been efforts to run OpenMP on software distributed shared memory systems, to translate OpenMP into MPI and to extend OpenMP for non-shared memory systems.

OpenMP is the way to implement multithreading, a method of parallelizing where a master thread forks a specified number of slave threads ( as it uses the Master Slave Model) and the system divides a task among them. The threads then run at the same time, with the Main Environment allocating threads to different processors.

The Part of the code that is meant to run in the parallel area is marked accordingly, with a compiler directive that forms threads before the section is executed. Each thread has an id attached to it which can be obtained using a function omp\_get\_thread\_num(). The thread id is an integer, and the master thread has an id of 0. After the execution of the parallelized code, the threads report back to the master Program and hence we have a completely compiled parallel program.

By default, each thread executes the parallelized section of code independently. Worksharing can be used to divide a task among the threads so that each thread executes its allocated part of the code. We can achieve both the parallelism methods of task and data parallelism by using OpenMP this way.

The environment allocates threads to processors depending on usage, machine load and other factors. The runtime environment can assign the number of threads based on

environment variables, or the code can do so using functions. The OpenMP functions are included in a header file labelled <0mp.h> in C/C++.

#### **SIMD**

**Single instruction, multiple data** (**SIMD**) comes under the class of parallel computers in Flynn's taxonomy. It talks about computers with multiple processing elements that perform the same operation on multiple data points at the same point of time. These machines take advantage of data level parallelism, but not concurrency: there are parallel computations, but only a single process at a given moment. SIMD is particularly applicable to normal problems such as adjusting the volume of digital audio. Most modern CPU designs use SIMD instructions to improve the performance of multimedia use.

SIMD was the basis for vector supercomputers of the early 1970s such as the CDC Star-100 and the Texas Instruments ASC, which could operate on a "vector" of data with a single instruction. Vector processing was especially popularized by Cray in the 1970s and 1980s[1]. Vector-processing architectures are now considered separate from SIMD computers, based on the fact that vector computers processed the vectors one word at a time through pipelined processors (though still based on a single instruction), whereas modern SIMD computers process all elements of the vector simultaneously.

All of these developments have been made toward support for real-time graphics, and are therefore brought forward toward processing in two, three, or four dimensions, usually with vector lengths of between two and sixteen words, depending on it's data type and architecture. When we want to make the difference between the SIMD architectures of old an the newer architectures, the newer are considered "short-vector" architectures, as earlier SIMD and vector supercomputers had vector lengths from 64 to 64,000. A modern supercomputer is almost always a cluster of MIMD computers, each of which implements (short-vector) SIMD instructions. A modern desktop computer is often a multiprocessor MIMD computer where each processor can execute short-vector SIMD instructions.

The Algorithms we have chosen to do the project are Floyd Warshall's Algorithm and Bellman Ford's Algorithm.

#### A) Bellman Ford's Algorithm

The Bellman Ford algorithm is an algorithm for finding shortest paths in a weighted graph with positive or negative edge weights with no negative cycles. A single execution of the algorithm will find the lengths (summed weights) of shortest paths between all pairs of vertices. Although it does not return details of the paths themselves, it is possible to reconstruct the paths with simple modifications to the algorithm. Versions of the algorithm can also be used for finding the transitive closure of a relation R , or (in connection with the Schulze voting system) widest paths between all pairs of vertices in a weighted graph.

### 2) Literature Review

1) Performance of Shortest Path Algorithm Based on Parallel Vertex Traversal

Written by- Mihailo Vesović, Aleksandra Smiljanić, Dušan Kostić Date Published: February 2016

Abstract: Shortest path algorithms for different applications, such as Internet routing, VLSI design and so on are used. Floyd Warshall and Bellman-Ford are commonly used shortest path algorithms which are typically implemented in networks with hundreds of nodes. However, scale of shortest path problems is increasing, and more efficient algorithms are needed. With the development of multicore processors, one natural way to speedup shortest path algorithms is through parallelization. In this paper, we propose a novel shortest path algorithm with parallel vertex transversal, and compare its speed with standard solutions in datacenter topologies.

2) Performance Anaysis of Parallel Algorithms on Multi-Core Systems Written by: Sanjay Kumar Sharma and Dr Kusum Bhansali Date Published: October 2012

Abstarct: The current multi-core architectures have become popular due to performance, and efficient processing of multiple tasks simultaneously. Today's the parallel algorithms are focusing on multi-core systems. The design of parallel algorithm and performance measurement is the major issue on multi-core environment. If one wishes to execute a

single application faster, then the application must be divided into subtask or threads to deliver desired result. Numerical problems, especially the solution of linear system of equation have many applications in science and engineering. This paper describes and analyzes the parallel algorithms for computing the solution of dense system of linear equations, and to approximately compute the value of using OpenMP interface. The performances (speedup) of parallel algorithms on multi-core system have been presented. The experimental results on a multi-core processor show that the proposed parallel algorithms achieves good performance (speedup) compared to the sequential.

3) Parallel Computing using OpenMP Written By: Ms. Ashwini M. Bhugul Date Published: February ,2017

Abstract: Parallel Computing is most widely used paradigm today to improve the system performance. System performance can be increase by executing application on multiple processors. Today's Computers are complex they are capable of executing application program on multiple processors. OpenMP is API for running application parallelly to improve the speedup. This paper reviews about OpenMP API and focuses on improving the system performance.

4) OpenMP: an industry standard API for shared-memory programming

Written by: L Dagum, R Menon

Date Published: Jan,2010

Abstract: At its most elemental level, OpenMP is a set of compiler directives and callable runtime library routines that extend Fortran (and separately, C and C++ to express shared memory parallelism. It leaves the base language unspecified, and vendors can implement OpenMP in any Fortran compiler. Naturally, to support pointers and allocatables, Fortran 90 and Fortran 95 require the OpenMP implementation to include additional semantics over Fortran 77. OpenMP leverages many of the shared mamory concepts while extending them to support coarse grain parallelism. The standard also includes a callable runtime library with accompanying environment variables.

5) High performance computing using MPI and OpenMP on multi-core parallel systems

Written by: Haoqiang Jin, Piyush Mehrotra

Date Published:March,2012

Abstract: The rapidly increasing number of cores in modern microprocessors is pushing the current high performance computing (HPC) systems into the petascale and exascale era. The hybrid nature of these systems – distributed memory across nodes and shared

memory with non-uniform memory access within each node – poses a challenge to application developers. In this paper, we study a hybrid approach to programming such systems – a combination of two traditional programming models, MPI and OpenMP

### 3) Implementation

We have gone ahead to implement this project on the Ubuntu linux 18.04 LTS system. Where we have used c++ codes and intrinsically installed the gcc compiler and omp libraries used to run an OpenMP Program. The two codes are for Floyd Warshall's and Bellman Ford's Parallel codes using OpenMP + SIMD.

#### **Screenshots:**





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                   for (int rank = 0; rank < p; rank++) {
    has_change |= local_has_change[rank];</pre>
                   }
               if (!has_change) {
                    break;
              }
         }
has_change = true;;
                  }
              }
          }
*has_negative_cycle = has_change;
     3
     //step 4: free memory (if any)
}
int main(int argc, char **argv) {
   if (argc <= 1) {</pre>
          utils::abort_with_error_message("INPUT FILE WAS NOT FOUND!");
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                                                                                     FinalAPSP.cpp
#include <iostream:
#include <fstream>
#include <climits>
#include <string>
#include <sstream>
#include <omp.h>
using namespace std;
void updateSubmatrix(int a_row,int a_col,int b_row,int b_col,int c_row,int c_col,int **A,int b)
                    //This will update the tile which is having dimension bXb
                    int i,j,k;
for(k=0;k<b;k++)</pre>
                             for(i=0;i<b;i++)</pre>
                                       for(j=s)j<b;j++)

if(A[b_row+i][b_col+k]< INT_MAX && A[c_row+k][c_col+j]< INT_MAX )

A[a_row+i][a_col+j]=min(A[a_row+i][a_col+j],(A[b_row+i][b_col+k]+A[c_row+k][c_col+j]));
                   }
          }
void tiledFloydWarshall(int **A,int n,int b,int t)
          //ntrow is for number of times kth-block will iterate
//nbrow is actually the dimension of each tile nbrowXnbrow
int ntrow=n/b;
          int nbrow=n/ntrow;
          int k,i,j;
omp_set_num_threads(t);
          \label{eq:formula} \textbf{for}(k\texttt{=0};k\texttt{<ntrow};k\texttt{++}) \text{ //Iterator to position our main tile.}
                    //Phase 1: updates the k-th diagonal-tile(main tile)
                    updateSubmatrix(k*nbrow,k*nbrow,k*nbrow,k*nbrow,k*nbrow,k*nbrow,A,b);
                    //Phase 2:updates the tiles which are remained in the main tile row.
                    #pragma omp parallel for
for(j=0;j<ntrow;j++)</pre>
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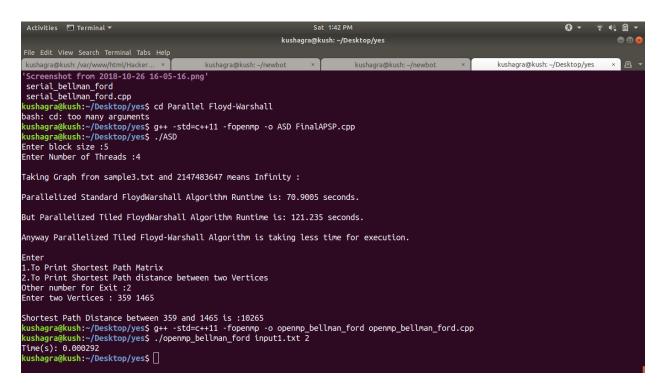
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   tr (argc <= Z) {
   utils::abort_with_error_message("NUMBER OF THREADS WAS NOT FOUND!");</pre>
   string filename = argv[1];
int p = atoi(argv[2]);
   int *dist;
bool has_negative_cycle = false;
   assert(utils::read_file(filename) == 0);
dist = (int *) malloc(sizeof(int) * utils::N);
   timeval start_wall_time_t, end_wall_time_t;
float ms_wall;
   //start timer
gettimeofday(&start_wall_time_t, nullptr);
   //bellman-ford algorithm
bellman_ford(p, utils::N, utils::mat, dist, &has_negative_cycle);
  std::cerr.setf(std::ios::fixed);
std::cerr << std::setprectston(6) << "Time(s): " << (ms_wall / 1000.0) << endl;
utils::print_result(has_negative_cycle, dist);
free(dist);
free(utils::mat);</pre>
   return 0;
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                   //Phase 3:updates the tiles which are remained in the main tile column.
                   #pragma omp parallel for
for(i=0;i<ntrow;i++)</pre>
                              if(i==k){}
else
                              {
                                        updateSubmatrix(i*nbrow, k*nbrow,i*nbrow, k*nbrow,k*nbrow, k*nbrow,A,b);
                   //Phase 4:finally it updates the remaining tiles except that main tile row and column of the matrix pragma omp parallel for collapse(2) pragmark{for}(i=0;i< ntrow;i++)
                             for(j=0;j<ntrow;j++)</pre>
                                       if(i==k||j==k){}
                                       else
{
                                                 updateSubmatrix(i*nbrow, j*nbrow,i*nbrow, k*nbrow,k*nbrow, j*nbrow,A,b);
                                       }
                   }
         }
struct Graph {
   int **A;
   int VertexCount;
    int EdgeCount;
};
int main()
         int i,j;
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         int t;
cout<<"Enter Number of Threads :";</pre>
         cin>>t:
         omp_set_num_threads(t);
         int n,dadd=0,dr;
    Graph G;
int **D;
bool declared = false;
string GraphType, token;
    if (line[0] == 'c')
          continue;
else if (line[0] == 'p')
                   istringstream iss(line);
iss >> token >> GraphType >> G.VertexCount >> G.EdgeCount;
n=G.VertexCount;
dr=n%b;
if(dr!=0)
                                                 dadd=b-dr;
                   if (G.VertexCount > 0)
                             G.A=new int*[G.VertexCount+dadd];
D= new int*[G.VertexCount+dadd];
for(int i=0;i<G.VertexCount+dadd;i++)</pre>
                                                 G.A[i]=new int[G.VertexCount+dadd];
D[i]=new int[G.VertexCount+dadd];
                                                                                                                                C++ ▼ Tab Width: 8 ▼ Ln 119, Col 51 ▼ INS
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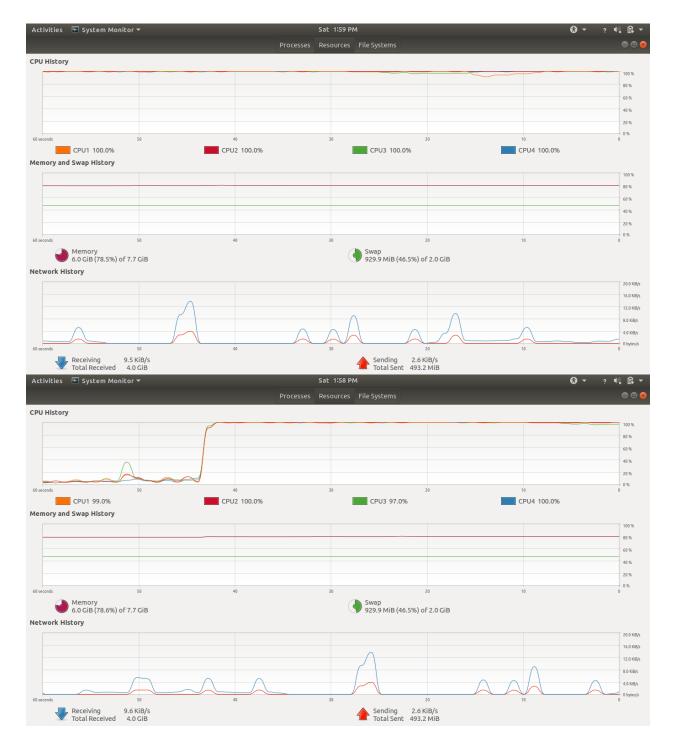
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            else if(declared && line[0] == 'a')
                int w;
               istringstream iss(line);
iss >> token >> i >> j >> w;
G.A[i-1][j-1] = w;
    }
    //Copying Graph 'G.A' to 'D' to apply standard floyd-warshall algorithm on 'D'. #pragma omp parallel for collapse(2)  \frac{\text{for}(i=0;i< n+dadd;i++)}{\text{for}(i=0;i< n+dadd;i++)} 
           for(j=0;j<n+dadd;j++)</pre>
                       D[i][j]=G.A[i][j];
           }
    }
    ' //'D' weight graph, Before Applying Standard Floyd-warshall Algorithm.
cout<<"Parallelized Standard Floyd-Warshall Algorithm :\n\n";
cout<<"Input Graph :\n";</pre>
    for(i=0;i<n;i++)
                    for(j=0;j<n;j++)
cout<<D[i][j]<<" ";
                     cout
cout<<"\n";
*/
    //Applying Parallelization to Standard Floyd-Warshall Algorithm and Calculating the Runtime for that Algorithm. double tm0 = omp_get_wtime(); for(int k=0;k<n;k++)  
#pragma omp parallel for collapse(2)  
for(i=0;i<n;i++)
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                 }
    tm0= omp_get_wtime() - tm0;
  /* //'D' weight graph, After Applying Standard Floyd-warshall Algorithm i.e. Shortest path matrix. cout<<"All Pairs Shortest Path Matrix :\n"; for(i=\theta;i< n;i++)
                     for(j=0;j<n;j++)
    cout<<D[i][j]<<" ";</pre>
                     cout<<"\n";
    } */
cout << "Parallelized Standard FloydWarshall Algorithm Runtime is: " << tm0 << " seconds.\n\n";</pre>
  /* //'G.A' weight graph, Before Applying Tiled Floyd-warshall Algorithm.
cout<<"Parallelized Tiled Floyd-Warshall Algorithm :\n\n";
cout<<"Input Graph :\n";
for(i=0;i<n;i++)</pre>
                     1
          //Applying Parallelization to Tiled Floyd-Warshall Algorithm and Calculating the Runtime for that Algorithm.
          double tm = omp_get_wtime();
tiledFloydWarshall(G.A,n+dadd,b,t);
tm = omp_get_wtime() - tm;
    /*//'G.A' weight graph, After Applying Tiled Floyd-warshall Algorithm i.e. Shortest path matrix.
cout<<"All Pairs Shortest Path Matrix :\n";
for(i=0;i<n;i++)
r</pre>
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                             if(i==j && i<n)
                                      G.A[i][j]=0;
                                      G.A[i][j]=INT_MAX;
                           }
                    declared=true;
        else if(declared && line[0] == 'a')
            int w;
           istringstream iss(line);
iss >> token >> i >> j >> w;
G.A[i-1][j-1] = w;
  }
   for(j=0;j<n+dadd;j++)</pre>
                 D[i][j]=G.A[i][j];
  }
/* //'D' weight graph, Before Applying Standard Floyd-warshall Algorithm. cout<<"Parallelized Standard Floyd-Warshall Algorithm :\n\n"; cout<<"Input Graph :\n"; for(1=0;:-n;t++)
                                                                                                               C++ ▼ Tab Width: 8 ▼ Ln 28, Col 12 ▼ INS
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### 4) Results and Discussions



## 5) Parameters to be Considered





#### 6)Conclusions

There is certainly a decrease in computation of Bellman Ford's Algorithm and Floyd Warshall's Algorithm when we use OpenMP +SIMD in the Algorithm as it uses all the 4 CPU's of the Computer as thought of. After parallelising the code was seen to be smoother and the output remained to result to us faster.

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