

Simulink Design Verifier Report

Eas_Integrated/Eas_Subsystem

SOMNATH

Simulink Design Verifier Report: Eas_Integrated/Eas_Subsystem

SOMNATH

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Chapter 1. Summary

Analysis Information.

Model:	Eas_Integrated
Release:	R2021a
Analyzed Subsystem:	Eas_Integrated/Eas_Subsystem
Checksum:	2364402400 1502429244 1830922972 2321936094
Mode:	Property proving
Model Representation:	Cache from 29-Jul-2022 22:13:46
Status:	Completed normally
PreProcessing Time:	7s
Analysis Time:	11s

Objectives Status.

Number of Objectives:	0
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Chapter 2. Analysis Information

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Model Information

File:	Eas_Integrated
Version:	1.54
Time Stamp:	Thu Jul 28 22:48:44 2022
Author:	Admin

Analysis Options

Mode:	PropertyProving
Rebuild Model Representation:	IfChangeIsDetected
Proving Strategy:	Prove
Proof Assumptions:	UseLocalSettings
Test Objectives:	UseLocalSettings
Assertions:	UseLocalSettings
Maximum Analysis Time:	300s
Block Replacement:	off
Parameters Analysis:	off
Include expected output values:	on
Randomize data that do not affect the outcome:	on
Additional analysis to reduce instances of rational approximation:	on
Save Data:	on
Save Harness:	on
Save Report:	off