

**Run Summary**

Pass	Fail	Warning	Not Run	Total
✓ 94	✗ 0	⚠ 16	⠼ 0	110

**Modeling Standards for ISO 26262****Display configuration management data**

Display model configuration and checksum information

NOTE: These checks were performed on a sub-system, but are based on root-level settings.

Model configuration and checksum information

Attribute	Value
Model Version	1.44
Author	Admin
Date	Sun Jul 24 18:53:53 2022
Model Checksum	2666649537 3110180097 414838025 1150892330

**Display model metrics and complexity report**

Display number of elements and name, level, and depth of subsystems for the model or subsystem

**Model metrics information**

Display number of elements for Simulink blocks and Stateflow constructs

**Summary**

Element Type	Count
Import	52
Outport	35
SubSystem	15

**Simulink**

Block Type	Count
Import	52
Outport	35
Constant	32
RelationalOperator	24
SubSystem	15
Logic	13
Product	4
EnablePort	3
Switch	3
Gain	2
LampBlock	2
Sqrt	1

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**Model complexity information**

Display name, level, and depth of subsystems

Maximum Subsystem Depth: 4

#### Subsystem Depth

Subsystem Name	Level	Depth
<a href="#">..../Eas_Subsystem/DPF_Subsystem</a>	1	2
<a href="#">..../DPF_Subsystem/Auto_Regeneration</a>	2	1
<a href="#">..../DPF_Subsystem/Manual_Regn_Subsystem</a>	2	1
<a href="#">..../DPF_Subsystem/Post_Regn_Subsystem</a>	2	1
<a href="#">..../DPF_Subsystem/Regn_Display_Subsystem</a>	2	1
<a href="#">..../DPF_Subsystem/Service_Regeneration</a>	2	1
<a href="#">..../Eas_Subsystem/SCR_Regeneration_Subsystem</a>	1	3
<a href="#">..../Adblue_Injection_System</a>	2	1
<a href="#">..../SCR_Regeneration_Subsystem/Adblue_Level2</a>	2	2
<a href="#">..../Adblue_Level2/Enumerated_Constant</a>	3	1
<a href="#">..../Adblue_Level2/Enumerated_Constant1</a>	3	1
<a href="#">..../Scr_Regeneration_Parameter</a>	2	2
<a href="#">..../Adblue_Level_indication</a>	3	1
<a href="#">..../Adblue_Quality_indication</a>	3	1
<a href="#">..../Scr_temp_indication</a>	3	1

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#### ✓ Check for unconnected objects

Identify unconnected lines, input ports, and output ports in the subsystem

##### Passed

There are no unconnected lines, input ports, and output ports in this subsystem.

#### ☒ 1 High-Integrity Systems    ✓91 ✘0 ⚠2 📈0

#### ☒ 1.1 Simulink    ✓29 ✘0 ⚠2 📈0

#### ✓ Check usage of Abs blocks

Identify Abs blocks that have unreachable code or produce overflows.

##### Passed

No Abs blocks found causing unreachable code or produce overflows.

#### ✓ Check usage of Math Function blocks (rem and reciprocal functions)

Identify Math Function blocks using rem and reciprocal functions that cause non-finite results.

##### Passed

All Math Function blocks in the model use operators appropriately.

#### ✓ Check usage of Sqrt blocks

Identify Sqrt blocks with inputs that can be negative.

##### Passed

No Sqrt blocks found with inputs that can be negative.

#### ✓ Check usage of Math Function blocks (log and log10 functions)

Identify Math Function blocks using log and log10 functions that cause non-finite results.

**Passed**

All Math Function blocks in the model use operators appropriately.

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**✓ Check usage of While Iterator blocks**

Identify While Iterator blocks that do not have a positive value for the maximum number of iterations.

**Passed**

No While Iterator blocks found that might cause infinite loops

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**✓ Check usage of For and While Iterator subsystems**

Identify sample time-dependent blocks in While and For Iterator subsystems.

**Passed**

No sample time-dependent blocks in For or While Iterator subsystems.

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**✓ Check usage of For Iterator blocks**

Identify For Iterator blocks that cause variable loops.

**Passed**

No For Iterator blocks found that cause variable loops.

---

**✓ Check usage of If blocks and If Action Subsystem blocks**

Identify If and If Action Subsystem blocks without else conditions.

**Passed**

No If blocks with questionable configurations or connections were found.

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**✓ Check usage of Switch Case blocks and Switch Case Action Subsystem blocks**

Identify inappropriately used Switch Case blocks and Switch Case Action Subsystem blocks.

**Passed**

No Switch Case blocks with questionable configurations or connections were found.

---

**✓ Check usage of conditionally executed subsystems**

Identify inappropriate blocks in conditionally executed subsystems.

**Passed**

No blocks with improper sample times or asynchronously executed sample-time dependent blocks were found.

---

**✓ Check usage of Merge blocks**

Identify Merge blocks constructs which can lead to ambiguous behavior.

**Passed**

No merge blocks found which can lead to ambiguous behavior.

---

**✓ Check Relational Operator blocks equating floating-point types**

Identify Relational Operator blocks that equate floating-point types.

**Passed**

No Relational Operator blocks found that equate floating-point types.

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**⚠ Check usage of Relational Operator blocks**

Identify Relational Operator blocks that operate on different data types or have a non-boolean output.

#### Warning

The following Relational Operator blocks operate on different data types or have a non-boolean output:

- [..../Eas\\_Subsystem/DPF\\_Subsystem/Auto\\_Regeneration/Relational\\_Operator1](#)

#### Recommended Action

Ensure the block has same data types for all inputs.

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### ✓ Check usage of Logical Operator blocks

Identify Logical Operator blocks that operate on non-boolean data types.

#### Passed

No Logical Operator blocks found that operate on non-boolean data types.

---

### ✓ Check usage of bit operation blocks

Identify bit operation blocks with signed data types as inputs.

#### Passed

No bit operation blocks found with signed data types as inputs.

---

### ✓ Check for blocks not recommended for C/C++ production code deployment

Identify blocks not supported by code generation or not recommended for C/C++ production code deployment.

#### Passed

No blocks found which are not recommended for C/C++ production code deployment.

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### ✓ Check for inconsistent vector indexing methods

Identify inconsistent usage of vector indexing methods across the model or subsystem.

#### Passed

No blocks found using inconsistent indexing modes.

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### ✓ Check data types for blocks with index signals

Identify blocks with index signals that have data types other than integers or enums.

#### Passed

No blocks or charts found with index signals or variables that have data types other than integer or enums.

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### ✓ Check usage of variant blocks

Check variant block settings that might result in code that doesn't trace back to requirements.

#### Passed

There are no variant blocks that have "Generate preprocessor conditionals" active.

---

### ✓ Check usage of lookup table blocks

Check for Lookup Table blocks, Prelookup blocks and Interpolation blocks that do not generate out-of-range checking code.

#### Passed

No lookup table blocks found to not generate out-of-range checking code.

---

### ✓ Check usage of Signal Routing blocks

Identify usage of Signal Routing blocks in Simulink that might impact safety.

#### Passed

No Switch blocks that might generate code with inequality operations ( $\sim=$ ) in expressions where at least one side of the expression is a floating-point variable or constant were found.

---

#### ✓ Check for root Imports with missing properties

Identify Import blocks in the top-level of the model with missing or inherited sample times, data types, or port dimensions. Import block properties are specified with block parameters or Simulink signal data objects that explicitly resolve to the connected signal lines.

##### Passed

There are no Import blocks in the top-level of the model with missing or inherited sample times, data types, or port dimensions

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#### ⚠ Check for root Imports with missing range definitions

Identify root-level Import blocks with missing or erroneous minimum or maximum values. Import block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

##### Warning

The following Import blocks and Simulink signal objects have missing or erroneous range definitions:

- [Eas\\_Integrated/Coolant\\_Temp](#)
- [Eas\\_Integrated/Crank\\_Angle](#)
- [Eas\\_Integrated/DPF\\_Temp\\_Auto](#)
- [Eas\\_Integrated/DPF\\_Temp\\_Manual](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Auto](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Manual](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Service](#)
- [Eas\\_Integrated/Vehicle\\_Speed](#)
- [Eas\\_Integrated/rpm](#)

##### Recommended Action

Model contains Imports with numeric data types that have missing range parameters (minimum and/or maximum). For the listed Import blocks and Simulink signal objects, specify scalar minimum and maximum parameters.

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Identify root-level Import blocks with missing or erroneous minimum or maximum values. Import block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

##### Warning

The following Import blocks and Simulink signal objects have missing or erroneous range definitions:

- [Eas\\_Integrated/Coolant\\_Temp](#)
- [Eas\\_Integrated/Crank\\_Angle](#)
- [Eas\\_Integrated/DPF\\_Temp\\_Auto](#)
- [Eas\\_Integrated/DPF\\_Temp\\_Manual](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Auto](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Manual](#)
- [Eas\\_Integrated/Soot\\_Deposition\\_Service](#)
- [Eas\\_Integrated/Vehicle\\_Speed](#)
- [Eas\\_Integrated/rpm](#)

##### Recommended Action

Model contains Import blocks or Simulink signal objects with inherited data type. For the Import blocks or Simulink signal objects, select a build-in, enum, Simulink.Bus, Simulink.NumericType or a Simulink.AliasType data type.

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#### ✓ Check for root Outports with missing range definitions

Identify root-level Outport blocks with missing or erroneous minimum or maximum values. Outport block minimum and maximum values are specified with block parameters or Simulink signal objects that explicitly resolve to the connected signal lines.

##### Passed

There are no missing or erroneous Outport range properties at the model root level.

Note: Root Outports with inherited data types are not analyzed by this check.

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#### ✓ Check usage of Reciprocal Sqrt blocks

Identify Reciprocal Sqrt blocks with inputs that can go zero or negative.

### **Passed**

No Reciprocal Sqrt blocks found with inputs that can go zero or negative.

---

#### **✓ Check usage of Assignment blocks**

Identify Assignment blocks whose array fields are not initialized.

### **Passed**

All Assignment blocks are configured with block parameter "Action if any output element is not assigned" set to Warning or Error.

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#### **✓ Check global variables in graphical functions**

Identify expressions that both read and write to the same global data.

### **Passed**

No expressions found that both read and write to the same global data.

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#### **✓ Check usage of Gain blocks**

Identify Gain blocks with value which resolves to 1.

### **Passed**

No Gain blocks found with value which resolves to 1.

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#### **✓ Check for length of user-defined object names**

Identify user-defined object names with length greater than threshold.

### **Passed**

No Subsystem blocks found with function name length greater than Maximum identifier length. There are no data objects with names having length greater than Maximum identifier length.

---

#### **✓ Check data type of loop control variables**

Identify loop control variables using non-integer data types.

### **Passed**

No For Iterator blocks or MATLAB Function blocks found using non-integer data type for loop control counter variable.

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#### **✓ Check for divide-by-zero calculations**

Identify blocks that can result in divide-by-zero calculations.

### **Passed**

No blocks found that can result in divide-by-zero calculations.

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▫ **1.2 Stateflow**    15 0 0 0

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#### **✓ Check state machine type of Stateflow charts**

Identify Stateflow Charts whose State Machine Type differs from the type set in the Model Advisor Configuration Editor.

### **Passed**

No Stateflow Charts found that deviate from recommended state machine type.

---

#### **✓ Check Stateflow charts for ordering of states and transitions**

Identify Stateflow charts that do not use explicit ordering of parallel states and transitions.

### **Passed**

No Stateflow Charts found that deviate from recommended state/transition execution order settings.

---

## Check usage of bitwise operations in Stateflow charts

Identify usage of signed data type operands to bitwise operations in Stateflow charts.

**Passed**

No Stateflow objects found that use signed data type operands with bitwise operations.

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## Check usage of recursions

**NOTE:** This check can only be run from root level of a model.

Identify usage of recursive behavior.

**Passed**

No entities involved in recursions were found.

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## Check for Strong Data Typing with Simulink I/O

Verify configuration settings for strong data typing on the boundaries between Simulink and Stateflow.

**Passed**

No Stateflow charts found that set 'Use Strong Data Typing with Simulink I/O' to off.

---

## Check Stateflow debugging options

Identify whether Stateflow debugging options are set appropriately.

**Passed**

All Stateflow debugging options are set appropriately.

---

## Check Stateflow charts for transition paths that cross parallel state boundaries

Identify transition paths that cross parallel state boundaries in Stateflow charts.

**Passed**

No transition paths crossing parallel state boundaries were found in Stateflow charts.

---

## Check for inappropriate use of transition paths

Identify transition paths that go into and out of a state without ending on a substate.

**Passed**

No transition paths found that go into and out of a state without ending on a substate.

---

## Check Stateflow charts for strong data typing

Identify expressions with variables and parameters of different data types in Stateflow objects.

**Passed**

No expressions were found with variables and parameters of different data types.

---

## Check naming of ports in Stateflow charts

Identify mismatches between names of Stateflow ports and associated signals.

**Passed**

There are no name mismatches between Stateflow ports and associated signals

---

## Check scoping of Stateflow data objects

Identify Stateflow data objects with local scope that are not scoped at the chart level or below.

**Passed**

All Stateflow data objects are properly scoped.

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## Check Stateflow charts for uniquely defined data objects

Identify local data identifiers that are defined in multiple scopes within a chart.

**Passed**

No Stateflow data identifiers found to be defined in multiple scopes.

---

## Check usage of shift operations for Stateflow data

Identify usage of Stateflow bit-shifting operations that might impact safety.

**Passed**

There are no Stateflow bit-shifting operations greater than the bit-width of the input or output type.

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## Check assignment operations in Stateflow charts

Identify assignment operations in Stateflow objects which cast integer and fixed-point calculations to wider datatype.

**Passed**

No assignment operations were found which cast integer and fixed-point calculations to wider datatype.

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## Check Stateflow charts for unary operators

Identify unary minus operators on unsigned data types in Stateflow objects.

**Passed**

No unary minus operations on unsigned data types were found in Stateflow objects.

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 **1.3 MATLAB**     10  0  0 

## Check usage of standardized MATLAB function headers

Identify usage of standardized function headers in MATLAB function.

**Passed**

No MATLAB function blocks found without standardized function headers.

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## Check for MATLAB Function interfaces with inherited properties

Identify MATLAB Functions that have inputs, outputs, or parameters with inherited complexity or data type properties.

**Passed**

No MATLAB Function interfaces with inherited complexity or data type properties found.

---

## Check MATLAB Function metrics

Identify MATLAB Functions that violate code and complexity metrics.

**Passed**

No MATLAB Function blocks found that violate code and complexity metrics.

---

## Check MATLAB Code Analyzer messages

Check MATLAB functions for %#codegen directive, MATLAB Code Analyzer messages, and justification message IDs.

**Passed**

No MATLAB Function blocks found with Code Analyzer messages, missing %#codegen directive or inappropriate usage of justification message IDs.

---

## Check if/elseif/else patterns in MATLAB Function blocks

Identify if/elseif/else patterns without appropriate else conditions in embedded MATLAB code.

### Passed

No inappropriate if/elseif/else patterns found.

## ✓ Check switch statements in MATLAB Function blocks

Identify inappropriately used switch statements in embedded MATLAB code.

### Passed

No inappropriately used switch statements found.

## ✓ Check usage of relational operators in MATLAB Function blocks

Identify relational operators operating on operands of different data types in MATLAB Function blocks.

### Passed

No relational operators found operating on operands of different data types.

## ✓ Check usage of equality operators in MATLAB Function blocks

Identify equality operators used with floating-point operands in MATLAB Function blocks.

### Passed

No equality operators found operating on floating-point operands.

## ✓ Check usage of logical operators and functions in MATLAB Function blocks

Identify logical operators and functions operating on operands with numerical data types.

### Passed

No logical operators or functions found operating on operands with numerical data types.

## ✓ Check type and size of condition expressions

Identify condition expressions which are not logical scalars.

### Passed

No condition expressions found which are not logical scalars.

## ✗ 1.4 Configuration

✓32 ✗0 ⚠0 ⏱0

## ✓ Check safety-related diagnostic settings for data store memory

Check diagnostic settings in the model configuration that apply to data store memory and might impact safety.

### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Detect read before write (ReadBeforeWriteMsg)</a>	EnableAllAsError	EnableAllAsError
Pass	<a href="#">Detect write after read (WriteAfterReadMsg)</a>	EnableAllAsError	EnableAllAsError
Pass	<a href="#">Detect write after write (WriteAfterWriteMsg)</a>	EnableAllAsError	EnableAllAsError
Pass	<a href="#">Multitask data store (MultiTaskDSMMsg)</a>	error	error
Pass	<a href="#">Duplicate data store names (UniqueDataStoreMsg)</a>	error	error

## ✓ Check safety-related diagnostic settings for saving

Check diagnostic settings in the model configuration that apply to saving model files.

### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Block diagram contains disabled library links (SaveWithDisabledLinksMsg)</a>	error	error
Pass	<a href="#">Block diagram contains parameterized library links (SaveWithParameterizedLinksMsg)</a>	error	error

## ✓ Check safety-related model referencing settings

Check model referencing settings in the model configuration that might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Rebuild (UpdateModelReferenceTargets)</a>	IfOutOfDateOrStructuralChange	AssumeUpToDate, IfOutOfDateOrStructuralChange
Pass	<a href="#">Pass fixed-size scalar root inputs by value for code generation (ModelReferencePassRootInputsByReference)</a> *	on	on
Pass	<a href="#">Minimize algebraic loop occurrences (ModelReferenceMinAlgLoopOccurrences)</a>	off	off

### Recommended Action

\* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

## ✓ Check safety-related code generation settings for comments

Check code generation settings in the model configuration that apply comments and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	<a href="#">Include comments (GenerateComments)</a>	on	on	
Pass	<a href="#">Simulink block comments (SimulinkBlockComments)</a>	on	on	GenerateComments
Pass	<a href="#">Show eliminated blocks (ShowEliminatedStatement)</a>	on	on	GenerateComments
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	
Pass	<a href="#">Verbose comments for 'Model default' storage class (ForceParamTrailComments)</a>	on	on	GenerateComments
D - Pass	<a href="#">Include comments (GenerateComments)</a>	on	on	
Pass	<a href="#">Requirements in block comments (ReqsInCode)</a>	on	on	SystemTargetFile, GenerateComments

## ✓ Check safety-related code generation interface settings

Check code generation interface settings in the model configuration that might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	<a href="#">non-finite numbers (SupportNonFinite)</a>	off	off	

Pass	<a href="#">absolute time (SupportAbsoluteTime)</a>	off	off	SystemTargetFile
Pass	<a href="#">continuous time (SupportContinuousTime)</a>	off	off	SystemTargetFile
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	
Pass	<a href="#">Support non-inlined S-functions (SupportNonInlinedSFCns)</a>	off	off	SystemTargetFile
Pass	<a href="#">Classic call interface (GRTInterface)</a>	off	off	
Pass	<a href="#">Single output/update function (CombineOutputUpdateFcns)</a>	on	on	
Pass	<a href="#">Terminate function required (IncludeMdlTerminateFcn)</a>	off	off	SystemTargetFile
Pass	<a href="#">Remove error status field in real-time model data structure (SuppressErrorStatus)</a>	on	on	SystemTargetFile
Pass	<a href="#">MAT-file logging (MatFileLogging)</a>	off	off	

#### ✓ Check safety-related solver settings for simulation time

Identify if the model Start time is set to 0 and Stop time is less than the Application Life Span.

**Passed**

No issues found with solver settings for simulation time.

#### ✓ Check safety-related solver settings for solver options

Check solver settings in the model configuration that apply to solvers and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Type (SolverType)</a>	Fixed-step	Fixed-step
Pass	<a href="#">Solver (SolverName)</a>	FixedStepDiscrete	FixedStepDiscrete

#### ✓ Check safety-related solver settings for tasking and sample-time

Check solver settings in the model configuration that apply to tasking and sample-time constraints and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Not Recommended Values
Pass	<a href="#">Automatically handle rate transition for data transfer (AutolnsertRateTranBlk)</a>	off	on

#### ✓ Check safety-related diagnostic settings for solvers

Check diagnostic settings in the model configuration that apply to solvers and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Algebraic loop (AlgebraicLoopMsg)</a>	error	error
Pass	<a href="#">Minimize algebraic loop (ArtificialAlgebraicLoopMsg)</a>	error	error
Pass	<a href="#">Block priority violation (BlockPriorityViolationMsg)</a>	error	error
Pass	<a href="#">Automatic solver parameter selection (SolverPrmCheckMsg)</a>	error	error
Pass	<a href="#">State name clash (StateNameClashWarn)</a>	warning	warning

#### ✓ Check safety-related diagnostic settings for sample time

Check diagnostic settings in the model configuration that apply to sample time and might impact safety.

#### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Source block specifies -1 sample time (InheritedTsInSrcMsg)</a>	error	error
Pass	<a href="#">Multitask data transfer (MultiTaskRateTransMsg)</a>	error	error
Pass	<a href="#">Multitask conditionally executed subsystem (MultiTaskCondExecSysMsg)</a>	error	error
Pass	<a href="#">Enforce sample times specified by Signal Specification blocks (SigSpecEnsureSampleTimeMsg)</a>	error	error
Pass	<a href="#">Single task data transfer (SingleTaskRateTransMsg)</a>	error	error
Pass	<a href="#">Tasks with equal priority (TasksWithSamePriorityMsg)</a>	error	error
Pass	<a href="#">Unspecified inheritability of sample time (UnknownTslnhSupMsg)</a>	error	error

### ✓ Check safety-related optimization settings for logic signals

Check optimization settings in the model configuration that apply to logic signals and might impact safety.

#### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Implement logic signals as Boolean data (vs. double) (BooleanDataType)</a>	on	on

### ✓ Check safety-related block reduction optimization settings

Check block reduction optimization settings in the model configuration that might impact safety.

#### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Block reduction (BlockReduction)</a>	off	off

### ✓ Check safety-related code generation settings for code style

Check code generation settings in the model configuration that apply to code style and might impact safety.

#### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	<a href="#">Parentheses level (ParenthesesLevel)</a>	Maximum	Maximum	SystemTargetFile
Pass	<a href="#">Preserve operand order in expression (PreserveExpressionOrder)</a>	on	on	SystemTargetFile
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	

### ✓ Check safety-related optimization settings for application lifespan

Check optimization settings in the model configuration that apply to application lifespan and might impact safety.

#### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Application lifespan (days) (LifeSpan)</a>	inf	Inf

#### ✓ Check safety-related code generation identifier settings

Check code generation identifier settings in the model configuration that might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Not Recommended Values	Prerequisites
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target		
Pass	<a href="#">Minimum mangle length (MangleLength)</a>	4		1, 2, 3	SystemTargetFile

#### ✓ Check safety-related optimization settings for data initialization

Check optimization settings in the model configuration that apply to data initialization and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	
D - Pass	<a href="#">Code interface packaging (CodeInterfacePackaging)</a>	Nonreusable function	Reusable function, Nonreusable function	
Pass	<a href="#">Remove root level I/O zero initialization (ZeroExternalMemoryAtStartup)*</a>	on	on	SystemTargetFile, CodeInterfacePackaging
Pass	<a href="#">Remove internal data zero initialization (ZeroInternalMemoryAtStartup)*</a>	on	on	SystemTargetFile, CodeInterfacePackaging

#### Recommended Action

\* The Command-Line values provided in the table are reverse of the settings in the Configuration Parameters Dialog. Therefore, 'on' in the Command-Line corresponds to an "Off" setting in the dialog, and 'off' in the Command-Line corresponds to an "On" setting in the dialog.

#### ✓ Check safety-related optimization settings for data type conversions

Check optimization settings in the model configuration that apply to data type conversions and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Remove code from floating-point to integer conversions that wraps out-of-range values (EfficientFloat2IntCast)</a>	on	on

#### ✓ Check safety-related optimization settings for division arithmetic exceptions

Check optimization settings in the model configuration that apply to division arithmetic exceptions and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	
Pass	<a href="#">Remove code that protects against division arithmetic exceptions (NoFixptDivByZeroProtection)</a>	off	off	SystemTargetFile

#### ✓ Check safety-related optimization settings for specified minimum and maximum values

Check optimization settings in the model configuration that apply to specified minimum and maximum values and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	<a href="#">Optimize using the specified minimum and maximum values (UseSpecifiedMinMax)</a>	off	off	SystemTargetFile
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	

#### ✓ Check safety-related settings for hardware implementation

##### Check 'Byte ordering' and 'Signed integer division rounds to' parameters

Identify inconsistencies or underspecification of hardware attributes that can lead to incorrect and inefficient generated code.

**Passed**

Target specification is consistent.

##### Check whether 'Production hardware' and 'Test hardware' match

Search for 'Test hardware is the same as production hardware' in the Configuration Parameters dialog box and check if it is selected. If it is cleared, identify whether target specifications match.

**Passed**

'Test hardware is the same as production hardware' is selected or is cleared and the target specifications match.

#### ✓ Check safety-related diagnostic settings for compatibility

Check diagnostic settings in the model configuration that affect compatibility and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">S-function upgrades needed (SFcnCompatibilityMsg)</a>	error	error

#### ✓ Check safety-related diagnostic settings for parameters

Check diagnostic settings in the model configuration that apply to parameters and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Detect downcast (ParameterDowncastMsg)</a>	error	error
Pass	<a href="#">Detect underflow (ParameterUnderflowMsg)</a>	error	error

Pass	<a href="#">Detect overflow (ParameterOverflowMsg)</a>	error	error
Pass	<a href="#">Detect precision loss (ParameterPrecisionLossMsg)</a>	error	error
Pass	<a href="#">Detect loss of tunability (ParameterTunabilityLossMsg)</a>	error	error

#### ✓ Check safety-related diagnostic settings for Merge blocks

Check diagnostic settings in the model configuration that apply to Merge blocks and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Detect multiple driving blocks executing at the same time step (MergeDetectMultiDrivingBlocksExec)</a>	error	error

#### ✓ Check safety-related diagnostic settings for model initialization

Check diagnostic settings in the model configuration that affect model initialization and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Underspecified initialization detection (UnderspecifiedInitializationDetection)</a>	Simplified	Simplified

#### ✓ Check safety-related diagnostic settings for data used for debugging

Check diagnostic settings in the model configuration that apply to data used for debugging and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Model Verification block enabling (AssertControl)</a>	DisableAll	DisableAll

#### ✓ Check safety-related diagnostic settings for signal connectivity

Check diagnostic settings in the model configuration that apply to signal connectivity and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Signal label mismatch (SignalLabelMismatchMsg)</a>	error	error
Pass	<a href="#">Unconnected block input ports (UnconnectedInputMsg)</a>	error	error
Pass	<a href="#">Unconnected block output ports (UnconnectedOutputMsg)</a>	error	error
Pass	<a href="#">Unconnected line (UnconnectedLineMsg)</a>	error	error

#### ✓ Check safety-related diagnostic settings for bus connectivity

Check diagnostic settings in the model configuration that apply to bus connectivity and might impact safety.

##### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Unspecified bus object at root Outport block (RootOutportRequireBusObject)</a>	error	error
Pass	<a href="#">Element name mismatch (BusObjectLabelMismatch)</a>	error	error
Pass	<a href="#">Bus signal treated as vector (StrictBusMsg)</a>	ErrorOnBusTreatedAsVector	ErrorOnBusTreatedAsVector
Pass	<a href="#">Non-bus signals treated as bus signals (NonBusSignalsTreatedAsBus)</a>	error	error

#### ✓ Check safety-related diagnostic settings that apply to function-call connectivity

Check diagnostic settings in the model configuration that apply to function-call connectivity and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">InvalidFcnCallConnMsg</a>	error	error
Pass	<a href="#">Context-dependent inputs (FcnCallInpInsideContextMsg)</a>	error	error

#### ✓ Check safety-related diagnostic settings for type conversions

Check diagnostic settings in the model configuration that apply to type conversions and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Unnecessary type conversions (UnnecessaryDatatypeConvMsg)</a>	warning	warning
Pass	<a href="#">Vector/matrix block input conversion (VectorMatrixConversionMsg)</a>	error	error
Pass	<a href="#">32-bit integer to single precision float conversion (Int32ToFloatConvMsg)</a>	warning	warning

#### ✓ Check safety-related diagnostic settings for model referencing

Check diagnostic settings in the model configuration that apply to model referencing and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Port and parameter mismatch (ModelReferenceIOMismatchMessage)</a>	error	error
Pass	<a href="#">Invalid root Inport/Outport block connection (ModelReferenceIMsg)</a>	error	error
Pass	<a href="#">Unsupported data logging (ModelReferenceDataLoggingMessage)</a>	error	error

#### ✓ Check safety-related diagnostic settings for Stateflow

Check diagnostic settings in the model configuration that apply to Stateflow and might impact safety.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Unexpected backtracking (SFUnexpectedBacktrackingDiag)</a>	error	error
Pass	<a href="#">Invalid input data access in chart initialization</a>	error	error

	<a href="#">(SFInvalidInputDataAccessInChartInitDiag)</a>		
Pass	<a href="#">No unconditional default transitions (SFNoUnconditionalDefaultTransitionDiag)</a>	error	error
Pass	<a href="#">Transition outside natural parent (SFTransitionOutsideNaturalParentDiag)</a>	error	error
Pass	<a href="#">Unreachable execution path (SFUnreachableExecutionPathDiag)</a>	error	error
Pass	<a href="#">Undirected event broadcasts (SFUndirectedBroadcastEventsDiag)</a>	error	error
Pass	<a href="#">Transition action specified before condition action (SFTransitionActionBeforeConditionDiag)</a>	error	error
Pass	<a href="#">Read-before-write to output in Moore chart (SFOutputUsedAsStateInMooreChartDiag)</a>	error	error
Pass	<a href="#">Absolute time temporal value shorter than sampling_period (SFTemporalDelaySmallerThanSampleTimeDiag)</a>	error	error
Pass	<a href="#">Self-transition on leaf state (SFSelfTransitionDiag)</a>	error	error
Pass	<a href="#">'Execute-at-initialization' disabled in presence of input events (SFFexecutionAtInitializationDiag)</a>	error	error
Pass	<a href="#">Use of machine-parented data instead of Data Store Memory. (SFMachineParentedDataDiag)</a>	error	error

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## ✓ Check safety-related diagnostic settings for signal data

Check diagnostic settings in the model configuration that apply to signal data and might impact safety.

### Passed

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values
Pass	<a href="#">Signal resolution (SignalResolutionControl)</a>	UseLocalSettings	None, UseLocalSettings
Pass	<a href="#">Division by singular matrix (CheckMatrixSingularityMsg)</a>	error	error
Pass	<a href="#">Underspecified data types (UnderSpecifiedDataTypeMsg)</a>	error	error
Pass	<a href="#">Wrap on overflow (IntegerOverflowMsg)</a>	error	error
Pass	<a href="#">Saturate on overflow (IntegerSaturationMsg)</a>	error	error
Pass	<a href="#">Inf or NaN block output (SignalInfNanChecking)</a>	error	error
Pass	<a href="#">"rt" prefix for identifiers (RTPrefix)</a>	error	error
Pass	<a href="#">Simulation range checking (SignalRangeChecking)</a>	error	error

☒ 1.5 Naming    2 0 0 0

## ✓ Check model file name

Identify inappropriate characters and length issues in model file name.

### Passed

No issues found with model file name.

## ✓ Check model object names

Identify invalid names of following model objects (first invalid name fragment is highlighted):

- Blocks
- Signals
- Parameters
- Buses
- Stateflow elements

### Passed

No invalid names of model objects found

☒ 1.6 Requirements    1 0 0 0

## Check for model elements that do not link to requirements

Check for model elements that do not link to a requirements document.

**Passed**

No model elements found without links to requirements.

**1.7 Code**    2 0 0 0

## Check for blocks not recommended for MISRA C:2012

Identify blocks that are not recommended for MISRA C:2012 compliant code generation.

**Passed**

None of the blocks are defined as "not recommended" for MISRA C:2012 compliant code generation.

## Check configuration parameters for MISRA C:2012

Identify configuration parameters that might impact MISRA C:2012 compliant code generation.

**Passed**

All constraints on model configuration parameters have been met.

Status	Parameter	Current Value	Recommended Values	Prerequisites
Pass	<a href="#">Model Verification block enabling (AssertControl)</a>	DisableAll	DisableAll	
D - Pass	<a href="#">Shared code placement (UtilityFuncGeneration)</a>	Shared location	Shared location	
Pass	<a href="#">Generate shared constants (GenerateSharedConstants)</a>	off	off	UtilityFuncGeneration
D - Pass	<a href="#">System target file (SystemTargetFile)</a>	ERT based target	ERT based target	
Pass	<a href="#">continuous time (SupportContinuousTime)</a>	off	off	SystemTargetFile
Pass	<a href="#">Support non-inlined S-functions (SupportNonInlinedSFcn)</a>	off	off	SystemTargetFile
Pass	<a href="#">MAT-file logging (MatFileLogging)</a>	off	off	
Pass	<a href="#">Code replacement library (CodeReplacementLibrary)</a>	None	None, AUTOSAR 4.0	
Pass	<a href="#">Parentheses level (ParenthesesLevel)</a>	Maximum	Maximum	SystemTargetFile
Pass	<a href="#">Casting modes (CastingMode)</a>	Standards	Standards	SystemTargetFile
Pass	<a href="#">System-generated identifiers (InternalIdentifier)</a>	Shortened	Shortened	SystemTargetFile
Pass	<a href="#">Signed integer division rounds to (ProdIntDivRoundTo)</a>	Zero	Zero, Floor	
Pass	<a href="#">Use division for fixed-point net slope computation (UseDivisionForNetSlopeComputation)</a>	on	on, UseDivisionForReciprocalsOfIntegersOnly	
Pass	<a href="#">Replace multiplications by powers of two with signed bitwise shifts (EnableSignedLeftShifts)</a>	off	off	SystemTargetFile
Pass	<a href="#">Allow right shifts on signed integers (EnableSignedRightShifts)</a>	off	off	SystemTargetFile
Pass	<a href="#">Wrap on overflow (IntegerOverflowMsg)</a>	error	warning, error	
Pass	<a href="#">Inf or NaN block output (SignalInfNanChecking)</a>	error	warning, error	
Pass	<a href="#">Dynamic memory allocation in MATLAB functions (MATLABDynamicMemAlloc)</a>	off	off	
Pass	<a href="#">External mode (ExtMode)</a>	off	off	
Pass	<a href="#">Undirected event broadcasts</a>	error	error	

	<a href="#">(SFUndirectedBroadcastEventsDiag)</a>			
Pass	<a href="#">Compile-time recursion limit for MATLAB functions (CompileTimeRecursionLimit)</a>	0	0	
Pass	<a href="#">Enable run-time recursion for MATLAB functions (EnableRuntimeRecursion)</a>	off	off	
D - Pass	<a href="#">Include comments (GenerateComments)</a>	on	on	
Pass	<a href="#">MATLAB user comments (MATLABFcnDesc)</a>	on	on	GenerateComments, SystemTargetFile

[^ Less](#)

✉ 2 Bug Reports    0 0 14 0

## ⚠ Display bug reports for Embedded Coder

Display bug reports for Embedded Coder (R2021a) available at <https://www.mathworks.com/support/bugreports>.  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 97 Embedded Coder bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2574138</a>	Embedded Coder - Missing Rte prefixes for AUTOSAR.Parameters with SystemConstant storage class	19 Jul 2022
<a href="#">2646742</a>	Embedded Coder - <b>Incorrect Code Generation</b> : C++ code generation might produce an incorrect initial value setting for a root outport when using function prototype control	13 Jul 2022
<a href="#">2534839</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect results might occur for a model using S-function block and symbolic dimensions	13 Jul 2022
<a href="#">2757161</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect code generated when a parameter or signal is an array of buses and specifies preservation of dimensions	20 Jun 2022
<a href="#">2733984</a>	Embedded Coder - Nominal casting mode might remove unnecessary typecasts leading to warnings on compilers that expect explicit typecasts	17 Jun 2022
<a href="#">2695510</a>	Embedded Coder - Argument was not found or found more than 2 hits or Unexpected number of elements in GraphicallIndex error from SIL/PIL simulations of AUTOSAR model	16 Jun 2022
<a href="#">2587940</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Generated code might be incorrect for a model with Product of Elements block	16 Jun 2022
<a href="#">2552611</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Embedded Coder might generate incorrect code with Optimize global data access set to Minimize global data access	16 Jun 2022
<a href="#">2570544</a>	Embedded Coder - A2L and ARXML generation might not be supported for models with lookup tables and breakpoint parameters which are fed through input ports	13 Jun 2022
<a href="#">2660030</a>	Embedded Coder - C++ and AUTOSAR code generation may not reflect Shared utilities identifier format setting	10 Jun 2022
<a href="#">2729318</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect code generated when Model block, Vector Concatenate block, and Root Outport block are connected in a series and the root outport maps to an access function storage class	31 May 2022
<a href="#">2708440</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect C code generated when function-call subsystem writes to its output conditionally.	27 May 2022
<a href="#">2710248</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Generated code might be incorrect for AUTOSAR model with a multidimensional port parameter and ArrayLayout set to Row-major	23 May 2022
<a href="#">2699306</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Generated code might be incorrect for models containing If Action subsystem connected to Memory block	16 May 2022
<a href="#">2438729</a>	Embedded Coder - <b>Incorrect Code Generation</b> : MATLAB Function block containing sub-function with in-place specification on inputs with variable dimensions might result in incorrect generated code	10 May 2022
<a href="#">2702616</a>	Embedded Coder - Default mappings override individual mappings for model reference outputs if both storage classes are built-in or predefined	09 May 2022
<a href="#">2621150</a>	Embedded Coder - Using a Data Store Read block with a GetSet storage class that feeds into a Prelookup block or Interpolation Using Prelookup block might generate extraneous set function calls	27 Apr 2022
<a href="#">2486443</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Embedded Coder might not initialize an exported or imported signal with a constant value	19 Apr 2022
<a href="#">2519041</a>	Embedded Coder - SIL might fail when model contains Direct Lookup Table (n-D) block with input table data as a Constant block input having parameter	13 Apr 2022

<a href="#">2702128</a>	Embedded Coder - Code generation fails when Embedded Coder Dictionary loads custom package	11 Apr 2022
<a href="#">2527763</a>	Embedded Coder - Data Type Propagation block does not support symbolic dimensions	06 Apr 2022
<a href="#">2641368</a>	Embedded Coder - Code generation might fail for C Caller or C Function blocks with input argument of type const char*	04 Apr 2022
<a href="#">2625169</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Simulation result might be incorrect for a MATLAB Function block inside a For Each subsystem	04 Apr 2022
<a href="#">2609990</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Update to Stateflow output may generate incorrect code	04 Apr 2022
<a href="#">2526774</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect code generated when model parameter argument uses a storage class that sets Data Initialization to Auto	30 Mar 2022
<a href="#">2696163</a>	Embedded Coder - Unable to compile generated code when a Simulink.Parameter object with a non-Auto storage class has the value set to a parameter expression that contains an array	24 Mar 2022
<a href="#">2640026</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results are possible for a model containing an Enabled subsystem that has an Integrator block	18 Mar 2022
<a href="#">2574539</a>	Embedded Coder - Assertion when generating code from a Simulink model with dynamically indexed array elements	15 Mar 2022
<a href="#">2657499</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur when a Matrix Concatenate block is connected to a Model block	14 Mar 2022
<a href="#">2667604</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model containing a Merge block and a Unit Delay block or signals specified to have a Reusable custom storage class	14 Mar 2022
<a href="#">2438077</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect behavior for models that contain a Rate Transition block that is not configured to ensure data integrity	09 Mar 2022
<a href="#">2539845</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model consisting of cascading Hit Crossing and Unit Delay blocks	09 Mar 2022
<a href="#">2536586</a>	Embedded Coder - Code replacement for MATLAB Fast Fourier transform functions might cause build error or produce incorrect code replacement report	09 Mar 2022
<a href="#">2538453</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model containing an Enabled or a Triggered Subsystem block that contains a Unit Delay block	09 Mar 2022
<a href="#">2572396</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results are possible for a model containing a Merge block that feeds into a Unit Delay block	09 Mar 2022
<a href="#">2582989</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect answer may be generated with models that have blocks with long parameter names	09 Mar 2022
<a href="#">2394053</a>	Embedded Coder - <b>Incorrect Code Generation:</b> MATLAB Function block containing sub-function with in-place specification on inputs with variable dimensions might result in incorrect code	09 Mar 2022
<a href="#">2538217</a>	Embedded Coder - MATLAB might crash for models with Code Mappings	09 Mar 2022
<a href="#">2600086</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Wrong answer for model with Outports using GetSet or AccessFunction storage class and a referenced model containing a Function-Call Subsystem block	09 Mar 2022
<a href="#">2542017</a>	Embedded Coder - Function Caller block does not call scoped Simulink functions in SIL/PIL simulations	09 Mar 2022
<a href="#">2602635</a>	Embedded Coder - Generated code might be incorrect for Terminate Function block connected to root Outport block	09 Mar 2022
<a href="#">2601019</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Code generated for Bit-wise OR operation between negative integer and Boolean variable might produce incorrect answers	09 Mar 2022
<a href="#">2598522</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model containing a Lookup Table block having symbolic variable with negative sign	09 Mar 2022
<a href="#">2602060</a>	Embedded Coder - Different code might be generated when you change the code generation order of models that use the same reusable library subsystem	09 Mar 2022
<a href="#">2592617</a>	Embedded Coder - AUTOSAR models having Simulink signals or parameters in the base workspace that use storage classes might generate uncompliable code.	09 Mar 2022
<a href="#">2588597</a>	Embedded Coder - MATLAB Function block with native MATLAB functions that have matrix inputs may cause failures	07 Feb 2022
<a href="#">2636948</a>	Embedded Coder - Incorrect simulation results in rapid accelerator mode for a model containing a Function-Call Subsystem block	20 Jan 2022
<a href="#">2371665</a>	Embedded Coder - Reset function in model reference generated code has incorrect signature	03 Dec 2021
<a href="#">2454249</a>	Embedded Coder - Atomic subsystem SIL/PIL simulations do not support GetSet storage class	22 Sep 2021
<a href="#">2417029</a>	Embedded Coder - Model in .mdl format opened in 21a with saved C++ mapping may issue warnings on load or lose mapping specifications	22 Sep 2021
<a href="#">2398710</a>	Embedded Coder - Model with inactive caller functions produces SIL/PIL simulation error	22 Sep 2021
<a href="#">2438057</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model containing Data Store Memory and Model blocks	22 Sep 2021
<a href="#">2436639</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur when the output of a	22 Sep 2021

	Stateflow chart inherits properties from a signal object	2021
<a href="#">2405125</a>	Embedded Coder - Incorrect constraints might be in the generated code for a model that uses constants in symbolic expression	22 Sep 2021
<a href="#">2451129</a>	Embedded Coder - Invalid ARXML exported when AUTOSAR mapping specifies C type qualifiers for static or constant memory elements typed by data types of category TYPE_REFERENCE	22 Sep 2021
<a href="#">2424554</a>	Embedded Coder - SIL simulation for AUTOSAR model with imported system constants might produce different results from Normal mode or might not return data	22 Sep 2021
<a href="#">2436633</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model consisting of cascading Unit Delay and Probe blocks	22 Sep 2021
<a href="#">2418827</a>	Embedded Coder - Unnecessary casts in the generated code might result in compilation error for C++ simulation targets in referenced models	22 Sep 2021
<a href="#">2437086</a>	Embedded Coder - Assertion error when generating code for a model that computes the modulus of a negative value in a symbolic size expression	22 Sep 2021
<a href="#">2437096</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model containing a conditional subsystem that inherits initial conditions for output values	22 Sep 2021
<a href="#">2358247</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect answers in rapid accelerator mode when Stateflow charts call multi-instance model blocks in export-function mode	22 Sep 2021
<a href="#">2473990</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results are possible for a model hierarchy in which one model has a single instance of a reusable library subsystem and another model has multiple instances of the same reusable library subsystem	22 Sep 2021
<a href="#">2471811</a>	Embedded Coder - Default code mapping in a shared data dictionary may be reset to default	22 Sep 2021
<a href="#">2451400</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur when a reusable subsystem input connects to a Model block input which connects to a Simulink Function block	22 Sep 2021
<a href="#">2436658</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results are possible for a model containing a Data Store Memory block and a Function-Call Subsystem block	22 Sep 2021
<a href="#">2441015</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect answer may be observed with reusable subsystems receiving N-D signals in the row-major mode	22 Sep 2021
<a href="#">2436097</a>	Embedded Coder - Static code metrics report shows an error	22 Sep 2021
<a href="#">2480768</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur for a model with a function-call subsystem that has feedback loop of output going into the input	22 Sep 2021
<a href="#">2481749</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results are possible for a model with local block outputs enabled and a Zero-Order Hold block with multiple rates inside a For Each subsystem	22 Sep 2021
<a href="#">2479152</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur if reusable subsystem is connected to Conditional Subsystem's outport through Selector block with storage class on it's inport.	22 Sep 2021
<a href="#">2482170</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Incorrect results might occur if grounded Function-Call Subsystem with IC specified on subsystem's outport is present inside model reference block	22 Sep 2021
<a href="#">2495845</a>	Embedded Coder - Incorrect AUTOSAR IncludedDataTypeSet entries exported for Simulink enumerated types with custom storage class Exported Global	22 Sep 2021
<a href="#">2498858</a>	Embedded Coder - <b>Incorrect Code Generation:</b> Simulink model containing a Demux block connected to a Stateflow chart might generate incorrect code	22 Sep 2021
<a href="#">2423137</a>	Embedded Coder - Model build time much longer when generating A2L file	22 Sep 2021
<a href="#">2470133</a>	Embedded Coder - Code generation may fail when custom Subsystems are added using TLC directives	22 Sep 2021
<a href="#">2457978</a>	Embedded Coder - Invalid memory section error in atomic subsystem SIL/PIL simulations	22 Sep 2021
<a href="#">2482162</a>	Embedded Coder - Boolean constants are not initialized properly in <model>_data.c.	22 Sep 2021
<a href="#">2499766</a>	Embedded Coder - Error when logging AUTOSAR software component Inport or Outport signals during SIL or PIL simulation	22 Sep 2021
<a href="#">2446802</a>	Embedded Coder - Compiler error for xil_interface.c in SIL or PIL simulation that logs AUTOSAR signals and states	22 Sep 2021
<a href="#">2560512</a>	Embedded Coder - Error when calling TLC library function LibBlockSampleTime	22 Sep 2021
<a href="#">2382414</a>	Embedded Coder - Compilation error occurs if reusable Subsystem inside Variant Subsystem is connected to Scope block	22 Sep 2021
<a href="#">2546762</a>	Embedded Coder - Code generation fails for model with a root inport that outputs a nonvirtual bus	24 Aug 2021
<a href="#">2478537</a>	Embedded Coder - MATLAB might crash when building a model with the parameter RollThreshold higher than the number of iterations of a For Each Subsystem nested in a For Iterator Subsystem	19 Aug 2021
<a href="#">2483012</a>	Embedded Coder - Non-virtual bus elements cause C++ code generation to fail	15 Jul 2021
<a href="#">2463352</a>	Embedded Coder - Delay in MATLAB Coder Workflow	21 May 2021
<a href="#">2461932</a>	Embedded Coder - PIL:pil_subsystem:UnsupportedSubsystem error from atomic subsystem SIL or PIL	05 May

	simulations	2021
<a href="#">2450560</a>	Embedded Coder - PIL simulation error when target package with debugger provides connectivity and target hardware byte ordering is Big Endian	27 Apr 2021
<a href="#">2063366</a>	Embedded Coder - <b>Incorrect Code Generation</b> : AUTOSAR generated code might write uninitialized value if array data is conditionally and partially written to root outport	16 Jul 2020
<a href="#">2166906</a>	Embedded Coder - SIL/PIL simulation fails if model contains Reset Function block and model step function uses function prototype control	14 May 2020
<a href="#">1934700</a>	Embedded Coder - Model block SIL or PIL simulation produces error for AUTOSAR software component with model workspace parameters mapped to SharedParameter	06 Feb 2020
<a href="#">2133775</a>	Embedded Coder - MATLAB might crash when generating code for a model containing C action language Stateflow Chart with shift operation applied to custom storage class	06 Feb 2020
<a href="#">2106435</a>	Embedded Coder - Code generation error for AUTOSAR model in which Simulink Function sends message to root outport	19 Dec 2019
<a href="#">2072645</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect results might occur for C++ std::string in MATLAB Function block	11 Oct 2019
<a href="#">1999672</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect results are possible for a model with a For Each subsystem block	16 Aug 2019
<a href="#">2007592</a>	Embedded Coder - <b>Incorrect Code Generation</b> : Incorrect results might occur for a model with a Bus Assignment block and an Assignment block	16 Aug 2019
<a href="#">1955846</a>	Embedded Coder - MATLAB might crash while building a model with a Reusable custom storage class specification on root i/o	24 Apr 2019
<a href="#">1709275</a>	Embedded Coder - Generated code for Stateflow Chart may contain dead initialization code	12 Feb 2018

[^ Less](#)

### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

#### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### Display bug reports for IEC Certification Kit

Display bug reports for IEC Certification Kit (R2021a) available at <https://www.mathworks.com/support/bugreports> .

**NOTE:** This check does not determine whether your model might be affected by these bugs.

#### Warning

There are 6 IEC Certification Kit bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2664996</a>	IEC Certification Kit - IEC Certification Kit may exclude some Simulink Design Verifier validation tests cases due to timeout	07 Jul 2022
<a href="#">2737735</a>	IEC Certification Kit - IEC Certification Kit test case for Simulink Design Verifier fails to execute when Stateflow is not available	10 May 2022
<a href="#">2680526</a>	IEC Certification Kit - IEC Certification Kit test procedure for Simulink Coverage fails to complete when Fixed-Point Designer or DSP System Toolbox is not available	24 Mar 2022
<a href="#">2449871</a>	IEC Certification Kit - Several Simulink Check tests cases fail to execute when Fixed-Point Designer is not available	09 Mar 2022
<a href="#">2384778</a>	IEC Certification Kit - IEC Certification Kit validation suite for Simulink Requirements fails in time zones different than Eastern Time Zone (ET)	23 Sep 2021
<a href="#">2460424</a>	IEC Certification Kit - Hyperlink in Simulink Coverage test procedure does not execute test suite certkitiec_slcov_eml	22 Sep 2021

### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

#### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

## **⚠️ Display bug reports for Polyspace Code Prover**

Display bug reports for Polyspace Code Prover (R2021a) available at <https://www.mathworks.com/support/bugreports>.

**NOTE:** This check does not determine whether your model might be affected by these bugs.

### **Warning**

There are 4 Polyspace Code Prover bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2714466</a>	Polyspace Code Prover - Stack overflow exception with Polyspace Code Prover on Windows systems	12 Jul 2022
<a href="#">2566304</a>	Polyspace Code Prover - On Windows 11, Polyspace Code Prover might error out during the Software Safety Analysis Level 1 part of the analysis and provide less precise results	09 Mar 2022
<a href="#">2498314</a>	Polyspace Code Prover - Polyspace compilation error when using option -osek-multitasking or -autosar-multitasking	27 Oct 2021
<a href="#">2463083</a>	Polyspace Code Prover - Incorrect red Correctness condition check with message invalid function call on C++ code	22 Sep 2021

### **Recommended Action**

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

## **⚠️ Display bug reports for Polyspace Bug Finder**

Display bug reports for Polyspace Bug Finder (R2021a) available at <https://www.mathworks.com/support/bugreports>.

**NOTE:** This check does not determine whether your model might be affected by these bugs.

### **Warning**

There are 38 Polyspace Bug Finder bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2766151</a>	Polyspace Bug Finder - AUTOSAR-C++14 A7-1-3 false positive when using std::string	13 Jul 2022
<a href="#">2750967</a>	Polyspace Bug Finder - AUTOSAR-C++14 A12-1-1 false positive violation when a class calls a delegating constructor	11 Jul 2022
<a href="#">2771715</a>	Polyspace Bug Finder - polyspace-configure cannot extract target characteristics of CodeWarrior compiler	28 Jun 2022
<a href="#">2759641</a>	Polyspace Bug Finder - AUTOSAR-C++14 M5-3-1 false positive on template instantiation using template parameter	27 Jun 2022
<a href="#">2059416</a>	Polyspace Bug Finder - MISRA-C:2012 17.8 false positive violation on function parameters	20 Jun 2022
<a href="#">2669421</a>	Polyspace Bug Finder - AUTOSAR-C++14 A5-1-1 false positive on negative constant	26 Apr 2022
<a href="#">2693088</a>	Polyspace Bug Finder - AUTOSAR-C++14 A5-1-1 false positive on ranged-based for-loop	26 Apr 2022
<a href="#">2677178</a>	Polyspace Bug Finder - Custom Rule 20.1 is incorrectly raised when the number of characters in a line is equal to the limit	12 Apr 2022
<a href="#">2661494</a>	Polyspace Bug Finder - False positive violation of AUTOSAR-C++14 A5-2-2 when using C++ casts {}	12 Apr 2022
<a href="#">2699548</a>	Polyspace Bug Finder - Polyspace-configure fails to detect the compiler on Windows, but detects a nonexistent executed program C:\Program.exe	29 Mar 2022
<a href="#">2565249</a>	Polyspace Bug Finder - MATLAB might crash when generating an Embedded Coder report	28 Mar 2022
<a href="#">2622018</a>	Polyspace Bug Finder - Polyspace analysis fails when using standard headers on Unix systems	14 Mar 2022
<a href="#">2492695</a>	Polyspace Bug Finder - False positive violation of UINT_CONSTANT_OVFL on unary operator ~	09 Mar 2022

<a href="#">2613458</a>	Polyspace Bug Finder - Incorrect Polyspace project generation when using polyspace-configure with compilation database	09 Mar 2022
<a href="#">2628485</a>	Polyspace Bug Finder - On Windows 11, Polyspace Bug Finder might error out during the Function Analysis part of the analysis and provide only partial results	09 Mar 2022
<a href="#">2620754</a>	Polyspace Bug Finder - AUTOSAR-C++14 A2-7-3 false positive on documented declarations that are local to a function	09 Mar 2022
<a href="#">2540070</a>	Polyspace Bug Finder - AUTOSAR-C++14 M5-0-7 false positive when the output of a member function call is cast to float explicitly	09 Mar 2022
<a href="#">2622665</a>	Polyspace Bug Finder - MISRA-C:2012 8.13 false positive when accessing data by using a local copy of a nonconst pointer parameter	09 Mar 2022
<a href="#">2623758</a>	Polyspace Bug Finder - MISRA-C:2012 20.7 false positive in variable declarations	09 Mar 2022
<a href="#">2623774</a>	Polyspace Bug Finder - MISRA-C:2012 14.2 false positive on second expression when loop counter comparison is done on an addition	09 Mar 2022
<a href="#">2639582</a>	Polyspace Bug Finder - Software Quality Objectives report shows incorrect PASS/FAIL statuses	09 Mar 2022
<a href="#">2620421</a>	Polyspace Bug Finder - Polyspace Bug Finder or Code Prover analysis returns a C++ parser error: identifier "__builtin_bit_cast" is undefined	08 Mar 2022
<a href="#">2077167</a>	Polyspace Bug Finder - Code Generation Advisor does not run on a model that has a Polyspace options object in its configset	01 Mar 2022
<a href="#">2638862</a>	Polyspace Bug Finder - Polyspace compilation error when using Tasking compiler attribute __align()	06 Jan 2022
<a href="#">2375985</a>	Polyspace Bug Finder - Polyspace-configure command fails on Windows	03 Jan 2022
<a href="#">2301383</a>	Polyspace Bug Finder - Incorrect defect Unmodified variable not const-qualified on reference variable	22 Sep 2021
<a href="#">2418884</a>	Polyspace Bug Finder - Polyspace compilation error on anonymous unions when using option -compiler greenhills	22 Sep 2021
<a href="#">2413643</a>	Polyspace Bug Finder - Rule A8-4-7 report for types that are not trivially copyable	22 Sep 2021
<a href="#">2451568</a>	Polyspace Bug Finder - Polyspace products crash in ps_middle_end	22 Sep 2021
<a href="#">2247571</a>	Polyspace Bug Finder - False positive violations of MISSING_CONSTEXPR performance defect checker	22 Sep 2021
<a href="#">2445355</a>	Polyspace Bug Finder - AUTOSAR-C++14 A10-03-03 false positive violation on function member marked as "override final"	22 Sep 2021
<a href="#">2426444</a>	Polyspace Bug Finder - False Positive violation of MISRA C++:2008 10-2-1 (AUTOSAR C++14 M10-2-1) when a class is derived from at least two different types of instantiation of the same template class	22 Sep 2021
<a href="#">2473718</a>	Polyspace Bug Finder - MISRA-C:2012 20.1 False Positive violation after 2 consecutive #elif	22 Sep 2021
<a href="#">2493211</a>	Polyspace Bug Finder - False positive violation of A15-4-4 with explicitly declared noexcept constructors	22 Sep 2021
<a href="#">2497546</a>	Polyspace Bug Finder - False positive violation of AUTOSAR-C++14 A20-8-6 when assigning one const std::shared_ptr to another const std::shared_ptr	22 Sep 2021
<a href="#">2505942</a>	Polyspace Bug Finder - IAR Embedded Workbench (iar-ew) __section_begin and __section_end macros always return a pointer to address 0	22 Sep 2021
<a href="#">2509931</a>	Polyspace Bug Finder - The ps_cxx_fe process crashes when the -code-metrics option is used with gnu or clang compilers	22 Sep 2021
<a href="#">2132811</a>	Polyspace Bug Finder - polyspace-configure could not open temporary options file when using Renesas SH	25 Aug 2020

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### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### ⚠ Display bug reports for Polyspace Code Prover Server

Display bug reports for Polyspace Code Prover Server (R2021a) available at <https://www.mathworks.com/support/bugreports> .  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

**Warning**

There are 2 Polyspace Code Prover Server bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2498314</a>	Polyspace Code Prover - Polyspace compilation error when using option -osek-multitasking or -autosar-multitasking	27 Oct 2021
<a href="#">2463083</a>	Polyspace Code Prover - Incorrect red Correctness condition check with message invalid function call on C++ code	22 Sep 2021

**Recommended Action**

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

***Input Parameters Selection***

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

**⚠ Display bug reports for Polyspace Bug Finder Server**

Display bug reports for Polyspace Bug Finder Server (R2021a) available at <https://www.mathworks.com/support/bugreports>.

**NOTE:** This check does not determine whether your model might be affected by these bugs.

**Warning**

There are 13 Polyspace Bug Finder Server bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2771715</a>	Polyspace Bug Finder - polyspace-configure cannot extract target characteristics of CodeWarrior compiler	28 Jun 2022
<a href="#">2059416</a>	Polyspace Bug Finder - MISRA-C:2012 17.8 false positive violation on function parameters	20 Jun 2022
<a href="#">2677178</a>	Polyspace Bug Finder - Custom Rule 20.1 is incorrectly raised when the number of characters in a line is equal to the limit	12 Apr 2022
<a href="#">2661494</a>	Polyspace Bug Finder - False positive violation of AUTOSAR-C++14 A5-2-2 when using C++ casts {}	12 Apr 2022
<a href="#">2699548</a>	Polyspace Bug Finder - Polyspace-configure fails to detect the compiler on Windows, but detects a nonexistent executed program C:\Program.exe	29 Mar 2022
<a href="#">2622018</a>	Polyspace Bug Finder - Polyspace analysis fails when using standard headers on Unix systems	14 Mar 2022
<a href="#">2540070</a>	Polyspace Bug Finder - AUTOSAR-C++14 M5-0-7 false positive when the output of a member function call is cast to float explicitly	09 Mar 2022
<a href="#">2622665</a>	Polyspace Bug Finder - MISRA-C:2012 8.13 false positive when accessing data by using a local copy of a nonconst pointer parameter	09 Mar 2022
<a href="#">2623774</a>	Polyspace Bug Finder - MISRA-C:2012 14.2 false positive on second expression when loop counter comparison is done on an addition	09 Mar 2022
<a href="#">2418884</a>	Polyspace Bug Finder - Polyspace compilation error on anonymous unions when using option -compiler greenhills	22 Sep 2021
<a href="#">2473718</a>	Polyspace Bug Finder - MISRA-C:2012 20.1 False Positive violation after 2 consecutive #elif	22 Sep 2021
<a href="#">2505942</a>	Polyspace Bug Finder - IAR Embedded Workbench (iar-ew) __section_begin and __section_end macros always return a pointer to address 0	22 Sep 2021
<a href="#">2132811</a>	Polyspace Bug Finder - polyspace-configure could not open temporary options file when using Renesas SH	25 Aug 2020

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**Recommended Action**

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

***Input Parameters Selection***

Name	Value

Only show bug reports modified after date(mm/dd/yyyy)

## ⚠ Display bug reports for Simulink Design Verifier

Display bug reports for Simulink Design Verifier (R2021a) available at <https://www.mathworks.com/support/bugreports>.

**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 55 Simulink Design Verifier bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2713230</a>	Simulink Design Verifier - Design Verifier incorrectly reports design errors in IRT Subsystems and System Objects as Valid	13 Jul 2022
<a href="#">2700129</a>	Simulink Design Verifier - Incorrect results for custom block replacement rule when referring to sqrt block in sldvdemo_custom_blockreplib.slx	13 Jul 2022
<a href="#">2610113</a>	Simulink Design Verifier - Design Verifier compatibility check fails for a multi-rate model containing bus element ports	13 Jul 2022
<a href="#">2715256</a>	Simulink Design Verifier - Analysis terminates with an Internal error in Simulink Design Verifier abstract interpretation back end message	27 Jun 2022
<a href="#">2682546</a>	Simulink Design Verifier - Compatibility check may fail with top model and referenced model linked to different data dictionaries	27 Jun 2022
<a href="#">2673489</a>	Simulink Design Verifier - MATLAB might crash for models with multiple callsites of the same function or system object analyzed by Simulink Design Verifier	27 Jun 2022
<a href="#">2627596</a>	Simulink Design Verifier - Lookup Table Dynamic block using Simulink.AliasType objects might be unnecessarily stubbed during analysis	23 May 2022
<a href="#">2695061</a>	Simulink Design Verifier - Incorrect SldvData with option Include expected outputs selected and with signals enabled for logging	06 Apr 2022
<a href="#">2580839</a>	Simulink Design Verifier - Stubbed constructs are incorrectly treated as returning the same value each time they are called in that time step	06 Apr 2022
<a href="#">2358135</a>	Simulink Design Verifier - Test generation shows objectives status as Undecided with Testcases for certain models with Block Reduction off	06 Apr 2022
<a href="#">2627566</a>	Simulink Design Verifier - Analysis may terminate without producing results when performing Test Generation with LongTestcases strategy	04 Apr 2022
<a href="#">2687987</a>	Simulink Design Verifier - Incorrect results with Run additional analysis to reduce instances of rational approximation when Enable timer optimizations is selected	14 Mar 2022
<a href="#">2508622</a>	Simulink Design Verifier - In Design Error Detection workflow, upcasting the datatype for Abs block shows an overflow objective being falsified, but the simulation of the counterexample does not result in an overflow	09 Mar 2022
<a href="#">2585244</a>	Simulink Design Verifier - Incorrect Unsatisfiable or Dead Logic objectives for Simulink Function with multiple callers	09 Mar 2022
<a href="#">2504906</a>	Simulink Design Verifier - Incorrect Dead Logic or Unsatisfiable objectives for Relational Operator blocks that have muxed vector signal inputs	09 Mar 2022
<a href="#">2561592</a>	Simulink Design Verifier - Simulink Design Verifier incorrectly reuses model representation when CovLogicBlockShortCircuit or CovMcdcMode parameter value is changed	09 Mar 2022
<a href="#">2548201</a>	Simulink Design Verifier - Incorrect Unsatisfiable objectives for Saturation blocks with a vector of Inf bounds	09 Mar 2022
<a href="#">2605515</a>	Simulink Design Verifier - Compatibility check fails for models containing Stateflow chart with grouped states and Data Store Memory access	09 Mar 2022
<a href="#">2446268</a>	Simulink Design Verifier - Simulink Design Verifier analysis may provide incorrect results when observing a constant signal	09 Mar 2022
<a href="#">2619788</a>	Simulink Design Verifier - Compatibility check for Software-in-the-Loop (SIL) test generation analysis fails	09 Mar 2022
<a href="#">2590973</a>	Simulink Design Verifier - Running analysis on subsystem harness model using Simulink Design Verifier errors out and results in model incompatibility	09 Mar 2022
<a href="#">2394356</a>	Simulink Design Verifier - Poorly formatted Iteration names and Excel tab names when generated test cases are exported to Simulink Test	09 Mar 2022
<a href="#">2611345</a>	Simulink Design Verifier - Expected output field may not be present in the Simulink Design Verifier results for model having output ports as n-dimensional vector	09 Mar 2022
<a href="#">2599554</a>	Simulink Design Verifier - Simulink Design Verifier compatibility check fails for models having custom code location specified using relative paths	09 Mar 2022
<a href="#">2667523</a>	Simulink Design Verifier - Simulink Design Verifier may incorrectly show Incompatible for models with arrays of complex numbers as output or input to function calls	09 Mar 2022
<a href="#">2671059</a>	Simulink Design Verifier - Loss of test objectives when performing test case generation in C/C++ S-Functions and Simulink Custom Code blocks	09 Mar 2022

<a href="#">2662482</a>	Simulink Design Verifier - Incorrect Unsatisfiable objectives for MCDC objectives where the overall condition is always false	09 Mar 2022
<a href="#">2646336</a>	Simulink Design Verifier - Incorrect diagnostic error message during compatibility check	09 Mar 2022
<a href="#">2483709</a>	Simulink Design Verifier - Simulink Design Verifier analysis on a subsystem fails if model parameter SolverPrmCheckMsg is disabled	17 Feb 2022
<a href="#">2444147</a>	Simulink Design Verifier - Simulink Design Verifier compatibility check fails with an error Empty character vector corresponding to an enumerated value	17 Feb 2022
<a href="#">2512200</a>	Simulink Design Verifier - Simulink Design Verifier incorrectly reports decision objective for the last input index of a Multiport switch block as Unsatisfiable.	17 Feb 2022
<a href="#">2552454</a>	Simulink Design Verifier - Model with a shift by an explicit negative constant may experience incorrect Unsatisfiable or Valid objectives	17 Feb 2022
<a href="#">2453651</a>	Simulink Design Verifier - Simulink Design Verifier Compatibility check fails with error changing property '<parameterName>' is not allowed	17 Feb 2022
<a href="#">2535875</a>	Simulink Design Verifier - Simulink Design Verifier might stall during the preprocessing stage for models with fixed-point 2-D Lookup Tables	17 Feb 2022
<a href="#">2522967</a>	Simulink Design Verifier - Potential incorrect Dead Logic or Unsatisfiable objectives for MinMax blocks with large vector inputs with fixed-point or boolean arguments	27 Jan 2022
<a href="#">2464204</a>	Simulink Design Verifier - Simulink data dictionary gets dirty on compatibility check for models with parameters in .sldd	13 Dec 2021
<a href="#">2611212</a>	Simulink Design Verifier - Compatibility check and block replacement API fails with error about incorrect block execution priorities for models with Initialize Functions and model references	18 Nov 2021
<a href="#">2469655</a>	Simulink Design Verifier - Simulink Design Verifier might report incorrect Unsatisfiable objectives for Relational Operator blocks where vector inputs are compared to a minimum or maximum values for a particular data type	15 Nov 2021
<a href="#">2402262</a>	Simulink Design Verifier - Simulink Design Verifier analysis may provide incorrect results on models containing Simulink Functions with certain settings	28 Sep 2021
<a href="#">2371826</a>	Simulink Design Verifier - Satisfied - No Test Case reported for all the model coverage objectives, even when analysis runs for a longer time	22 Sep 2021
<a href="#">2438958</a>	Simulink Design Verifier - Simulink Design Verifier analysis finishes quickly with all objectives reported as Undecided	22 Sep 2021
<a href="#">2433983</a>	Simulink Design Verifier - Incorrect error message when generating test cases for Simulink Test harness using existing coverage data	22 Sep 2021
<a href="#">2446406</a>	Simulink Design Verifier - status = logical 0 printed on MATLAB command prompt with SLDV Test Generation workflow	22 Sep 2021
<a href="#">2419195</a>	Simulink Design Verifier - Sldvgencov function may return coverage data that does not include coverage metrics for which it generates test cases	22 Sep 2021
<a href="#">2437703</a>	Simulink Design Verifier - Simulink Design Verifier compatibility check fails if invoked multiple times on a model using data dictionary	22 Sep 2021
<a href="#">2453018</a>	Simulink Design Verifier - Simulink Design Verifier may error out for models containing Stateflow Charts with exported functions that contain reads or writes to global variables	22 Sep 2021
<a href="#">2269840</a>	Simulink Design Verifier - Simulation of newly generated tests fail in Simulink Test Manager for export function models	22 Sep 2021
<a href="#">2487080</a>	Simulink Design Verifier - Simulink Design Verifier does not generate tests for relational boundary objectives when Add Tests for Missing Coverage is clicked in Simulink Test Manager	22 Sep 2021
<a href="#">2476330</a>	Simulink Design Verifier - sldvmakeharness fails for models with Dimension Variants	22 Sep 2021
<a href="#">2452582</a>	Simulink Design Verifier - Statuses of test objectives reported incorrectly while trying to run Coverage Top off workflow	22 Sep 2021
<a href="#">2495455</a>	Simulink Design Verifier - Expected Output field in sldvData during Test Generation workflow is not populated completely	22 Sep 2021
<a href="#">2427470</a>	Simulink Design Verifier - Design Verifier does not support analysis of model blocks calling Simulink functions across the model boundary	22 Sep 2021
<a href="#">2476223</a>	Simulink Design Verifier - Report generation using saved sldvData fails in the subsystem extraction workflow	22 Sep 2021
<a href="#">2400971</a>	Simulink Design Verifier - Simulink Design Verifier analysis report may contain garbled text on non-English MATLAB on Windows platform	22 Sep 2021
<a href="#">2476025</a>	Simulink Design Verifier - Coverage objectives incorrectly reported as Unsatisfiable or Dead Logic inside reachable MATLAB function	02 Aug 2021

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#### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

#### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

## ⚠ Display bug reports for Simulink PLC Coder

Display bug reports for Simulink PLC Coder (R2021a) available at <https://www.mathworks.com/support/bugreports> .  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 13 Simulink PLC Coder bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2495472</a>	Simulink PLC Coder - PLC Code generation for MultiProg and PCWorx targets may fail for some models	06 Apr 2022
<a href="#">2471567</a>	Simulink PLC Coder - Incorrect error message for multi-word datatype during PLC code generation	04 Apr 2022
<a href="#">2442004</a>	Simulink PLC Coder - Incorrect Code Generation: Using an assignment block with 2-D matrix and vector assignment with for-iterator indexing could result in incorrect code generation	04 Apr 2022
<a href="#">2604399</a>	Simulink PLC Coder - Code generated via Simulink PLC Coder target creates empty variables in Codesys 3.5 IDE	09 Mar 2022
<a href="#">2587382</a>	Simulink PLC Coder - Additional Alias type named ZCSigState generated in PLC code	09 Mar 2022
<a href="#">2537495</a>	Simulink PLC Coder - Assertion error when generating PLC code 'In setNameOrAssert: Assertion failed: found == this'	27 Jan 2022
<a href="#">2416453</a>	Simulink PLC Coder - MATLAB might crash or hang during PLC code generation from Stateflow chart	22 Sep 2021
<a href="#">2441951</a>	Simulink PLC Coder - Incorrect Code Generation: Using an assignment block with partial element assignments could result in incorrect code	22 Sep 2021
<a href="#">2450425</a>	Simulink PLC Coder - PLC code generation errors out for some Stateflow models	22 Sep 2021
<a href="#">2478983</a>	Simulink PLC Coder - PLC Coder could generate incorrectly typed matrix address expressions	22 Sep 2021
<a href="#">2457505</a>	Simulink PLC Coder - Incorrect Code Generation: PLC code generated for Stateflow models that contain temporal conditions inside IF statements may be incorrect	22 Sep 2021
<a href="#">2216089</a>	Simulink PLC Coder - MATLAB might crash when generating PLC code for a model that uses Simulink.Signal	11 Jun 2020
<a href="#">2062037</a>	Simulink PLC Coder - Incorrect Code Generation: PLC Coder generates wrong code for the Discrete-time Integrator block using unsupported integrator methods	11 Oct 2019

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### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

## ⚠ Display bug reports for Simulink Check

Display bug reports for Simulink Check (R2021a) available at <https://www.mathworks.com/support/bugreports> .  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 87 Simulink Check bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2730454</a>	Simulink Check - Model Advisor check Check position of signal labels (mathworks.jmaab.db_0097) displays an incorrect warning for signal lines	27 Jun 2022
<a href="#">2683067</a>	Simulink Check - JMAAB check for db_0126 reports incorrect warning for events in Stateflow charts	17 Jun 2022

<a href="#">2707456</a>	Simulink Coverage - Assignment statements in C charts might have incorrect number of condition coverage objectives	23 May 2022
<a href="#">2669296</a>	Simulink Check - JMAAB check for jc_0627 displays abnormal exit warning for Discrete-Time Integrator blocks	19 Apr 2022
<a href="#">2705502</a>	Simulink Check - Model Advisor Exclusions menu does not get the correct block type for Stateflow blocks.	18 Apr 2022
<a href="#">2713025</a>	Simulink Coverage - Simulink Coverage does not report the implicit default case as a decision outcome	08 Apr 2022
<a href="#">2669272</a>	Simulink Check - JMAAB check for jm_0012 incorrectly flags syntax for events that are not local	08 Apr 2022
<a href="#">2630066</a>	Simulink Check - JMAAB check for db_0125 does not report warning for data objects in State Transition Tables	06 Apr 2022
<a href="#">2624372</a>	Simulink Check - JMAAB check for jc_0762 gives inconsistent warnings	06 Apr 2022
<a href="#">2610205</a>	Simulink Check - JMAAB check for na_0001 does not report ~ and ^ used in logical operations	06 Apr 2022
<a href="#">2446841</a>	Simulink Check - JMAAB check for jc_0651 displays an incorrect warning for root-level Outport blocks	06 Apr 2022
<a href="#">2507908</a>	Simulink Check - The JMAAB check for jc_0009 flags signals originating from a subsystem linked to a Simulink library	09 Mar 2022
<a href="#">2511933</a>	Simulink Check - High-Integrity check MATLAB code with logical operators and functions does not analyze external MATLAB files at secondary and further levels	09 Mar 2022
<a href="#">2509332</a>	Simulink Check - The edit time check does not flag Stateflow Junctions without default transitions.	09 Mar 2022
<a href="#">2469462</a>	Simulink Check - Edit-time checks incorrectly flag blocks on non-English platforms	09 Mar 2022
<a href="#">2554306</a>	Simulink Check - JMAAB check for db_0141 doesn't have sub-id selection	09 Mar 2022
<a href="#">2558775</a>	Simulink Check - Requirement links to test assessments are not recognized by the metrics of the Model Testing Dashboard	09 Mar 2022
<a href="#">2458329</a>	Simulink Check - JMAAB check for jc_0736 displays an incorrect warning for conditional and loop statements in state actions	09 Mar 2022
<a href="#">2564915</a>	Simulink Check - JMAAB check for db_0129 does not report transitions crossing graphical functions or subcharts	09 Mar 2022
<a href="#">2564970</a>	Simulink Check - JMAAB check for db_0129 does not report supertransitions crossing states	09 Mar 2022
<a href="#">2567643</a>	Simulink Check - JMAAB check for jc_0626 does not report Lookup Table blocks not having recommended Interpolation method settings	09 Mar 2022
<a href="#">2580359</a>	Simulink Check - JMAAB check for jc_0772 does not report transitions with c, c++ comments	09 Mar 2022
<a href="#">2579538</a>	Simulink Check - JMAAB check for jc_0009 does not report subsystems that do not have propagated signal labels	09 Mar 2022
<a href="#">2579526</a>	Simulink Check - JMAAB check for jc_0009 does not report subsystems with RTWSystemCode parameter set as Auto	09 Mar 2022
<a href="#">2574572</a>	Simulink Check - JMAAB check for jc_0741 does not report states having multiple action labels for an action	09 Mar 2022
<a href="#">2574563</a>	Simulink Check - JMAAB check for jc_0734 displays an incorrect warning for states with words that start with action labels	09 Mar 2022
<a href="#">2574556</a>	Simulink Check - JMAAB check for jc_0733 does not report states having action types written out of order with spaces	09 Mar 2022
<a href="#">2568871</a>	Simulink Check - JMAAB check for jc_0628 displays an incorrect warning for Saturation and Saturation Dynamic blocks	09 Mar 2022
<a href="#">2568870</a>	Simulink Check - JMAAB check for jc_0623 does not report delay blocks used in continuous type model or subsystem	09 Mar 2022
<a href="#">2583147</a>	Simulink Check - Metrics Dashboard and model metrics API use incorrect bin edges for metric distributions when the metric results are close in value	09 Mar 2022
<a href="#">2593371</a>	Simulink Check - JMAAB check for jc_0630 displays an incorrect warning for Multiport Switch block	09 Mar 2022
<a href="#">2600519</a>	Simulink Check - JMAAB check for db_0140 does not report Constant block that do not display nondefault block parameter values	09 Mar 2022
<a href="#">2605220</a>	Simulink Check - JMAAB check for jc_0643 does not detect fixed point data type in Simulink data object	09 Mar 2022
<a href="#">2607625</a>	Simulink Check - JMAAB check for jc_0231 displays an incorrect warning for hidden Simulink blocks inside Stateflow charts	09 Mar 2022
<a href="#">2619243</a>	Simulink Check - Running a Model Advisor check multiple times with different input parameters using ModelAdvisor.run results in the check not running	09 Mar 2022
<a href="#">2626539</a>	Simulink Check - JMAAB check for db_0042 incorrectly flags hidden Simulink blocks	09 Mar

		2022
<a href="#">2625745</a>	Simulink Check - JMAAB check for db_0129 does not report transition lines that overlap one another	09 Mar 2022
<a href="#">2624304</a>	Simulink Check - Sub-check b of JMAAB check for jc_0121 does not flag when the Sum block is part of feedback loop	09 Mar 2022
<a href="#">2581481</a>	Simulink Check - JMAAB check for na_0002: sub-checks do not correspond to correct sub-ids	09 Mar 2022
<a href="#">2627366</a>	Simulink Check - MISRA C:2012 check Check for unsupported block names does not report block names in inactive variant subsystems	09 Mar 2022
<a href="#">2626628</a>	Simulink Check - The JMAAB check for jc_0641 does not report trigonometric function block	09 Mar 2022
<a href="#">2626638</a>	Simulink Check - The JMAAB check for jc_0641 reports Enable and Trigger blocks inside Subsystem blocks	09 Mar 2022
<a href="#">2632041</a>	Simulink Check - JMAAB check for db_0129 does not report transition lines that are diagonally oriented	09 Mar 2022
<a href="#">2636425</a>	Simulink Check - execute function returns uninformative error message when multiple artifacts are dirty	09 Mar 2022
<a href="#">2636355</a>	Simulink Check - jc_0732 check does not report Stateflow event names that match state names	09 Mar 2022
<a href="#">2634048</a>	Simulink Check - The JMAAB check for jc_0731 does not report use of slash in Stateflow state name	09 Mar 2022
<a href="#">2630004</a>	Simulink Check - The JMAAB check for na_0021, na_0031, and na_0037 are not available in JMAAB By Task folder	09 Mar 2022
<a href="#">2629915</a>	Simulink Check - The JMAAB check for na_0004 reports blocks that are excluded	09 Mar 2022
<a href="#">2626610</a>	Simulink Check - The JMAAB check for jc_0644 does not report Outport block	09 Mar 2022
<a href="#">2626591</a>	Simulink Check - The JMAAB check for jc_0602 reports Import blocks with prefixes	09 Mar 2022
<a href="#">2634137</a>	Simulink Check - JMAAB check for jc_0611 does not report warning for Product blocks with multiplication operators	09 Mar 2022
<a href="#">2626620</a>	Simulink Check - JMAAB check for jc_0752 does not report warning for parentheses in condition actions	09 Mar 2022
<a href="#">2627449</a>	Simulink Check - JMAAB check for jc_0009 gives incorrect result for signal connected to From block	09 Mar 2022
<a href="#">2631023</a>	Simulink Check - The JMAAB check for jc_0802 does not analyze Simulink Function inside Stateflow Chart	09 Mar 2022
<a href="#">2634803</a>	Simulink Check - jc_0802 check not reporting comparison with literals	09 Mar 2022
<a href="#">2639779</a>	Simulink Check - Sub-check a of JMAAB check for db_0146 incorrectly flags blocks inside linked-library blocks	09 Mar 2022
<a href="#">2662447</a>	Simulink Check - JMAAB check for db_0032 reports incorrect warning for signal lines	09 Mar 2022
<a href="#">2663795</a>	Simulink Check - JMAAB check for jc_0753 does not report Transition Actions not enclosed in curly braces	09 Mar 2022
<a href="#">2546461</a>	Simulink Check - The JMAAB check for jc_760 displays incorrect result for Subcharts	17 Feb 2022
<a href="#">2529892</a>	Simulink Check - JMAAB check for jc_0171 displays abnormal exit warning for subsystems with unconnected source ports	25 Jan 2022
<a href="#">2157846</a>	Simulink Check - Model Advisor checks do not report Reserved Keywords	25 Jan 2022
<a href="#">2540383</a>	Simulink Check - JMAAB check for jc_0009 does not flag input signals across subsystem hierarchy	25 Jan 2022
<a href="#">2624994</a>	Simulink Check - JMAAB check for jc_0451 use of unary minus on Boolean is not detected	25 Jan 2022
<a href="#">2366579</a>	Simulink Check - JMAAB check for jc_0531 does not comply with its JMAAB 5.1 modeling guideline	25 Jan 2022
<a href="#">2486728</a>	Simulink Check - Model Advisor check Check for optimal bus virtuality does not display warning for subsystem boundaries	02 Nov 2021
<a href="#">2417352</a>	Simulink Check - On launching Model Slicer, an incompatibility message incorrectly indicates that a Model block inside a For Each subsystem is configured in normal mode	22 Sep 2021
<a href="#">2428380</a>	Simulink Check - Removing a widget from the Metrics Dashboard layout crashes MATLAB	22 Sep 2021
<a href="#">2432791</a>	Simulink Check - NA-MAAB check for number of called function levels always assumes Function Call Level parameter to be 3	22 Sep 2021
<a href="#">2401263</a>	Simulink Check - High-Integrity check Check usage of recursions does not flag all instances of duplicate Charts and MATLAB Function Blocks	22 Sep 2021

<a href="#">2441988</a>	Simulink Coverage - Generating a code coverage report using cvhtml might cause MATLAB to crash	22 Sep 2021
<a href="#">2437043</a>	Simulink Coverage - Error when alternating coverage collection between normal simulation mode and SIL/PIL	22 Sep 2021
<a href="#">2450434</a>	Simulink Check - The MATLAB High-integrity checks need Stateflow license to appear in the Model Advisor	22 Sep 2021
<a href="#">2450284</a>	Simulink Check - Traceability matrix does not open from Model Advisor report	22 Sep 2021
<a href="#">2438353</a>	Simulink Check - Model Slicer displays an error when slicing a model that has variant subsystems that contain bus ports	22 Sep 2021
<a href="#">2486961</a>	Simulink Check - JMAAB check for db_0126 displays incorrect warnings for events using send syntax	22 Sep 2021
<a href="#">2481143</a>	Simulink Check - JMAAB check for jm_0012 displays abnormal exit warning for non-default sub-id selection	22 Sep 2021
<a href="#">2462987</a>	Simulink Check - The Model Advisor checks checks for na_0016, na_0018 and jc_0801 need Stateflow license to appear in the Model Advisor	22 Sep 2021
<a href="#">2462216</a>	Simulink Check - Launching Model Slicer to debug or inspect a Simulink Design Verifier objective displays an error when the model has a Referenced Configuration Set	22 Sep 2021
<a href="#">2483880</a>	Simulink Coverage - Generating coverage report takes very long time	22 Sep 2021
<a href="#">2507338</a>	Simulink Check - JMAAB check for db_0032 displays an incorrect warning for signal lines crossing over one another	22 Sep 2021
<a href="#">2511608</a>	Simulink Check - JMAAB check for db_0097 displays an incorrect warning for signal labels	22 Sep 2021
<a href="#">2530441</a>	Simulink Check - JMAAB check for jc_0741 displays an incorrect warning for variables used in transitions	22 Sep 2021
<a href="#">2530966</a>	Simulink Check - JMAAB check for db_0146 does not report iterator blocks positioned improperly	22 Sep 2021
<a href="#">2566548</a>	Simulink Check - The misra check for mathworks.misra.CompliantCGIRConstructions displays an error when model contains c2000 block	14 Sep 2021
<a href="#">2530623</a>	Simulink Check - The high integrity check for hisf_0003 displays unexpected Stateflow warnings for bit shift operations	24 Aug 2021
<a href="#">2148105</a>	Simulink Coverage - Coverage incorrectly reported as unsatisfied for MATLAB functions	05 Aug 2021
<a href="#">2426189</a>	Simulink Check - High-integrity checks for hisl_0021, hisl_0022, and hisl_0102 need Stateflow license to appear in the Model Advisor	18 Feb 2021

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### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### ⚠ Display bug reports for Simulink Coverage

Display bug reports for Simulink Coverage (R2021a) available at <https://www.mathworks.com/support/bugreports> .  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 19 Simulink Coverage bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2633995</a>	Simulink Coverage - Aggregating cvdata objects by using the "+" operator might cause an error	21 Jun 2022
<a href="#">2707456</a>	Simulink Coverage - Assignment statements in C charts might have incorrect number of condition coverage objectives	23 May 2022
<a href="#">2713025</a>	Simulink Coverage - Simulink Coverage does not report the implicit default case as a decision outcome	08 Apr 2022
<a href="#">2567786</a>	Simulink Coverage - Collecting code coverage for a model might cause a compiler error	09 Mar 2022

<a href="#">2566769</a>	Simulink Coverage - Aggregation of coverage data for subsystem into top-model might fail	09 Mar 2022
<a href="#">2583147</a>	Simulink Check - Metrics Dashboard and model metrics API use incorrect bin edges for metric distributions when the metric results are close in value	09 Mar 2022
<a href="#">2504462</a>	Simulink Coverage - Simulink blocks with fixed-point input might report incorrect relational boundary coverage results	09 Mar 2022
<a href="#">2615381</a>	Simulink Coverage - Incorrect Code Generation: Code coverage might provide wrong coverage values on first run	09 Mar 2022
<a href="#">2637454</a>	Simulink Coverage - MATLAB might throw an error when applying a coverage filter	09 Mar 2022
<a href="#">2655594</a>	Simulink Coverage - Aggregating coverage data for model and subsystem harnesses might error	09 Mar 2022
<a href="#">2677329</a>	Simulink Coverage - Generating a coverage report for a harness model containing a truth table block might cause an error	09 Mar 2022
<a href="#">2486335</a>	Simulink Coverage - Coverage simulation with a filter applied is very slow	17 Feb 2022
<a href="#">2437043</a>	Simulink Coverage - Error when alternating coverage collection between normal simulation mode and SIL/PIL	22 Sep 2021
<a href="#">2441988</a>	Simulink Coverage - Generating a code coverage report using cvhtml might cause MATLAB to crash	22 Sep 2021
<a href="#">2455392</a>	Simulink Coverage - Setting a custom output directory for Simulink Coverage might cause an error	22 Sep 2021
<a href="#">2417071</a>	Simulink Coverage - Running consecutive coverage analyses on a subsystem harness might cause an error	22 Sep 2021
<a href="#">2483880</a>	Simulink Coverage - Generating coverage report takes very long time	22 Sep 2021
<a href="#">2454026</a>	Simulink Coverage - Coverage not recorded for referenced Subsystem simulated using a test harness	22 Sep 2021
<a href="#">2148105</a>	Simulink Coverage - Coverage incorrectly reported as unsatisfied for MATLAB functions	05 Aug 2021

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### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### Display bug reports for Simulink Test

Display bug reports for Simulink Test (R2021a) available at <https://www.mathworks.com/support/bugreports> .

**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 43 Simulink Test bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2760859</a>	Simulink Test - Using visdiff with models containing internally saved test harnesses might error if Simulink Test license not available	30 Jun 2022
<a href="#">2692225</a>	Simulink Test - Normal simulation mode is displayed instead of SIL or PIL in Test Manager results	27 Jun 2022
<a href="#">2671291</a>	Simulink Test - Signal label mismatch during test harness compilation	27 Jun 2022
<a href="#">2489530</a>	Simulink Test - MATLAB becomes unresponsive when running tests on a model that contains a Record block	09 Mar 2022
<a href="#">2549562</a>	Simulink Test - Rebuilding test harness slow when system being tested outputs bus objects and harness source is Chart or Test Sequence	09 Mar 2022
<a href="#">2252269</a>	Simulink Test - Only the first model will use fast restart when executing different models using Simulink Test Manager	09 Mar 2022
<a href="#">2543000</a>	Simulink Test - Simulink Test Code Importer does not generate stub for undefined functions called with function pointer	09 Mar 2022

<a href="#">2492576</a>	Simulink Test - Saving model with test harnesses and immediately closing it might crash MATLAB	09 Mar 2022
<a href="#">2633552</a>	Simulink Test - Simulink Test Manager incorrectly reports test failure when using verify keyword	09 Mar 2022
<a href="#">2622612</a>	Simulink Test - Multorelease equivalence tests with incomplete first simulation produce passing results	09 Mar 2022
<a href="#">2542186</a>	Simulink Test - Creating a test harness with a large number of input or output ports might result in unconnected ports	17 Feb 2022
<a href="#">2623916</a>	Simulink Test - Parallel execution of test case using Stop Simulation at Last Time Point crashes MATLAB	17 Feb 2022
<a href="#">2442673</a>	Simulink Test - Simulink Test Manager degraded performance when test case has many baseline signals	27 Jan 2022
<a href="#">2477659</a>	Simulink Test - Recovered Stateflow Charts block inserted when Subsystem Reference block added to test harness	27 Jan 2022
<a href="#">2446058</a>	Simulink Test - NaN% reported for excluded code coverage in Simulink Test Manager coverage report	26 Jan 2022
<a href="#">2430795</a>	Simulink Test - Error when using Create Test for Model Component wizard in Test Manager for components inside function-call subsystems	25 Jan 2022
<a href="#">2507426</a>	Simulink Test - Running Simulink Design Verifier advisor checks when an external harness is open might crash MATLAB	25 Jan 2022
<a href="#">2375952</a>	Simulink Test - Capturing baseline data in previous release produces error when callbacks are used in Test Manager	22 Sep 2021
<a href="#">2455380</a>	Simulink Test - Iterations not shown in Iterations section of Test Manager after adding an external Input file to Inputs section	22 Sep 2021
<a href="#">2456325</a>	Simulink Test - setVariable method of sltest.testmanager.TestIteration errors for non-vector arguments	22 Sep 2021
<a href="#">2399895</a>	Simulink Test - Test for Model Component wizard in Test Manager incorrectly adds new test case to existing test file	22 Sep 2021
<a href="#">2448312</a>	Simulink Test - Simulink Test Observers output incorrect value for self activity in a Stateflow chart	22 Sep 2021
<a href="#">2424624</a>	Simulink Test - Test Harness using Import source for component with Bus Element Ports using nonvirtual buses in virtual bus hierarchy produces an error	22 Sep 2021
<a href="#">2441257</a>	Simulink Test - Code Importer errors during global I/O analysis if CopySourceFiles option is false	22 Sep 2021
<a href="#">2441053</a>	Simulink Test - Generating stubs for imported code with multiple source files creates duplicate stub definitions	22 Sep 2021
<a href="#">2438695</a>	Simulink Test - Test case cleanup callback for parallel test execution runs on host MATLAB	22 Sep 2021
<a href="#">2419139</a>	Simulink Test - Test file cleanup callback runs incorrectly when using MATLAB Unit Test Runner	22 Sep 2021
<a href="#">2474797</a>	Simulink Test - Signals connected to a FileLog block are not shown in Test Manager results	22 Sep 2021
<a href="#">2462594</a>	Simulink Test - Simulink Test Manager errors when using [Model Settings] as the Signal Editor override scenario	22 Sep 2021
<a href="#">2482099</a>	Simulink Test - Results for nested suites cannot be exported under certain conditions on Windows	22 Sep 2021
<a href="#">2236006</a>	Simulink Test - Test using sltest.testmanager.run on models with fast restart fail, but pass when using Test Manager	04 Apr 2022
<a href="#">2459400</a>	Simulink Test - Creating tests for model component workflow fails when the model has export function containing Simulink Functions	22 Sep 2021
<a href="#">2470096</a>	Simulink Test - Setting the Test Sequence block path in the Test Manager Inputs section makes MATLAB unresponsive	22 Sep 2021
<a href="#">2429169</a>	Simulink Test - Test files with more than 1000 test suites cannot be opened	22 Sep 2021
<a href="#">2544869</a>	Simulink Test - Running sldvcompat on an externally saved test harness might error	22 Sep 2021
<a href="#">2420217</a>	Simulink Test - Override SIL/PIL mode setting might be incorrect in the test result report	11 Feb 2021
<a href="#">2249557</a>	Simulink Test - Running a test file containing test cases with external test harnesses that contain a Signal Builder block might error	11 Jun 2020
<a href="#">2248003</a>	Simulink Test - Testing a component in a library when simulation mode is overridden to not use model settings might fail	11 Jun 2020
<a href="#">2239108</a>	Simulink Test - Test execution compiles model multiple times	11 Jun 2020
<a href="#">2237774</a>	Simulink Test - Dragging ports of a subsystem interface might cause lost connections in associated test harnesses	11 Jun 2020
<a href="#">2112483</a>	Simulink Test - Test that overrides Signal Editor scenario and includes inputs in the results produces an	13 Dec

	error	2019
<a href="#">2114999</a>	Simulink Test - Running test harnesses using Run with Stepper button on toolbar is not supported	13 Dec 2019
<a href="#">2120213</a>	Simulink Test - Comparison results for complex signals produce "Signals not aligned" warning	13 Dec 2019

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#### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

#### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### Display bug reports for Simulink Requirements

Display bug reports for Simulink Requirements (R2021a) available at <https://www.mathworks.com/support/bugreports>.

**NOTE:** This check does not determine whether your model might be affected by these bugs.

#### Warning

There are 30 Simulink Requirements bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2719309</a>	Requirements Toolbox - Change tracking, implementation and verification status degrade Requirements Editor performance	21 Apr 2022
<a href="#">2713025</a>	Simulink Coverage - Simulink Coverage does not report the implicit default case as a decision outcome	08 Apr 2022
<a href="#">2426702</a>	Requirements Toolbox - Loading requirement set unexpectedly loads additional requirement sets	17 Mar 2022
<a href="#">2548532</a>	Requirements Toolbox - Summary of requirements in exported ReqIF file contains escaped HTML tags in plain text after import from PREEvision	09 Mar 2022
<a href="#">2583147</a>	Simulink Check - Metrics Dashboard and model metrics API use incorrect bin edges for metric distributions when the metric results are close in value	09 Mar 2022
<a href="#">2579212</a>	Requirements Toolbox - Error when importing requirements from IBM DOORS Next	09 Mar 2022
<a href="#">2595797</a>	Requirements Toolbox - Navigation link from external document to MATLAB/Simulink does not work	09 Mar 2022
<a href="#">2585311</a>	Requirements Toolbox - Attributes lost after updating requirements imported from IBM DOORS Next	09 Mar 2022
<a href="#">2578661</a>	Requirements Toolbox - Unable to select Global Configuration when configuring MATLAB session for IBM DOORS Next integration	09 Mar 2022
<a href="#">2626349</a>	Requirements Toolbox - Custom attribute values are deleted when you move a requirement by clicking and dragging	09 Mar 2022
<a href="#">2599623</a>	Requirements Toolbox - Copying and pasting a requirement does not copy custom attributes	09 Mar 2022
<a href="#">2559487</a>	Requirements Toolbox - Bulleted list formatting lost when importing requirements from Microsoft Word	17 Feb 2022
<a href="#">2433114</a>	Requirements Toolbox - Requirements comments in generated code do not show linked requirement summary	22 Sep 2021
<a href="#">2441988</a>	Simulink Coverage - Generating a code coverage report using cvhtml might cause MATLAB to crash	22 Sep 2021
<a href="#">2437043</a>	Simulink Coverage - Error when alternating coverage collection between normal simulation mode and SIL/PIL	22 Sep 2021
<a href="#">2466226</a>	Requirements Toolbox - Error when attempting to import a DOORS 9 requirements module	22 Sep 2021
<a href="#">2386157</a>	Requirements Toolbox - Requirement links are incorrectly copied from Subsystem Reference blocks to instances in Simulink models	22 Sep 2021
<a href="#">2440193</a>	Requirements Toolbox - False positive indication of updated referenced requirements when updating from some ReqIF files	22 Sep 2021
<a href="#">2447343</a>	Requirements Toolbox - show() object function does not open OSLC resources in specified configuration context	22 Sep 2021
<a href="#">2431402</a>	Requirements Toolbox - queryRequirements() object function for oslc.core.QueryCapability object	22 Sep 2021

	disregards query parameter	2021
<a href="#">2404198</a>	Requirements Toolbox - When importing multiple ReqIF specifications into one requirement set, manually mapping attributes only works for one specification	22 Sep 2021
<a href="#">2483880</a>	Simulink Coverage - Generating coverage report takes very long time	22 Sep 2021
<a href="#">2438495</a>	Requirements Toolbox - MATLAB crashes when using Simulink Requirements with multiple folders with large number of linked files	22 Sep 2021
<a href="#">2247892</a>	Requirements Toolbox - Traceability Matrix does not render link icons correctly	11 Jun 2020
<a href="#">2247724</a>	Requirements Toolbox - Failure to connect with IBM DOORS Next (DNG) when importing requirements	11 Jun 2020
<a href="#">2210749</a>	Requirements Toolbox - 3rd-Party requirements tool does not accept ReqIF exported by Simulink Requirements	11 Jun 2020
<a href="#">2232550</a>	Requirements Toolbox - Displayed column widths in Requirements Editor might be reset	14 May 2020
<a href="#">2205640</a>	Requirements Toolbox - MATLAB crashes while updating requirement from IBM DOORS Next server	22 Apr 2020
<a href="#">2163041</a>	Requirements Toolbox - Missing requirement links for Stateflow objects in library after resolve-push	06 Feb 2020
<a href="#">1970160</a>	Requirements Toolbox - Error when clicking Show in document for references imported from IBM Rational DOORS Next Generation module	24 Apr 2019

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#### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

#### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

#### Display bug reports for AUTOSAR Blockset

Display bug reports for AUTOSAR Blockset (R2021a) available at <https://www.mathworks.com/support/bugreports> .  
**NOTE:** This check does not determine whether your model might be affected by these bugs.

#### Warning

There are 19 AUTOSAR Blockset bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2729301</a>	AUTOSAR Blockset - Incorrect Code Generation: AUTOSAR Classic model hierarchies that reference models containing global Simulink functions might generate incorrect code	14 Jul 2022
<a href="#">2447659</a>	AUTOSAR Blockset - Error during ARXML import with schema notations of style 000xx for Classic AUTOSAR	27 Jun 2022
<a href="#">2601633</a>	AUTOSAR Blockset - Incorrect Code Generation: IRVs in AUTOSAR exported function model might result in incorrect code when configured for expression folding	09 Mar 2022
<a href="#">2597436</a>	AUTOSAR Blockset - Review Manager not displayed in AUTOSAR architecture model toolbar	09 Mar 2022
<a href="#">2566047</a>	AUTOSAR Blockset - SwSystemConstantValueSets contains NaN values in generated ARXML	09 Mar 2022
<a href="#">2637341</a>	AUTOSAR Blockset - MATLAB may crash for AUTOSAR models while navigating the Code Mappings Editor	31 Jan 2022
<a href="#">2483786</a>	AUTOSAR Blockset - ARXML ImplementationDataTypes and Rte_Type.h type definitions are not generated for some AUTOSAR lookup tables	27 Jan 2022
<a href="#">2562861</a>	AUTOSAR Blockset - Adaptive AUTOSAR header file for stringview uses wrong file name	25 Jan 2022
<a href="#">2440939</a>	AUTOSAR Blockset - Import or export of AUTOSAR architecture models with execution order constraints may fail for branched inputs	25 Jan 2022
<a href="#">2426983</a>	AUTOSAR Blockset - Build failure for AUTOSAR model with unparented variation points	22 Sep 2021
<a href="#">2429904</a>	AUTOSAR Blockset - Short name uniqueness error after consecutive builds of AUTOSAR component model that inherits architecture model XML options	22 Sep 2021
<a href="#">2477623</a>	AUTOSAR Blockset - AUTOSAR Dictionary left-hand tree view not updated when interfaces renamed in	22 Sep

	list view	2021
<a href="#">2496727</a>	AUTOSAR Blockset - MATLAB crashes during update diagram of AUTOSAR model if bus element Import and bus element Outport are directly connected	22 Sep 2021
<a href="#">2451417</a>	AUTOSAR Blockset - AUTOSAR map block: error message for invalid fixed point word length configuration of two input ports is not clear	22 Sep 2021
<a href="#">2536571</a>	AUTOSAR Blockset - Code generation fails for AUTOSAR adaptive version R19-11 when used with third-party ara libraries	22 Sep 2021
<a href="#">2134859</a>	AUTOSAR Blockset - Component creation from ARXML fails if ClientServerOperation arguments are 64-bit integers	30 Jan 2020
<a href="#">2160270</a>	AUTOSAR Blockset - MATLAB crashes during code generation for an AUTOSAR model that has mapped signals, states, or data stores	30 Jan 2020
<a href="#">2118436</a>	AUTOSAR Blockset - Incorrect Code Generation: Incorrect event data received in AUTOSAR adaptive model when Message Receive block specifies Use initial value for Value source when queue is empty	13 Dec 2019
<a href="#">2123864</a>	AUTOSAR Blockset - AUTOSAR model build fails with "Unrecognized function or variable 'calPrmGraphicalName'"	13 Dec 2019

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### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### *Input Parameters Selection*

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	

### Display bug reports for HDL Coder

Display bug reports for HDL Coder (R2021a) available at <https://www.mathworks.com/support/bugreports> .

**NOTE:** This check does not determine whether your model might be affected by these bugs.

### Warning

There are 130 HDL Coder bug reports for release R2021a

ID	Bug Report Summary	Modified
<a href="#">2706749</a>	HDL Coder - Steaming and resource sharing optimization report can have incomplete information when requesting both optimizations on a subsystem	21 Jul 2022
<a href="#">2763512</a>	HDL Coder - Incorrect Code Generation: Multi-Input Sum Block configured with vector/Matrix inputs may results in a numerical mismatch	10 Jul 2022
<a href="#">2770189</a>	HDL Coder - Incorrect Code Generation: The model parameter Allow clock-rate pipelining of DUT output ports can result in inconsistent I/O between the original model and HDL code	24 Jun 2022
<a href="#">2700793</a>	HDL Coder - HDL code generation throws unintuitive message for DUT containing For Each Subsystem that has Show partition index output port enabled	22 Jun 2022
<a href="#">2506983</a>	HDL Coder - HDL code generation may fails when your Simulink model contains floating-point bus elements other than native floating point (NFP).	15 Jun 2022
<a href="#">2552498</a>	HDL Coder - HDL Workflow Advisor errors out when using DAC Sampling frequency greater than 7000 MHz in the IP Core Generation Reference Design Workflow	13 Jun 2022
<a href="#">2737456</a>	HDL Coder - HDL FIFO block generates an incorrect HDL code when the configuration parameters Minimize clock enables and Minimize global resets are enabled.	06 Jun 2022
<a href="#">2719205</a>	HDL Coder - Incorrect Code Generation: Some statements that include combinations of casting, rescaling, and bit concatenation operations using the rescale and bitconcat functions lead to incorrect code generation.	26 May 2022
<a href="#">2723825</a>	HDL Coder - Internal error thrown when generating code for delay block with enable port	22 Apr 2022
<a href="#">2432840</a>	HDL Coder - HDL Reciprocal block or Math Reciprocal block with Newton-Raphson Algorithm method generates incorrect HDL code for the fixed point inputs containing negative fraction length	12 Apr 2022
<a href="#">2375804</a>	HDL Coder - Assertion or MATLAB crash during HDL code generation in the presence of unconnected output ports of Stateflow charts with input events.	06 Apr 2022
<a href="#">2510146</a>	HDL Coder - Code generation might fail when SubsystemReuse parameter is set to 'Atomic and Virtual'	05 Apr 2022
<a href="#">2489534</a>	HDL Coder - Demux block with partially dead output ports might produce disconnected cosimulation model and incorrect test bench	05 Apr 2022
<a href="#">2653212</a>	HDL Coder - Incorrect Code Generation: When generating HDL code using the "MATLAB Datapath" architecture of a MATLAB function block, HDL Coder can produce incorrect code for logical indexing	04 Apr 2022

	expressions used in size identification functions	
<a href="#">2531427</a>	HDL Coder - HDL Workflow Advisor may display incorrect target frequency when switching workflows	04 Apr 2022
<a href="#">2516457</a>	HDL Coder - Invalid HDL code can be generated from General CRC Syndrome Detector HDL Optimized	04 Apr 2022
<a href="#">2507366</a>	HDL Coder - Incorrect HDL code might be generated for a model that contains Blackbox subsystem followed by Complex to Real-Imag block	04 Apr 2022
<a href="#">2506984</a>	HDL Coder - HDL code generation may fail for a referenced model when the port name ends with a space	04 Apr 2022
<a href="#">2499441</a>	HDL Coder - HDL code generation may fail for Simple Dual Port RAM block when Address port width is set with variable under certain conditions	04 Apr 2022
<a href="#">2493396</a>	HDL Coder - Duplicate HDL files are generated for identical atomic subsystems inside two or more different hierarchical paths of conditional networks	04 Apr 2022
<a href="#">2487678</a>	HDL Coder - MATLAB Function containing bitorreduce, bitandreduce or bitxorreduce might produce HDL code that causes compilation error	04 Apr 2022
<a href="#">2464359</a>	HDL Coder - Incorrect Code Generation: Incorrect HDL code may be generated for a trigger subsystem with non-boolean trigger value	04 Apr 2022
<a href="#">2456308</a>	HDL Coder - Unexpected error during HDL code generation for Stateflow Blocks containing enumerated data with specified initial values	04 Apr 2022
<a href="#">2649320</a>	HDL Coder - Incorrect code generation: Matrix gain ( $K^*u$ ) operations with scalar output might produce simulation mismatch between the original model and generated model	04 Apr 2022
<a href="#">2604240</a>	HDL Coder - Unsupported blocks for HDL Coder with a rate change can cause an unintuitive error while balancing delays during HDL code generation	25 Mar 2022
<a href="#">2646509</a>	HDL Coder - Math Function block with reciprocal function generates an incorrect HDL code for the fixed-point inputs that have fractional length greater than word length.	24 Mar 2022
<a href="#">2686975</a>	HDL Coder - Incorrect Code Generation: IP core register initial value is incorrect for fixed-point values with non-zero fraction length	23 Mar 2022
<a href="#">2678715</a>	HDL Coder - An error during HDL code generation can occur for a model that has streaming on and contains an Assignment block	21 Mar 2022
<a href="#">2690437</a>	HDL Coder - Internal assertion hit while generating HDL code for design using clock-rate pipelining and locally disabled delay balancing	16 Mar 2022
<a href="#">2683861</a>	HDL Coder - Signal logging prevents code reuse in HDL Code generation from Simulink models	11 Mar 2022
<a href="#">2481452</a>	HDL Coder - Incorrect Code Generation: Selecting a "Device Vendor" other than "ASIC/FPGA" can lead to generation of mismatching HDL code for models containing Stateflow Charts.	09 Mar 2022
<a href="#">2411786</a>	HDL Coder - Streaming optimization doesn't support MinMax block.	09 Mar 2022
<a href="#">2438689</a>	HDL Coder - Incorrect Code Generation: Nested if statements or switch statements may generate incorrect HDL Code when used in System objects or with optimizations enabled	09 Mar 2022
<a href="#">2469717</a>	HDL Coder - Incorrect Code Generation: Stateflow charts or MATLAB Function blocks containing persistent variables might produce incorrect HDL code because of pipeline latencies	09 Mar 2022
<a href="#">2505523</a>	HDL Coder - Compilation failure for generated Speedgoat interface model when the port is boolean/ufix data type	09 Mar 2022
<a href="#">2503282</a>	HDL Coder - Code generation may take long time with For Each subsystems connected to terminator blocks	09 Mar 2022
<a href="#">2488883</a>	HDL Coder - Generated model after HDL code generation may give incorrect results when Clock-rate pipelining and Sharing optimizations turned on.	09 Mar 2022
<a href="#">1916569</a>	HDL Coder - Incorrect Code Generation: Streaming/Sharing with Hierarchical Clock-Rate Pipelining can cause validation model mismatch	09 Mar 2022
<a href="#">2357112</a>	HDL Coder - The CPE report in HDL Codegen shows one or more blocks with timing delay, but clicking on on highlightCriticalPathEstimationOffendingBlocks.m script returns "Block not characterized".	09 Mar 2022
<a href="#">2509545</a>	HDL Coder - Resource utilization report does not report the data size of RAM accurately for complex inputs	09 Mar 2022
<a href="#">2503862</a>	HDL Coder - Using From/Goto blocks in HDL Coder may result in a crash	09 Mar 2022
<a href="#">2432905</a>	HDL Coder - Unsupported MATLAB-authored system objects throw ambiguous error during HDL code generation	09 Mar 2022
<a href="#">2496958</a>	HDL Coder - Write firmware to SD card step errors out in HDL Coder Support Package for Xilinx RFSoC Devices	09 Mar 2022
<a href="#">2517765</a>	HDL Coder - HDL Workflow Advisor with Simulink Real-time workflow generates empty software interface model when using the command-line (hdlcoder.runworkflow)	09 Mar 2022
<a href="#">2538817</a>	HDL Coder - The generated model may fail to compile for Bus Creator or Bus Selector blocks containing named signals	09 Mar 2022
<a href="#">2538599</a>	HDL Coder - Generated VHDL can show out of bounds errors can occur during simulation at time step 0	09 Mar 2022
<a href="#">2402935</a>	HDL Coder - Gain block with boolean output type generates incorrect HDL code	09 Mar 2022

<a href="#">2503855</a>	HDL Coder - Generated HDL code may not preserve user-provided index guarding logic	09 Mar 2022
<a href="#">2479571</a>	HDL Coder - Incorrect Code Generation: Generated code results might be incorrect when solver setting Automatically handle rate transition for data transfer is enabled	09 Mar 2022
<a href="#">2547316</a>	HDL Coder - The Scalarize Ports option in MATLAB HDL workflow advisor does not have impact on generated HDL code	09 Mar 2022
<a href="#">1860261</a>	HDL Coder - Incorrect Code Generation: FIR Rate Conversion HDL Optimized block may generate incorrect HDL code if it uses the request port	09 Mar 2022
<a href="#">2451234</a>	HDL Coder - MATLAB Function block with input names same as enumeration member names has name conflict in generated VHDL	09 Mar 2022
<a href="#">2463077</a>	HDL Coder - Incorrect Code Generation: Triggered Subsystem that has no HDL optimizations configured, generates an incorrect HDL code	09 Mar 2022
<a href="#">2479593</a>	HDL Coder - Incorrect Code Generation: Incorrect HDL code generation for a subsystem in the presence of Bus signals with fixed-point datatypes configured with 'Slope and Bias'.	09 Mar 2022
<a href="#">2575146</a>	HDL Coder - 1D - Direct Lookup Table(n-D) block that has Diagnostic for out-of-range input parameter set to 'Warning' or 'None' generates incorrect HDL code.	09 Mar 2022
<a href="#">2457738</a>	HDL Coder - Incorrect Code Generation: Accumulation operations modeled in Simulink using feedback loops may cause latency related HDL mismatch	09 Mar 2022
<a href="#">2616442</a>	HDL Coder - HDL code generation failure from a top-level model that contains a protected model	09 Mar 2022
<a href="#">2560618</a>	HDL Coder - Selector block configured with the large size matrix as input, generates out of memory error during HDL code generation	09 Mar 2022
<a href="#">2550753</a>	HDL Coder - Running Back Annotation may fail in Pre_route mode	09 Mar 2022
<a href="#">2656440</a>	HDL Coder - HDL IP core generation using Xilinx Vivado fails as of January 1, 2022	09 Mar 2022
<a href="#">2557181</a>	HDL Coder - HDL Code generation hangs and runs out of memory for the large Simulink model that uses critical path estimation	09 Mar 2022
<a href="#">2480051</a>	HDL Coder - Traceability links for the Delay blocks followed by Lookup Table blocks are broken when the DistributedPipelining setting for the parent subsystem is changed	09 Mar 2022
<a href="#">2649909</a>	HDL Coder - HDL Workflow Advisor errors out when the input port of AXI4-Stream Slave is mapped to a vector and the output port of AXI4-Stream Master is mapped to a scalar.	09 Mar 2022
<a href="#">2535544</a>	HDL Coder - Generated model from the Subsystem that has Repeat block may fail to compile when Allow tasks to execute concurrently on target is selected	09 Mar 2022
<a href="#">2537234</a>	HDL Coder - HDL code generation may fail when a referenced model has bus ports with multiple connections	09 Mar 2022
<a href="#">2599107</a>	HDL Coder - For a DUT containing only combinatorial logic, the generated IP core using the HDL Coder IP core generation workflow contains unconnected clock and reset signals	09 Mar 2022
<a href="#">2575081</a>	HDL Coder - HDL code generation errors out when using a masked subsystem that contains the self-modifiable masks	09 Mar 2022
<a href="#">2439242</a>	HDL Coder - Incorrect Code Generation: Latency upstream from a feedback loop might cause a numeric mismatch when the feedback loop traverses through hierarchy.	09 Mar 2022
<a href="#">2530537</a>	HDL Coder - Dot indexing error while running Hardware Setup Wizard for HDL Coder Support Package for Xilinx RFSoC Devices	09 Mar 2022
<a href="#">2671859</a>	HDL Coder - Incorrect Code Generation: Single-rate sharing/streaming can cause a silent wrong answer when the clock-rate region also has extra clock-rate latency	28 Feb 2022
<a href="#">2670282</a>	HDL Coder - Sharing can result in numeric mismatch when there are hierarchies inside the subsystems to be shared	07 Feb 2022
<a href="#">2424195</a>	HDL Coder - HDL Code generation crashes on modeling the 'Reshape' Block with Higher dimensional matrices.	04 Feb 2022
<a href="#">2645810</a>	HDL Coder - HDL Workflow Advisor Task 4.2 (Generate Software Interface) fails for library and subsystem reference blocks	01 Feb 2022
<a href="#">2618333</a>	HDL Coder - Incorrect Code Generation: In single clock mode, the timing controller can be incorrectly generated when requesting Oversampling and not utilizing Clock-Rate Pipelining.	31 Jan 2022
<a href="#">2590335</a>	HDL Coder - Incorrect Code Generation: MATLAB Function blocks in feedback loops which introduce latency can cause Modelsim failure	31 Jan 2022
<a href="#">2433888</a>	HDL Coder - Incorrect Code Generation: Subsystems with feedback loops might cause a numeric mismatch between the original and generated model when resource sharing is enabled and latency is introduced upstream	31 Jan 2022
<a href="#">2657136</a>	HDL Coder - An error during HDL code generation might occur for models with code reuse and clock-rate pipelining on	31 Jan 2022
<a href="#">2636492</a>	HDL Coder - HDL Coder generates an internal assertion when Flatten Hierarchy is turned On for the Filter blocks.	31 Jan 2022
<a href="#">2532541</a>	HDL Coder - Simscape HDL Workflow Advisor might generate an unexpected error about inconsistent array dimensions	25 Jan 2022
<a href="#">2052966</a>	HDL Coder - Incorrect Code Generation: Native floating-point with hierarchical clock-rate pipelining might cause validation model mismatch	25 Jan 2022

<a href="#">2528868</a>	HDL Coder - Incorrect precision used for fixed-point arithmetic for Stateflow charts using C action language	24 Jan 2022
<a href="#">2420776</a>	HDL Coder - Generated co-simulation model for a Model Reference block may have compilation issues when a referenced model contains the tunable parameters	21 Jan 2022
<a href="#">2479952</a>	HDL Coder - Traceability link for the Lookup Table block is broken in HDL Code Generation report when the input to the block is a vector	21 Jan 2022
<a href="#">2453594</a>	HDL Coder - Subsystems containing Bus Creator, Bus Selector or Bus Assignment blocks generates 'Delay balancing unsuccessful' error message	21 Jan 2022
<a href="#">2460920</a>	HDL Coder - Bus Expansion subsystem containing delay-block modeled using state-control semantics results in numeric mismatch	21 Jan 2022
<a href="#">2410574</a>	HDL Coder - 'Multi-Input Sum Block' that has configured with complex vector or matrix inputs results in an internal assertion	22 Oct 2021
<a href="#">2315303</a>	HDL Coder - Incorrect Code Generation: HDL code generated for integer division by a constant power of two in MATLAB code might be incorrect	22 Sep 2021
<a href="#">2322712</a>	HDL Coder - Numerical difference between Simulink and HDL Coder in Sqrt block for certain input values and datatypes.	22 Sep 2021
<a href="#">2272974</a>	HDL Coder - HDL blocks with 'inf' sample time may result in delay balancing failure.	22 Sep 2021
<a href="#">2226678</a>	HDL Coder - Incorrect Code Generation: Accumulation operations modeled in Simulink using feedback loops might cause HDL mismatch due to latency introduced by optimizations upstream	22 Sep 2021
<a href="#">2228896</a>	HDL Coder - Inefficient code generation for models with Clock Rate Pipelining and SqrtNewton architectures	22 Sep 2021
<a href="#">2258039</a>	HDL Coder - Incorrect Code Generation: Distributed Pipelining adds invalid registers when a model contains non-constant source blocks	22 Sep 2021
<a href="#">2300111</a>	HDL Coder - Incorrect Code Generation: Stateflow charts with nonzero initial values for outputs and containing input events might generate incorrect HDL code	22 Sep 2021
<a href="#">2403722</a>	HDL Coder - Task 4.2 Run Synthesis may fail in HDL Workflow advisor when using any version including and after Microsemi Libero SoC 12.1	22 Sep 2021
<a href="#">2401827</a>	HDL Coder - Generated VHDL code might fail to compile because of collision between component type names and the library name	22 Sep 2021
<a href="#">2372622</a>	HDL Coder - Internal error during HDL code generation from subsystem containing DocBlock in Native Floating Point mode	22 Sep 2021
<a href="#">2354425</a>	HDL Coder - Incorrect Code Generation: Using non-fixed-point constant literals in fixed-point division in Stateflow charts might cause incorrect HDL code generation	22 Sep 2021
<a href="#">2346684</a>	HDL Coder - Incorrect Code Generation: Missing zero protection logic in the MATLAB to HDL workflow may result in simulation mismatch or out of bounds indexing errors	22 Sep 2021
<a href="#">2302712</a>	HDL Coder - Multicycle path constraint file may not be generated when clock rate pipelining is requested on a model	22 Sep 2021
<a href="#">2421048</a>	HDL Coder - Delay balancing on the system might not work if global delay balancing is disabled and the model uses scoped delay balancing	22 Sep 2021
<a href="#">2413512</a>	HDL Coder - Incorrect Code Generation: HDL Code generated from a MATLAB code containing division by a constant may result in incorrect code	22 Sep 2021
<a href="#">2321239</a>	HDL Coder - MATLAB may crash or take long time during HDL code generation from a subsystem with a large matrix multiplication	22 Sep 2021
<a href="#">2441794</a>	HDL Coder - SinCos Cordic blocks do not participate in distributed pipelining if Distributed pipelining priority is set to Performance.	22 Sep 2021
<a href="#">2440476</a>	HDL Coder - Simscape HDL Workflow Advisor fails to generate the implementation model when the Simscape model contains model references	22 Sep 2021
<a href="#">2430694</a>	HDL Coder - HDL code generation may fail for a subsystem containing a referenced model with certain bus configurations when traceability is enabled	22 Sep 2021
<a href="#">2419461</a>	HDL Coder - HDL Code generation may cause assertion for a switch case block with fixed-point type in case values	22 Sep 2021
<a href="#">2434295</a>	HDL Coder - Test bench generation may fail when the bus selector block output signal having a name is connected to the bus creator block.	22 Sep 2021
<a href="#">2440547</a>	HDL Coder - Masked subsystems containing logic that folds into a constant value during HDL code generation causes code generation assertion	22 Sep 2021
<a href="#">2444302</a>	HDL Coder - Incorrect Code Generation: Incorrect PCIe register initial value for vectors with data type <= 16 bits	22 Sep 2021
<a href="#">2464503</a>	HDL Coder - Code generation may take a long time when too many mask variables are present a model	22 Sep 2021
<a href="#">2437518</a>	HDL Coder - Incorrect error message shown in HDL Workflow Advisor for FPGA-in-the-Loop workflow	22 Sep 2021
<a href="#">2231964</a>	HDL Coder - Arithmetic operators with half precision floating point types generates incorrect HDL code when input/output numbers falls under denormal range.	22 Sep 2021
<a href="#">2435457</a>	HDL Coder - Incorrect Code Generation: Fixed-point division that uses certain rounding methods might produce incorrect HDL code	22 Sep 2021
<a href="#">2469804</a>	HDL Coder - Unexpected pipelining interactions between AdaptivePipelining and MapToRAM	22 Sep

			2021
<a href="#">2460391</a>	HDL Coder - HDL code generation might cause assertion for nested if statements in which the outer if condition is a comparison to 0		22 Sep 2021
<a href="#">2456091</a>	HDL Coder - HDL Coder throws internal error during Model Generation when using buses and Clock Rate Pipelining		22 Sep 2021
<a href="#">2441052</a>	HDL Coder - High memory consumption during HDL code generation for large models resulting from Simscape to HDL workflow		22 Sep 2021
<a href="#">2411862</a>	HDL Coder - Step 5.1 Generate Simulink Real-Time interface in HDL Workflow Advisor fails for models with bus data types		22 Sep 2021
<a href="#">2403895</a>	HDL Coder - Stateflow charts containing bitconcat of constant values might produce HDL code that causes compilation error		22 Sep 2021
<a href="#">2472499</a>	HDL Coder - Unable to change HDL settings or run HDL WA if slrealtime.tlc is selected		22 Sep 2021
<a href="#">2457774</a>	HDL Coder - Simulation mismatches for the selector block		19 Apr 2022
<a href="#">2432516</a>	HDL Coder - cordicsin and cordiccos functions may throw error during HDL code generation with fixed-point conversion		22 Sep 2021
<a href="#">2388419</a>	HDL Coder - Internal error generated during HDL code generation from 1-D Lookup Table block when data types of Input and Breakpoints 1 parameter are different		22 Sep 2021
<a href="#">2535370</a>	HDL Coder - Adaptive Pipelining does not insert pipelines for Microsemi Libero SoC or Intel Quartus Pro devices		22 Sep 2021
<a href="#">2467814</a>	HDL Coder - Incorrect Code Generation: 'Multi-Input Sum Block' configured with vector inputs may results in a numerical mismatch.		22 Sep 2021
<a href="#">2331912</a>	HDL Coder - Assigning an array to a bus output from persistent variable in MATLAB Function block may generate extra variables in VHDL		21 Dec 2020
<a href="#">2214596</a>	HDL Coder - Error when generating VHDL test bench for model reference used as DUT with mismatch in value of ScalarizePorts property between the top model and referenced model		11 Jun 2020
<a href="#">2214587</a>	HDL Coder - Incorrect Code Generation: Potentially incorrect VHDL test bench code generated for multirate design with a vector port named phase at DUT interface and ScalarizePorts parameter set to on		11 Jun 2020
<a href="#">2082623</a>	HDL Coder - HDL implementation model with validation logic can generate assertions during simulation for default Validation logic tolerance setting		11 Jun 2020
<a href="#">2005355</a>	HDL Coder - Incorrect Code Generation: Sharing with certain configurations of enabled subsystems results in a mismatch in the validation model		16 Aug 2019

[^ Less](#)

### Recommended Action

To determine if your model is affected by the bugs, review the bug report descriptions and workarounds in the links provided in the ID column.

### Input Parameters Selection

Name	Value
Only show bug reports modified after date(mm/dd/yyyy)	