

信息技术导论

——微控制器基础知识

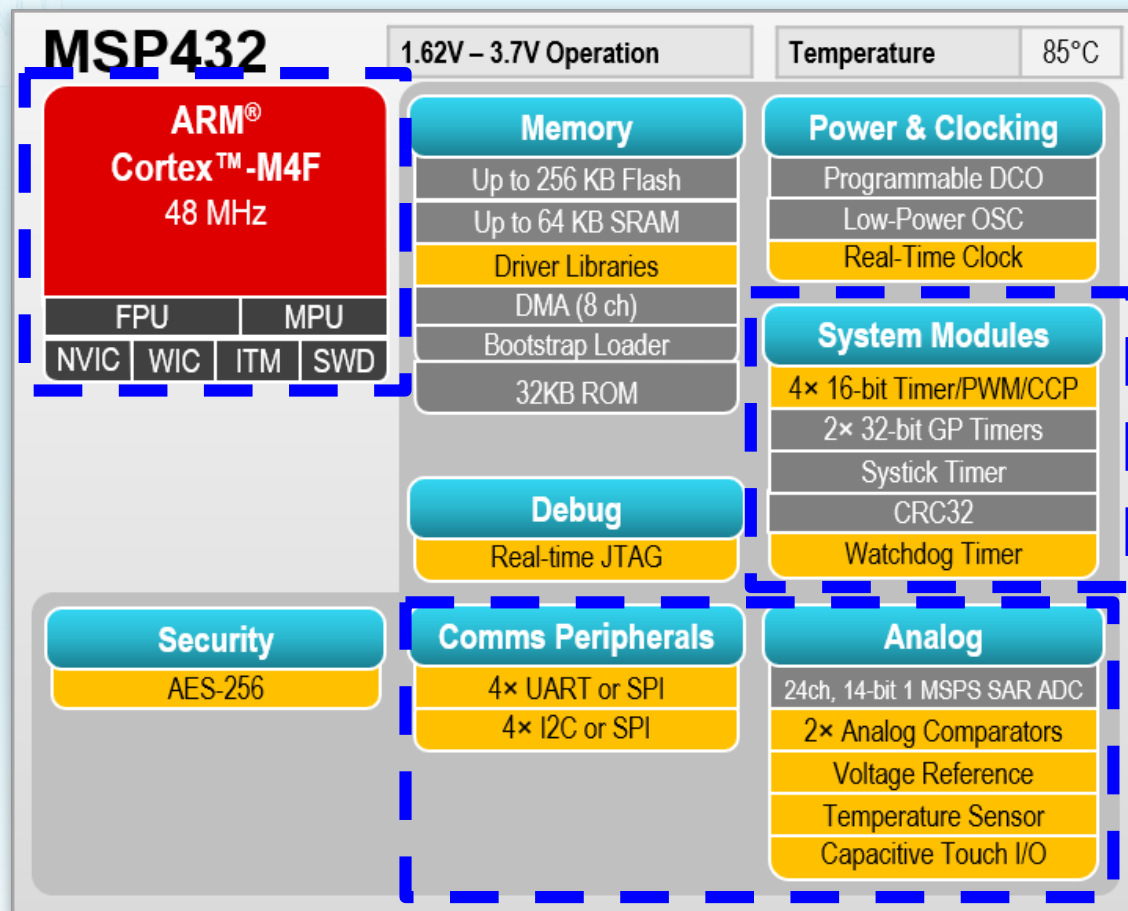
华中科技大学电信学院
2021级





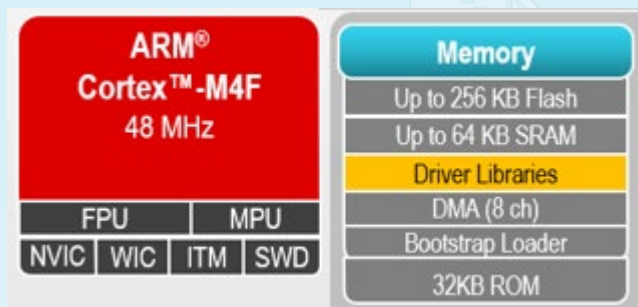
PART1 MSP432介绍

系统框架



控制器

处理器，主频，存储



类比

关于

系统正在监控并保护你的电脑。

[在 Windows 安全中心中查看详细信息](#)

设备规格

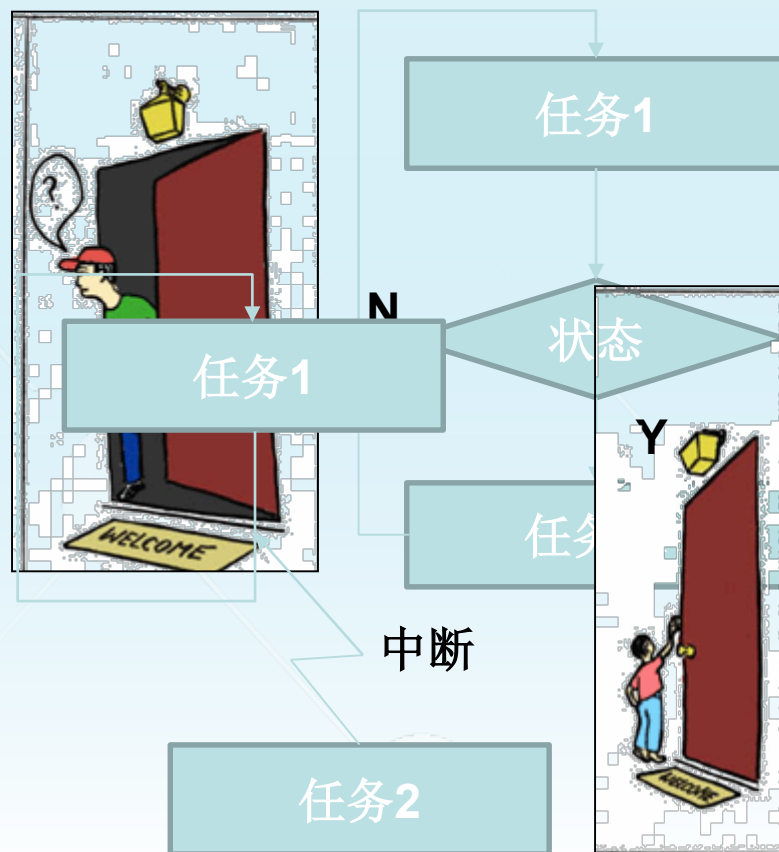
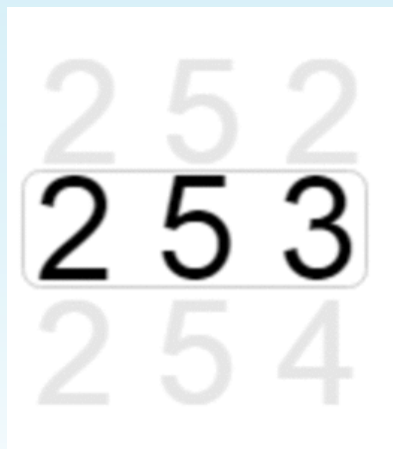
设备名称	DESKTOP-0A45KL5
处理器	AMD Ryzen 5 2500U with Radeon Vega Mobile Gfx 2.00 GHz
机带 RAM	8.00 GB (7.63 GB 可用)
设备 ID	2882508F-8725-47F6-86EC-122976F5CE80
产品 ID	00342-35298-55610-AAOEM
系统类型	64 位操作系统, 基于 x64 的处理器
笔和触控	没有可用于此显示器的笔或触控输入

复制

定时器与中断



System Modules
4× 16-bit Timer/PWM/CCP
2× 32-bit GP Timers
Systick Timer
CRC32
Watchdog Timer



外设接口



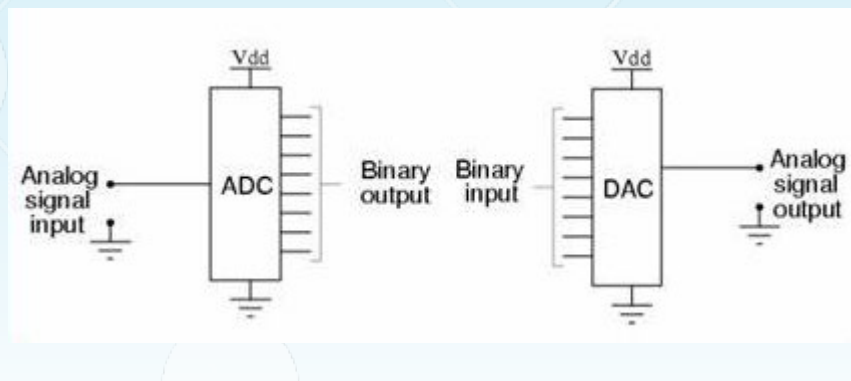
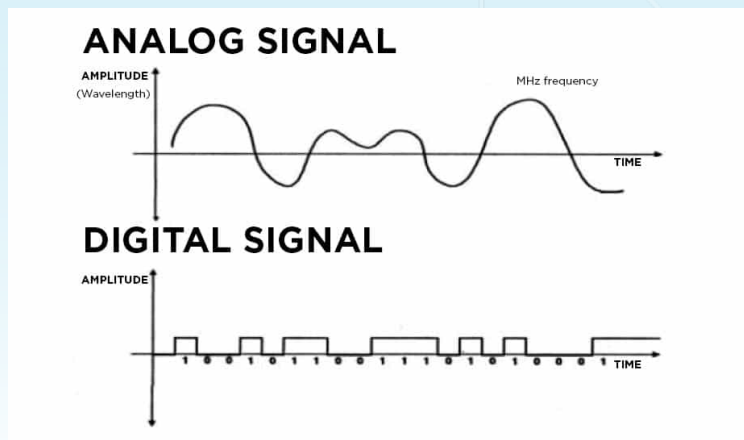
模拟与数字

输入输出 (Input/Output, I/O)

模数转换器(Analog to Digital Converter, ADC)

数模转换器(Digital to Analog Converter, DAC)

Analog
24ch, 14-bit 1 MSPS SAR ADC
2× Analog Comparators
Voltage Reference
Temperature Sensor
Capacitive Touch I/O



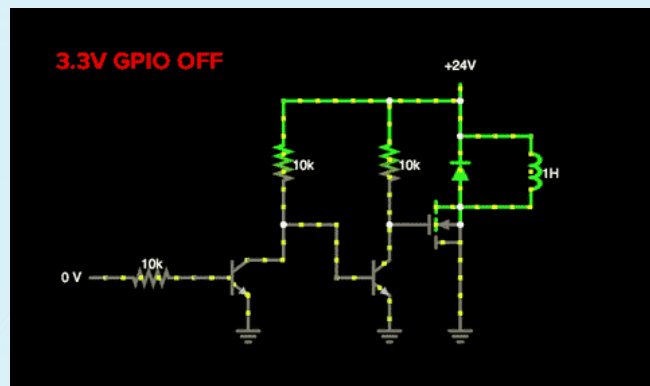


PART 2 GPIO介绍

引脚介绍

GPIO(General-Purpose Input/Output)

- 多达11个数字I / O端口(Port)，大多数端口包含8条I / O线(Pin)。
- 每条I / O线均可分别配置为输入或输出方向、单独读取或写入、分别配置为上拉或下拉电阻



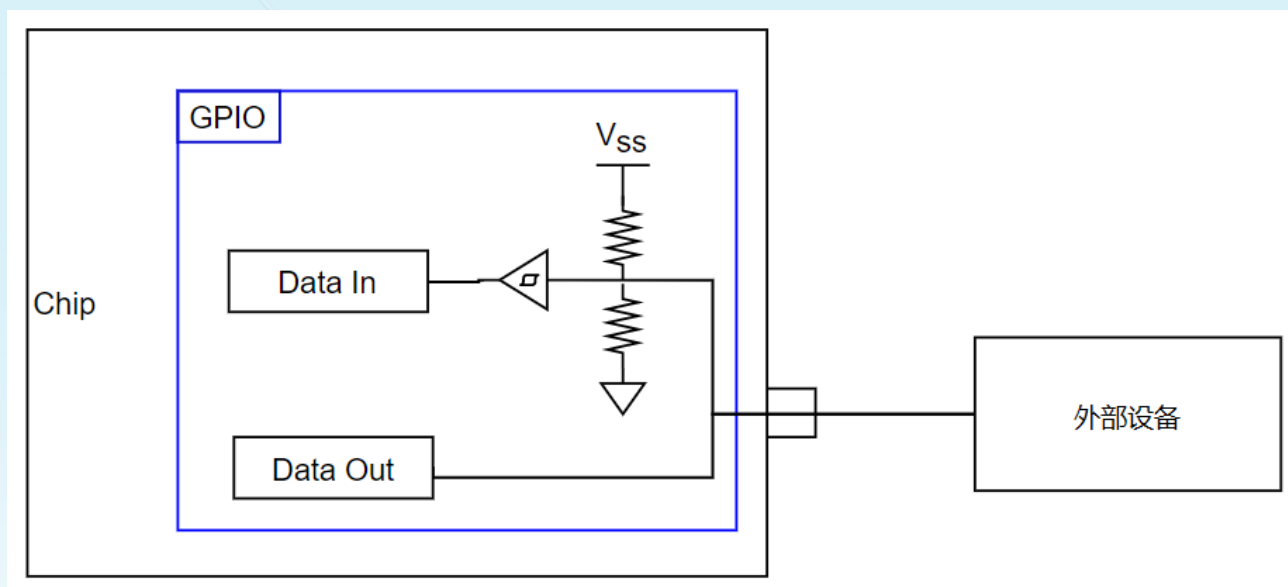
PxDIR	PxREN	PxOUT	I/O Configuration
0	0	x	Input
0	1	0	Input with pulldown resistor
0	1	1	Input with pullup resistor
1	x	x	Output

PxSEL1	PxSEL0	I/O Function
0	0	General purpose I/O is selected
0	1	Primary module function is selected
1	0	Secondary module function is selected
1	1	Tertiary module function is selected

引脚介绍

GPIO 有三种状态，高(High)、低(Low)、高阻(Hi-Z)。

当不进行上拉或下拉时，GPIO悬空，状态由外部环境确定，容易导致误触发。



pull-up 表示预设为 High，适合状态将拉低的外设；

pull-down 表示预设为 Low，适合状态将拉高的外设。

引脚应用



```
volatile uint32_t i;

// Stop watchdog timer
WDT_A_hold(WDT_A_BASE);

// Set P1.0 to output direction
GPIO_setAsOutputPin(
    GPIO_PORT_P1,
    GPIO_PIN0
);

while(1)
{
    // Toggle P1.0 output
    GPIO_toggleOutputOnPin(
        GPIO_PORT_P1,
        GPIO_PIN0
    );
    // Delay
    for(i=100000; i>0; i--);
}
```

```
volatile uint32_t i;
// Stop watchdog timer
WDTCTL = WDTPW | WDTHOLD;
// Set P1.0 to out-put direction
P1DIR |= 0x01;
while(1)
{
    // Toggle P1.0 using exclusive-OR
    P1OUT ^= 0x01;
    //Delay
    for(i=100000; i>0; i--);
}
```

引脚介绍

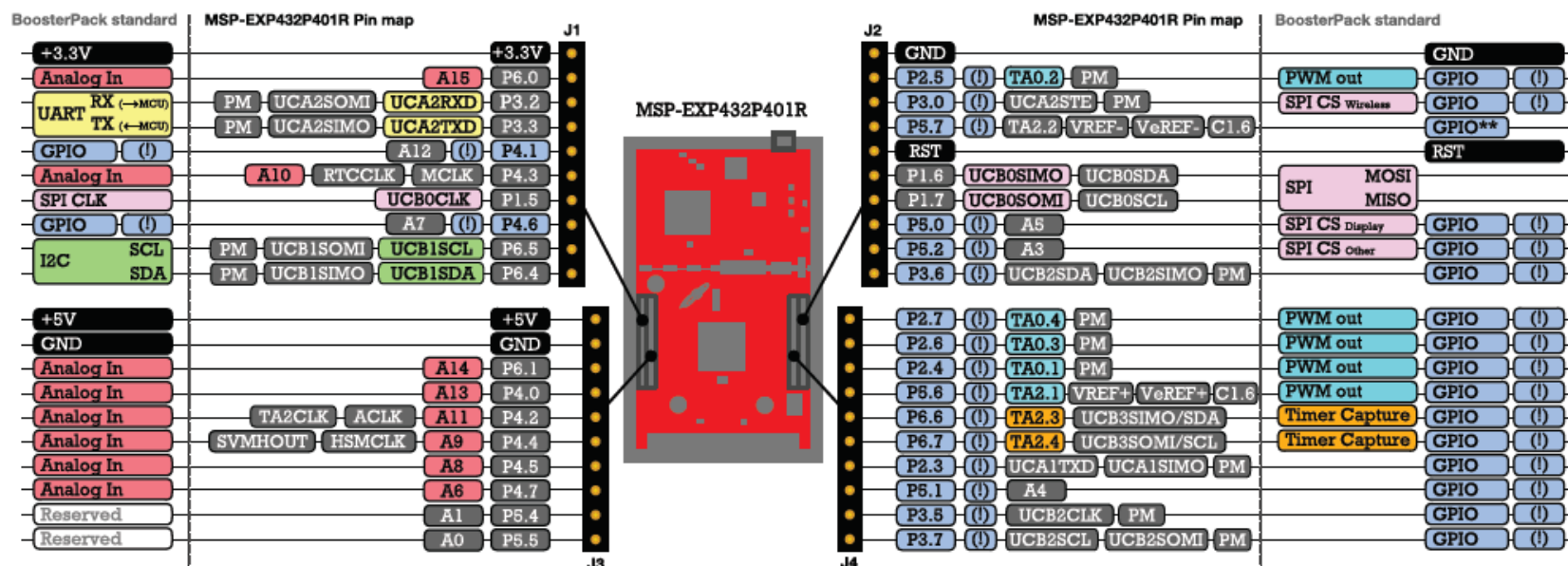
Below are the pins exposed @ the MSP-EXP432P401R LaunchPad BoosterPack connector.

Also shown are functions that map with the BoosterPack pinout standard. Refer to the MSP432P401R Datasheet for additional details.

NOTE: Some LaunchPads & BoosterPacks do not 100% comply with the standard, so please check your specific LaunchPad to ensure pin compatibility.

(!) Denotes I/O pins that are interrupt-capable

** Some LaunchPads do not have a GPIO here



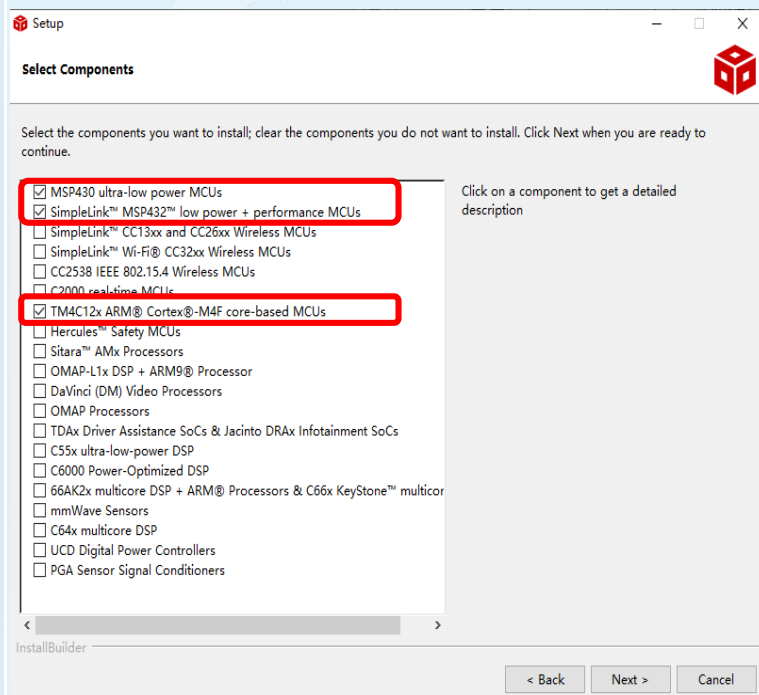
PxSEL1	PxSEL0	I/O Function
0	0	General purpose I/O is selected
0	1	Primary module function is selected
1	0	Secondary module function is selected
1	1	Tertiary module function is selected



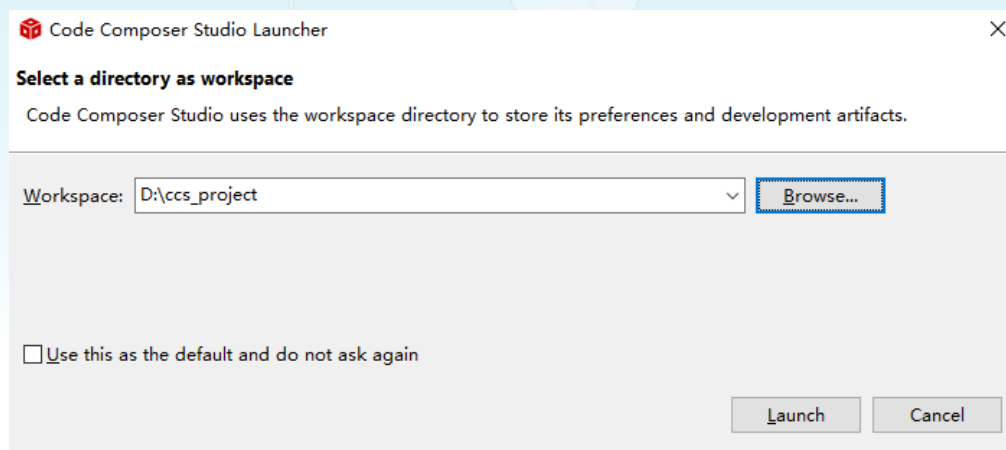
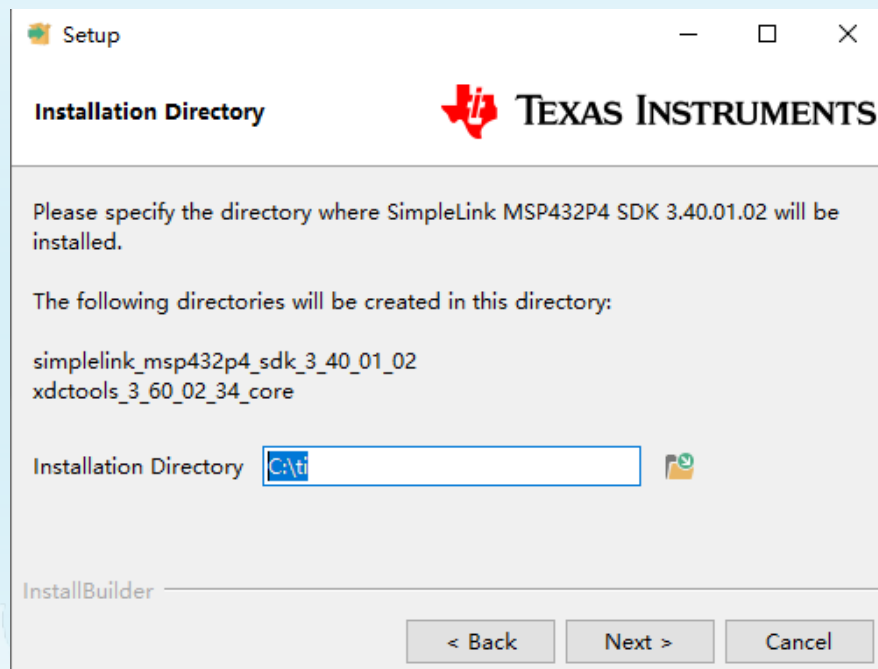
PART 3 软件介绍

软件安装

ccs_setup_10.2.0.00009.exe



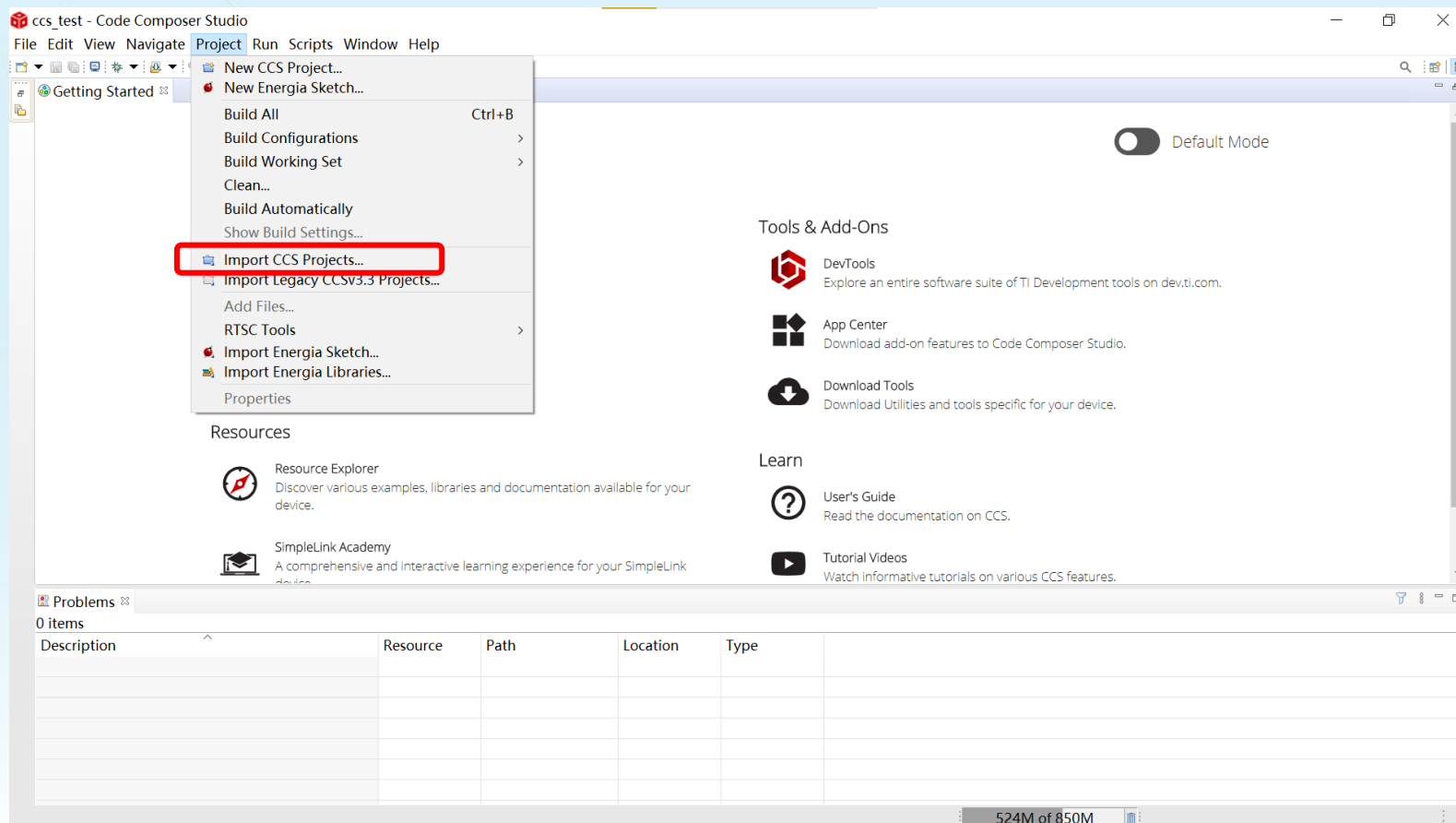
simplelink_msp432p4_sdk_3_40_01_02.exe



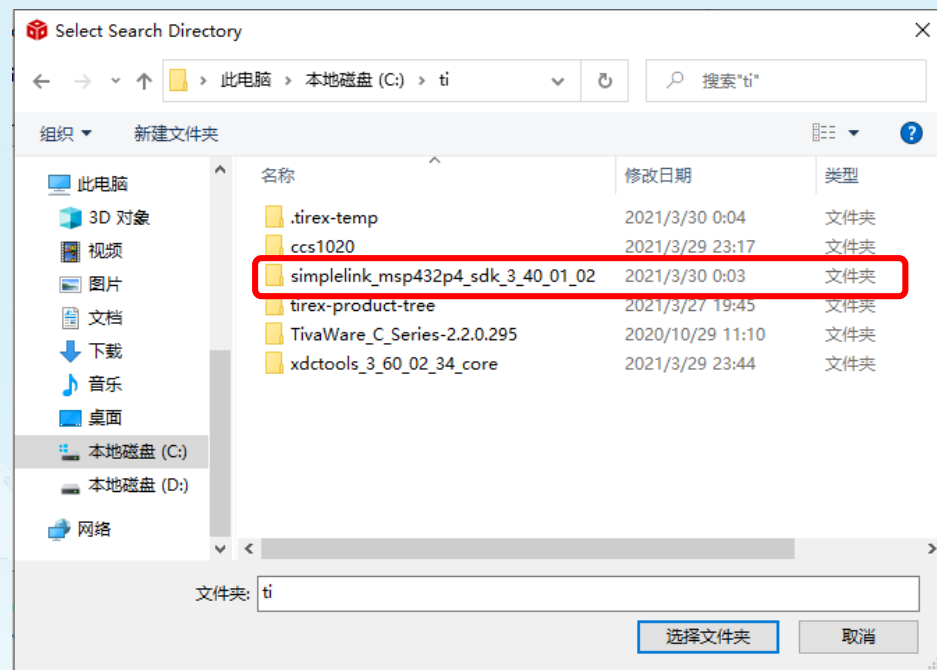
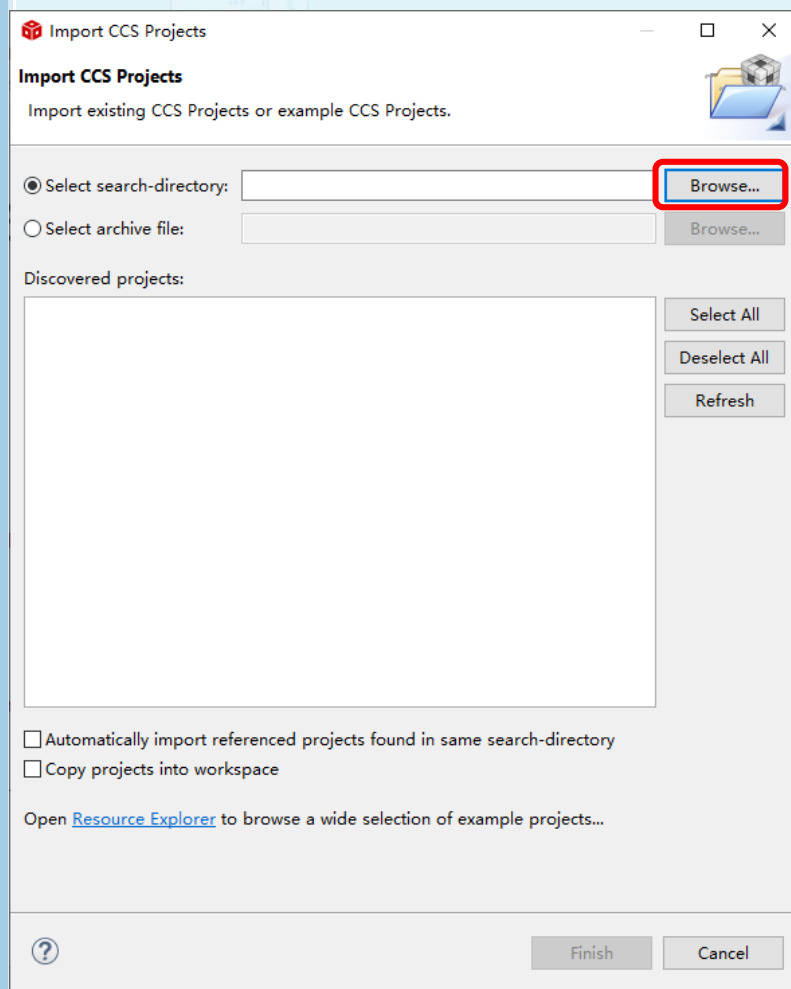
华中科技大学

Huazhong University of Science and Technology

导入例程



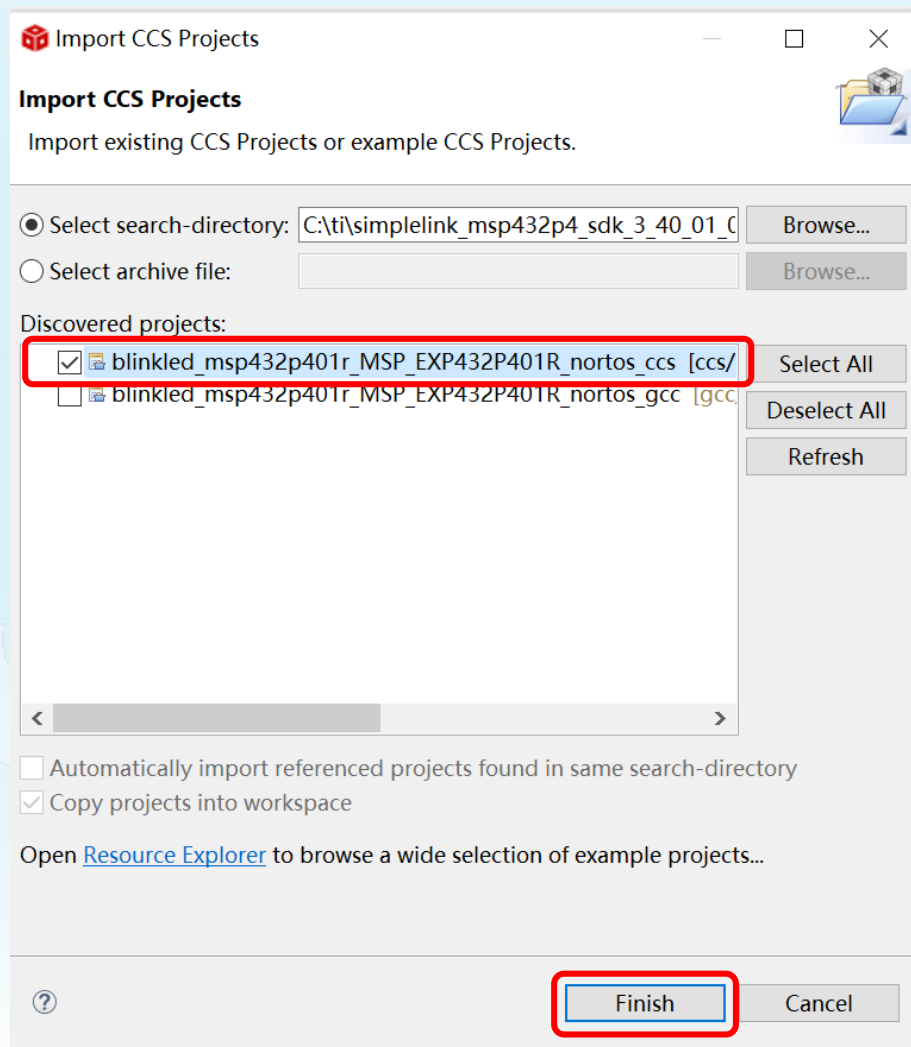
导入例程

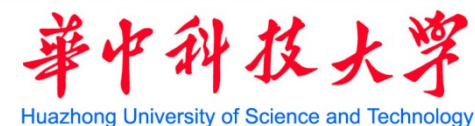


导入例程

例程路径:

C:\ti\simplelink_msp432p4_sdk_3_40_01_02\examples\nortos\MSP_EXP432P401R\demos\blinkled_msp432p401r



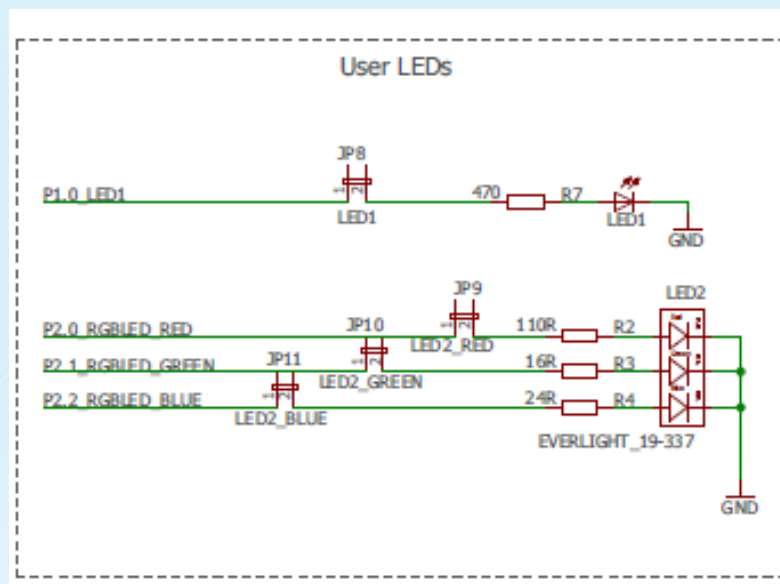




PART3 本周任务

课堂实验

1. 完成CCS软件安装
2. 导入blinkled_msp432p401r示例工程
3. 在例程上实现三色灯控制





Thank You !

