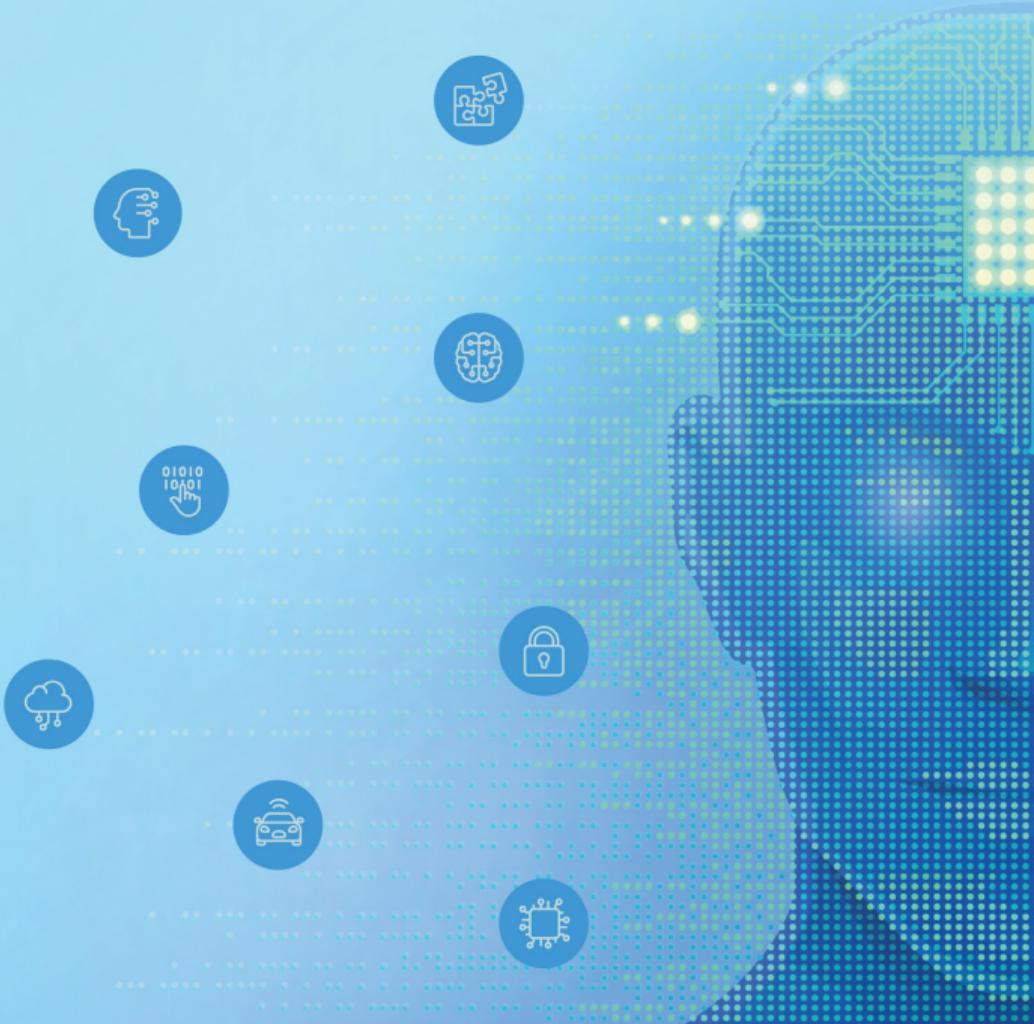




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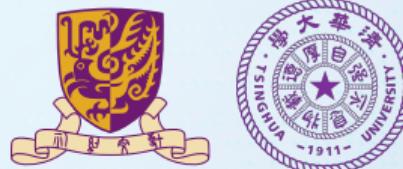


# DiffPattern: Layout Pattern Generation via Discrete Diffusion

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<sup>1</sup>Chinese University of Hong Kong

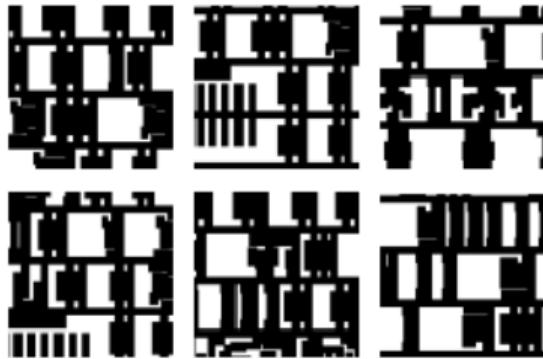
<sup>2</sup>Tsinghua University



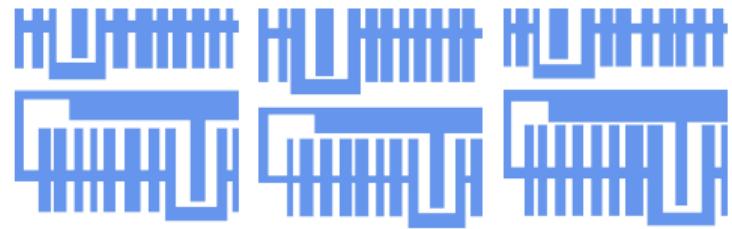
# Background Knowledge



# Layout Pattern Generation



Original Layout Patterns [ICCAD'20]



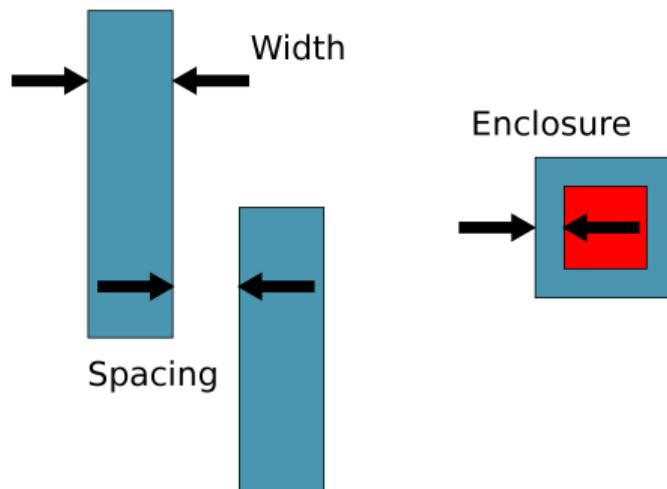
(a) (b) (c)

Generated Layout Patterns (Ours)

VLSI layout patterns provide critical resources in various designs for manufacturability research, from early technology node development to back-end design and sign-off flows[DAC'19]<sup>1</sup>.

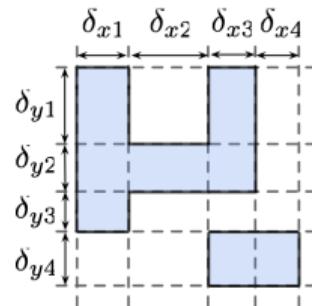
# An End-to-End Learning Solution?

## The three basic DRC checks



- Maybe No
- Gap between Discrete Rules and Continuous DNN Model

# Squish Pattern Representation



Topology:

$$\begin{bmatrix} 1 & 0 & 1 & 0 \\ 1 & 1 & 1 & 0 \\ 1 & 0 & 0 & 0 \\ 0 & 0 & 1 & 1 \end{bmatrix}$$

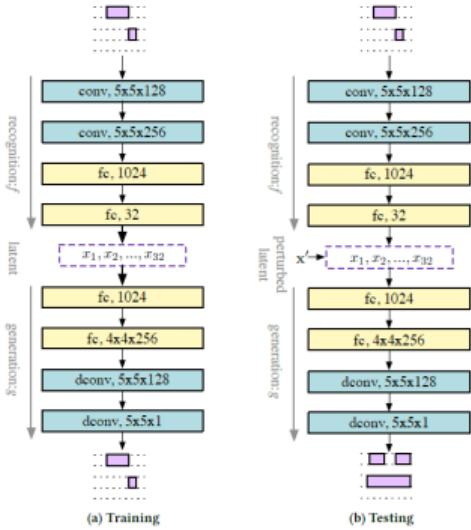
Geometry:  $\Delta_x = [\delta_{x1}, \delta_{x2}, \delta_{x3}, \delta_{x4}]$

$\Delta_y = [\delta_{y1}, \delta_{y2}, \delta_{y3}, \delta_{y4}]$

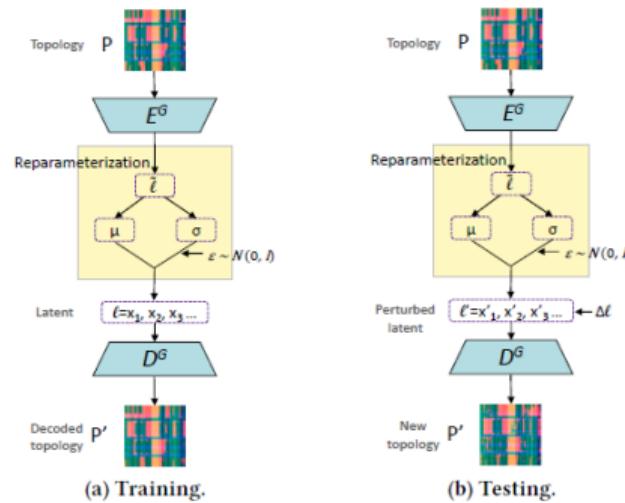
## Squish Pattern [US Patent'14]<sup>2</sup>

- Lossless and efficient representation method
- Encodes layout into pattern topology matrix and geometric information
- **Problem #1:** information density of each pixel is still not satisfactory

# Novel Pattern Generation



(a) Encoder-Decoder [DAC'19]

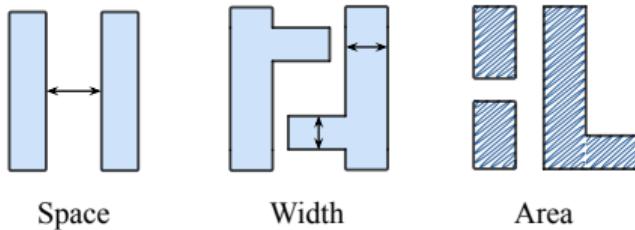


(b) VAE-based [ICCAD'20]<sup>3</sup>

- Generate gray image (topology) and transfer it into a binary image
- May lead to a deduction of information
- **Problem #2:** How to generate a binary mask directly?

<sup>3</sup>X. Zhang *et al.*, "Layout pattern generation and legalization with generative learning models", in Proc. ICCAD, 2020, pp. 1–9.

# Pattern Legalization



Examples of DRC Rule

## Finding legal distance vector for each topology

- Solving a Linear System (1D pattern) [DAC'19].
- Using Exist Distance Vector (2D pattern) [ICCAD'20]
- **Problem #3:** 2D pattern introduces non-linear constraint, hard to solve!

# Evaluation

- Pattern Diversity. Shannon entropy of the pattern complexity.

$$H = - \sum_i \sum_j P(c_{xi}, c_{yj}) \log P(c_{xi}, c_{yj}), \quad (1)$$

- Pattern Legality.

$$L = \frac{\# \text{ Legal Patterns}}{\# \text{ All Patterns}}. \quad (2)$$

# Denoising Diffusion Probabilistic Models [NeurIPS'20]<sup>4</sup>

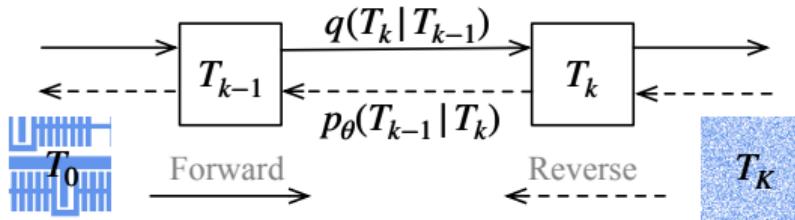


Illustration of denoising diffusion process.

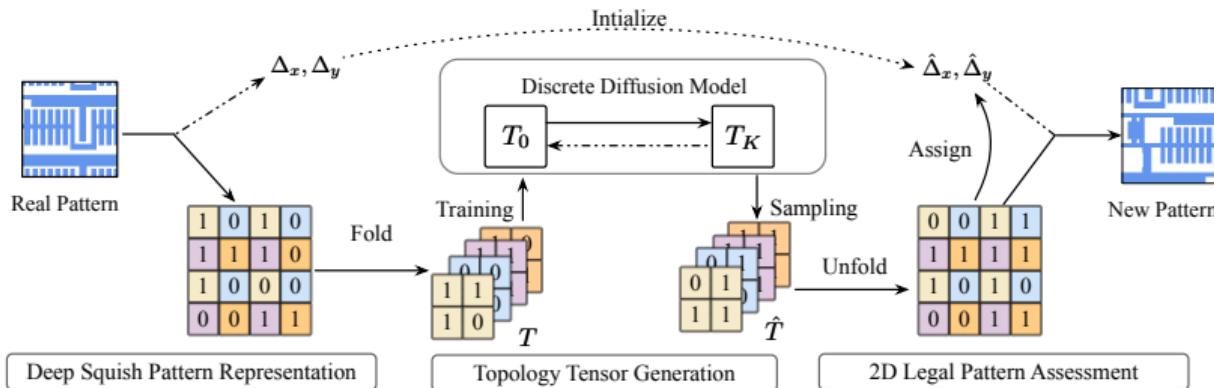
Forward Process:  $q(T_k | T_{k-1}) := \mathcal{N}(T_k; \sqrt{1 - \beta_k} T_{k-1}, \beta_k I)$ .

Reverse Process:  $p_\theta(T_{k-1} | T_k) := \mathcal{N}(T_{k-1}; \mu_\theta(T_k, k), \Sigma_\theta(T_k, k))$ .

# Proposed Method: DiffPattern

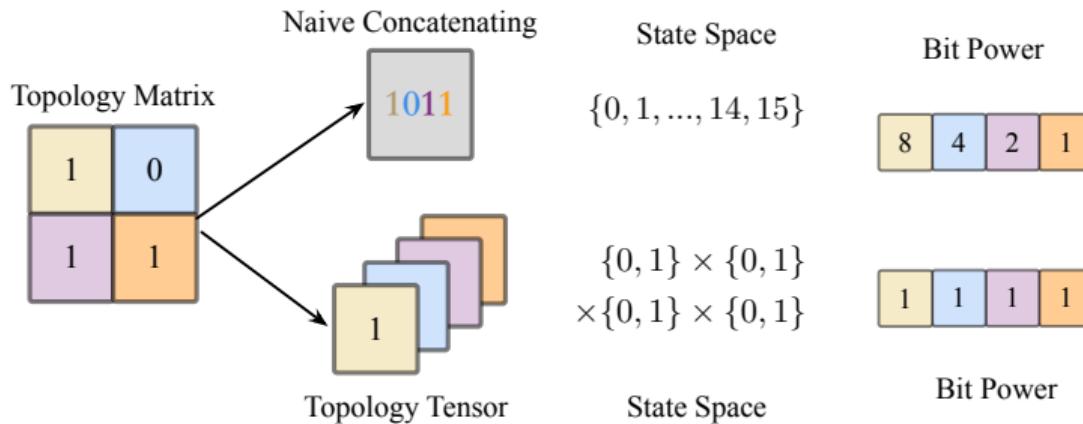


# Overview



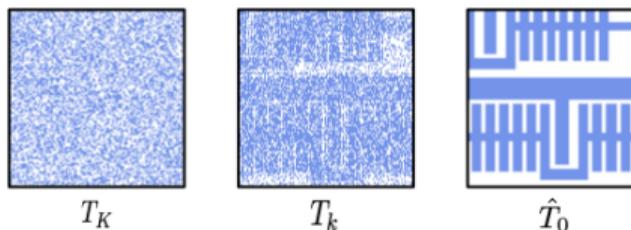
An illustration of the Diffpattern framework for reliable layout pattern generation.

# Problem #1: Deep Squish Pattern Representation



- The Topology Tensor is a lossless and compact representation of the topology matrix.
- The Naive Concatenating brings unbalanced power to each bit and an exponentially increasing state space.

## Problem #2: Topology Tensor Generation



An illustration of the (flattened) samples from our Discrete Diffusion Model.

Forward Process  $q(x_k | x_{k-1}) := \text{Cat}(x_k; p = x_{k-1}Q_k)$ ,

Multiple step forward at once.  $q(x_k|x_0) = \text{Cat}(x_k; p = x_0\bar{Q}_k)$ ,  $\bar{Q}_k = Q_1Q_2\dots Q_k$

Reverse Process  $p_\theta(x_{k-1}|x_k) = \sum_{\tilde{x}_0} q(x_{k-1}|x_k, \tilde{x}_0) p_\theta(\tilde{x}_0|x_k)$ .

Training Loss Function:  $L = D_{\text{KL}}(q(x_{k-1}|x_k, x_0) \| p_\theta(x_{k-1}|x_k)) - \lambda \log p_\theta(x_0|x_k)$ ,

## Problem #2: Topology Tensor Generation

A uniform stationary distribution is a natural choice in topology tensor generation. Given any  $x_0$ , the distribution of every entry  $x_k$  should follows,

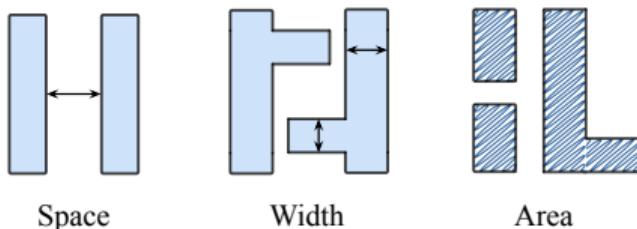
$$q(x_k|x_0) \rightarrow [0.5, 0.5], \text{ when } k \rightarrow K. \quad (3)$$

$$Q_k = \begin{bmatrix} 1 - \beta_k & \beta_k \\ \beta_k & 1 - \beta_k \end{bmatrix}, \quad (4)$$

$$\beta_k = \frac{(k-1)(\beta_K - \beta_1)}{K-1} + \beta_1, \quad k = 1, \dots, K, \quad (5)$$

where  $\beta_1$  and  $\beta_K$  are hyperparameters.

## Problem #3: 2D Pattern Legalization



Examples of DRC Rule

$$\begin{cases} \delta_{xi}, \delta_{yj} > 0, & \forall \delta_{xi}, \delta_{yj}; \\ \sum \delta_{xi} = \sqrt{CM}, \quad \sum \delta_{yj} = \sqrt{CM}; & \\ \sum_{i=a}^b \delta_i \geq Space_{min}, & \forall (a, b) \in Set_S; \\ \sum_{i=a}^b \delta_i \geq Width_{min}, & \forall (a, b) \in Set_W; \\ \sum \delta_{xi} \delta_{yj} \in [Area_{min}, Area_{max}], & \forall \text{ Polygon}; \end{cases} \quad (6)$$

# Experiment Results

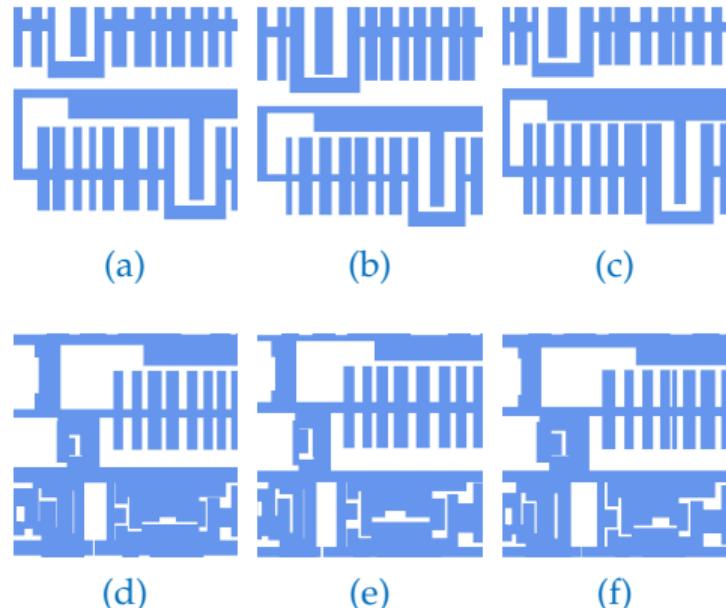


# Diversity and Legality

Set/Method	Generated Topology	Generated Patterns		Legal Patterns	
		Patterns	Diversity (↑)	Legality (↑)	Diversity (↑)
Real Patterns	-	-	-	13869	10.777
CAE [DAC'19]	100000	100000	4.5875	19	3.7871
VCAE [ICCAD'20]	100000	100000	<b>10.9311</b>	2126	9.9775
CAE+LegalGAN [ICCAD'20]	100000	100000	5.8465	3740	5.8142
VCAE+LegalGAN [ICCAD'20]	100000	100000	9.8692	84510	9.8669
LayoutTransformer [ICCAD'22]	-	100000	10.532	89726	10.527
DiffPattern-S	100000	100000	10.815	<b>100000</b>	<b>10.815</b>
DiffPattern-L	100000	10000000	10.815	<b>10000000</b>	<b>10.815</b>

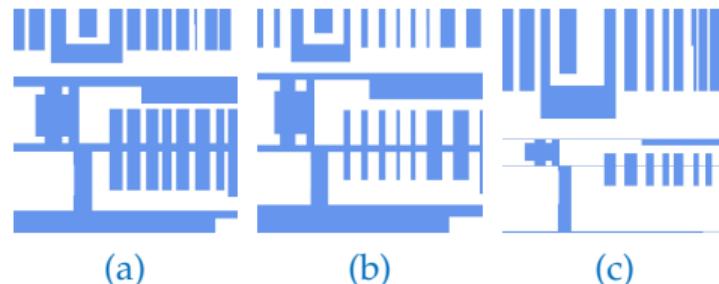
- DiffPattern achieves a perfect performance (i.e. 100%) under the metric of legality.
- DiffPattern also gets reasonable improvement (10.527→10.815) on the diversity.
- We generate 100 different layout patterns from each topology in DiffPattern-L.

# Flexibility: Generate Different Patterns from Single Topology.



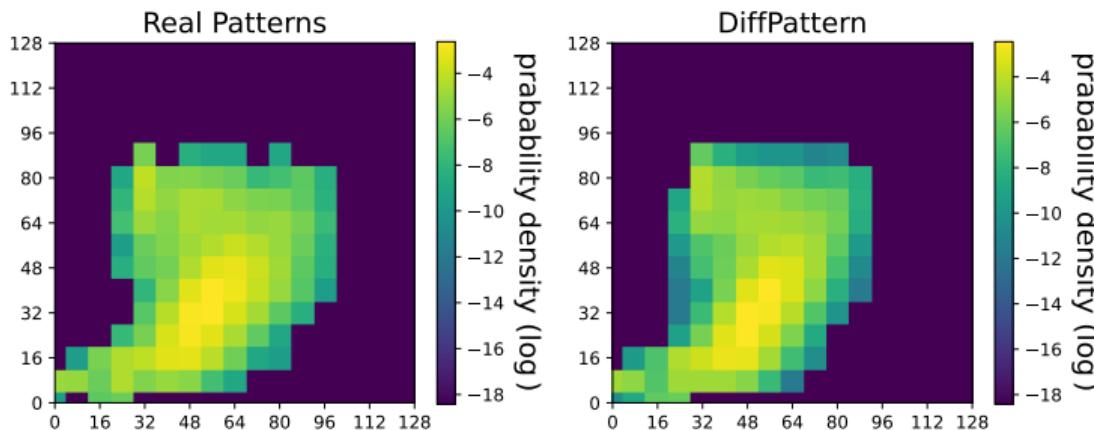
Different layout patterns that are generated from a single topology with the same design rule.

# Flexibility: Generate Legal Patterns with Different Design Rules.



Layout patterns that are generated from the same topology with different design rules: (a) Normal rule; (b) Larger  $space_{min}$ ; (c) Smaller  $Area_{max}$ .

# Distribution of Complexity



An illustration of complexity distribution.

# Model Efficiency

Phase/Method	Cost Time (s)	Acceleration
Sampling	0.544	N/A
Solving-R	0.269	1.00×
Solving-E	0.117	2.30×

- Initializing with existing results achieves  $2.30\times$  acceleration on CPU.



# THANK YOU!

