# Microprocessors and Micro controllers CSE3815

Fairuz Bilquis Khan April 2024

## **Evolution of Microprocessors**

There should be overview chart of difference between intel 4004 to pentium 4.

#### Microprocessors:

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'-->Is a semiconductor device consisting of electronics logic circuit. '--->Manufactured by using various fabrication schemes.
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- '--->Capable of performing computing functions.
- '---->Capable of transporting data/information.

Can be divided into 3 segments:

- (-)Arithmetic adn logic unit
- (-)Registers unit
- (-)Control unit

## Von Neumann Machine:

Von Neumann machine may refer to: Von Neumann architecture, a conceptual model of nearly all computer architecture

Three key concepts:

- (-)Data and instructions are stored in a single set of read-write memory.
- (-)Contents of memory are accessed by memory address without regard to the type of data.
- (-)Execution occurs in a sequential fashion, unless explicitly stated otherwise.

## **Computer System Components:**

(-) Memory:

Stores data and instructions.

(-) Input/Output:

Called peripherals.

Used to input and output data and instructions.

(-) Arithmetic and Logic Unit:

Performs arithmetic(+, -) operations.

Performs logical(AND, OR, XOR, Shift, Rotate) operations.

(-) Control Unit:

Coordinates the execution of instructions by managing the flow of data between the CPU's components.

- (-) System Interconnection and Interaction:
  - (+)BUS: A group of lines used to transfer bits between µp and other components. Bus is used to communicate between parts of the computer. There is only one transmitter at a time and only the addressed device can respond. ^\types:

>>address

>>data

>>control

## **CPU Components:**

## (-) Registers:

Small, fast storage in the CPU for data, instructions and other items. Includes the program counter (PC) and memory address registers (MAR). These registers need to match the width of the address bus for proper

These registers need to match the width of the address bus for proper memory access.

 ${\tt Data\ registers\ should\ match\ memory\ word\ size\ for\ efficient\ data\ transfer.}$ 

#### (-) Control Unit:

Generates control signals which are necessary for execution of instructions. Connect registers to the bus.

Controls the data flow between CPU and peripheral components.

Provides status, control and timing signals required for the operations of memory and I/O devices.

Acts as a brain of computer system.

All actions of the control unit are associated with the decoding and execution of instructions(fetch and execute cycles).

## (-) Arithmetic and Logic Unit:

Execute arithmetic and logical operations.

Accumulator is a special 8-bit register associated with ALU. Register 'A' in 8085 is an accumulator.

Source of one of the operands of an arithmetic or logical operation serves as one input to ALU.

Final result of an arithmetic or logical operation is placed in the accumulator.