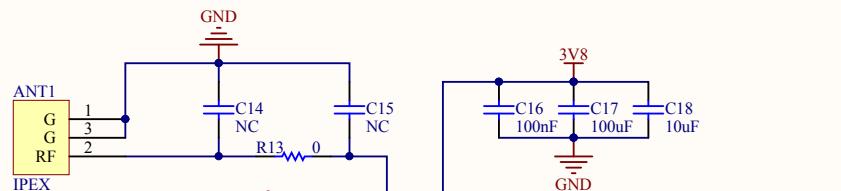
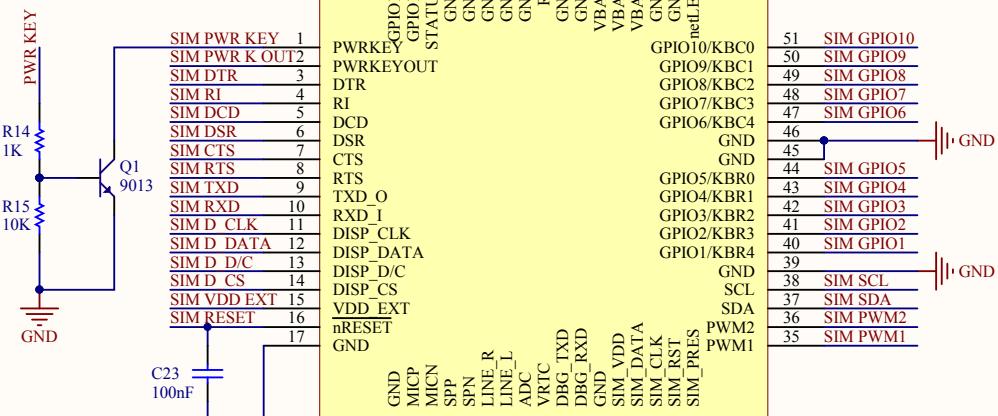


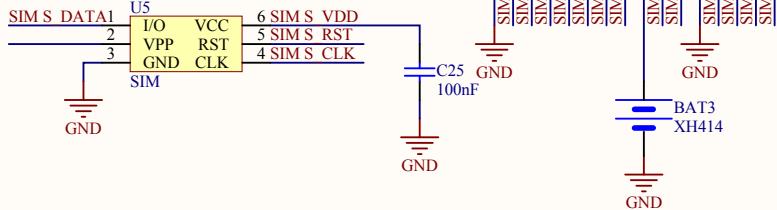
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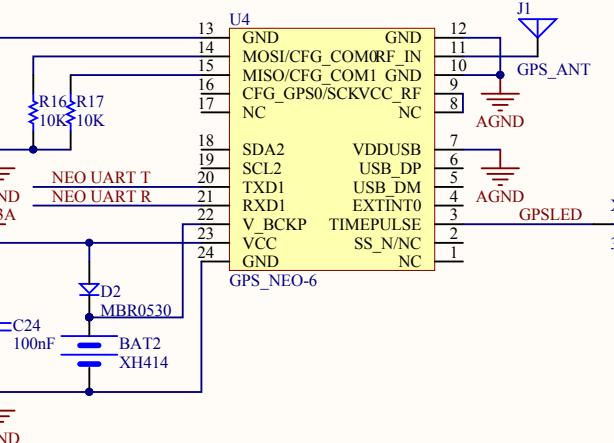
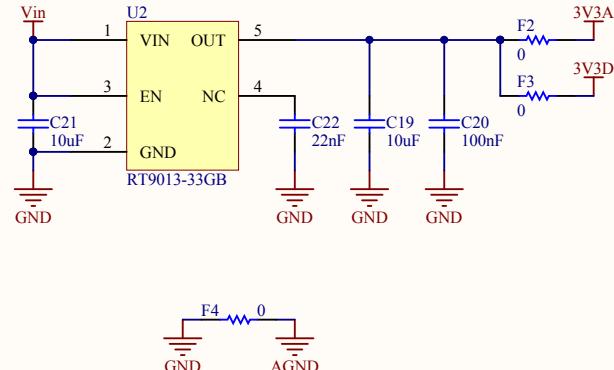
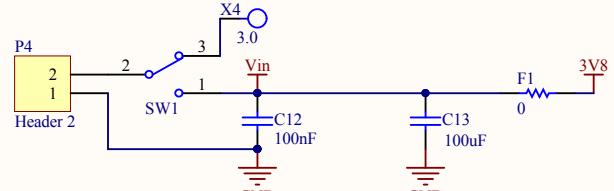
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C



D



Title: SIM900A

Size: A4

Number: 1

Revision: 1

Date: 2013/11/19

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File: D:\projects\altium_designer\GSM\sim900a.SchDoc

Altium

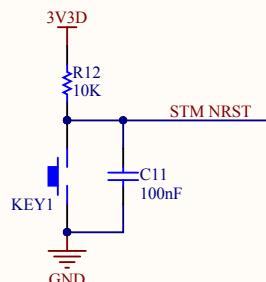
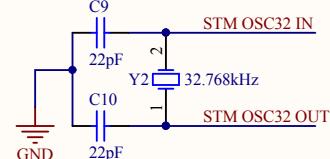
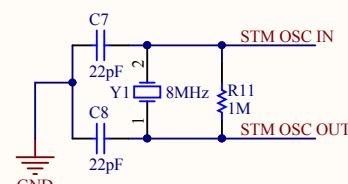
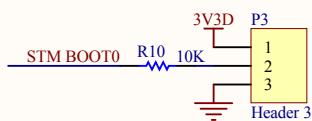
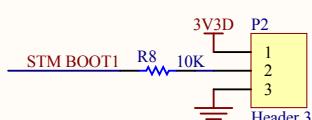
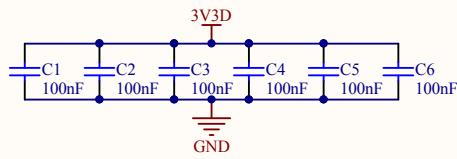
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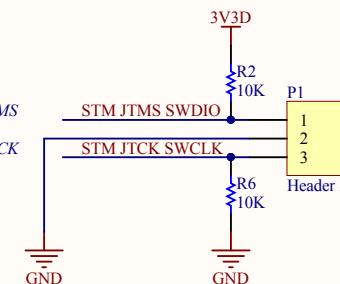
4

A



STM UART2 R	R1 560	SIM TXD
STM UART2 T	R3 560	SIM RXD
STM UART1 R	R4 560	NEO UART T
STM UART1 T	R5 560	NEO UART R
PA6	R7 560	SIM DTR
PA7		PWR KEY
STM NRST	R9 560	SIM RESET

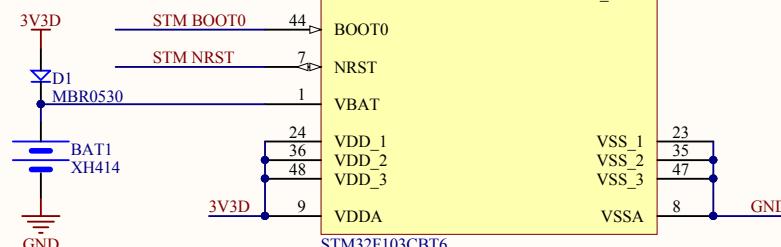
SWDIO = TMS
SWCLK = TCK



SWD

B

PA0	10	PA0-WKUP	PB0	18 PB0
PA1	11	PA1	PB1	19 PB1
STM UART2 T	12	PA2	PB2/BOOT1	20 STM BOOT1
STM UART2 R	13	PA3	PB3/JTDO	21 STM JTDO
PA4	14	PA4	PB4/JNTRST	22 STM JNTRST
PA5	15	PA5	PB5	23 PB5
PA6	16	PA6	PB6	24 PB6
PA7	17	PA7	PB7	25 PB7
PA8	29	PA8	PB8	26 PB8
STM UART1 T	30	PA9	PB9	27 PB9
STM UART1 R	31	PA10	PB10	28 STM UART3 T
PA11	32	PA11	PB11	29 STM UART3 R
PA12	33	PA12	PB12	30 PB12
STM JTMS/SWDIO	34	PA13/JTMS/SWDIO	PB13	31 PB13
STM JTCK/SWCLK	37	PA14/JTCK/SWCLK	PB14	32 PB14
STM JTDI	38	PA15/JTDI	PB15	33 PB15
STM OSC IN	5	OSC_IN/PD0	PC13-TAMPER-RTC	2 PC13-TAMPER-RTC
STM OSC OUT	6	OSC_OUT/PD1	PC14-OSC32_IN	3 PC14-OSC32_IN
			PC15-OSC32_OUT	4 PC15-OSC32_OUT

Title **CORE**

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Altium

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