

A
Report
of
Assignments
in
Power Electronics Laboratory

Prepared
by
Shivraj Vishwakarma
(M. Tech., PE, Roll No. – 224102112)

Submitted
To
Dr. A. Ravindranath
(Asst. Professor)



**Department of Electronics & Electrical Engg.
Indian Institute of Technology, Guwahati**

Experiment - 08

To Study the Operation of Single-Phase and Three Phase Voltage Source Inverters (VSI) Using MATLAB/Simulink.

NAME: SHIVRAJ VISHWAKARMA

ROLL. NO. 224102112

1. DESIGN PARAMETERS:

PARAMETERS	Single-phase VSI	Three-phase VSI
Output voltage	230 V, 50 Hz	400 V, 50 Hz
Output Power	1 kW	10 kW
Switching Frequency	10 kHz	10 kHz
Load type	Resistive	Resistive, Y-connected
Modulation index	0.4 and 0.8	0.4 and 0.8

2. PROCEDURE:

1. Consider a single-phase full-bridge VSI with LC filter and resistive load. The switching signals are generated using sine-triangle PWM with bipolar voltage switching. Calculate the input voltage, load resistance and LC filter parameters for the modulation index values given in the table.
2. Simulate the single-phase VSI using the parameters calculated in step 1. Take the snapshots of input voltage, output voltage of full-bridge (i.e., PWM voltage before filter), load voltage, and inductor current. Check whether load voltage is 230 V, 50 Hz, sine-wave.
3. Repeat steps 1 and 2 when the switching signals generated using sine-triangle PWM with unipolar voltage switching.
4. Now consider a three-phase, three-leg VSI supplying a three-phase Y-connected resistive load through an LC filter. The switching signals are generated using sine-triangle PWM. Calculate the input voltage, load resistance and LC filter parameters for the modulation index values given in the table.
5. Simulate the three-phase VSI using the parameters calculated in step 4. Take the snapshots of input voltage, output voltage of full-bridge (i.e., PWM voltage before filter), load voltage, and inductor current. Check whether load voltage is three-phase, 400 V, 50 Hz, balanced and sinusoidal.

1. Single Phase Full-Bridge VSI with LC filter and resistive load

➤ CIRCUIT DIAGRAM:

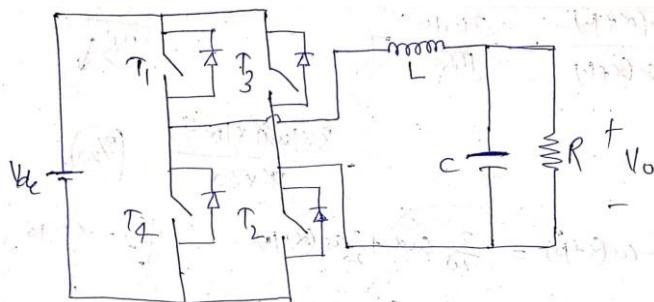


Fig 1: Single Phase full bridge VSI

➤ DESIGN PARAMETERS CALCULATION:

1). INPUT VOLTAGE:

We know that for single phase full bridge inverter the fundamental component of output voltage can be given as

$$\widehat{V_{01}} = m_a V_{dC} \Rightarrow V_{01RMS} = \frac{m_a V_{dC}}{\sqrt{2}} \Rightarrow V_{dC} = \frac{\sqrt{2} V_{01RMS}}{m_a} \quad (1)$$

Using the given values as $V_{01RMS} = 230 V$, at $m_a = 0.4$

$$V_{dC} = \frac{\sqrt{2} \cdot 230}{0.4} = 813.17 V \quad (2)$$

$$\text{And at } m_a = 0.8, \quad V_{dC} = \frac{\sqrt{2} \cdot 230}{0.8} = 406.58 V \quad (3)$$

2). LOAD RESISTANCE:

Since the output power is given as $P = 1 \text{ kW}$, so by using power expression,

$$P = \frac{V_o^2}{R} \Rightarrow R = \frac{V_o^2}{P} = \frac{230^2}{1000} = 52.9 \Omega \quad (3)$$

3). FILTER INDUCTOR & CAPACITOR:

Assuming $L = 2 \text{ mH}$, and since frequency is 10 kHz, so by using,

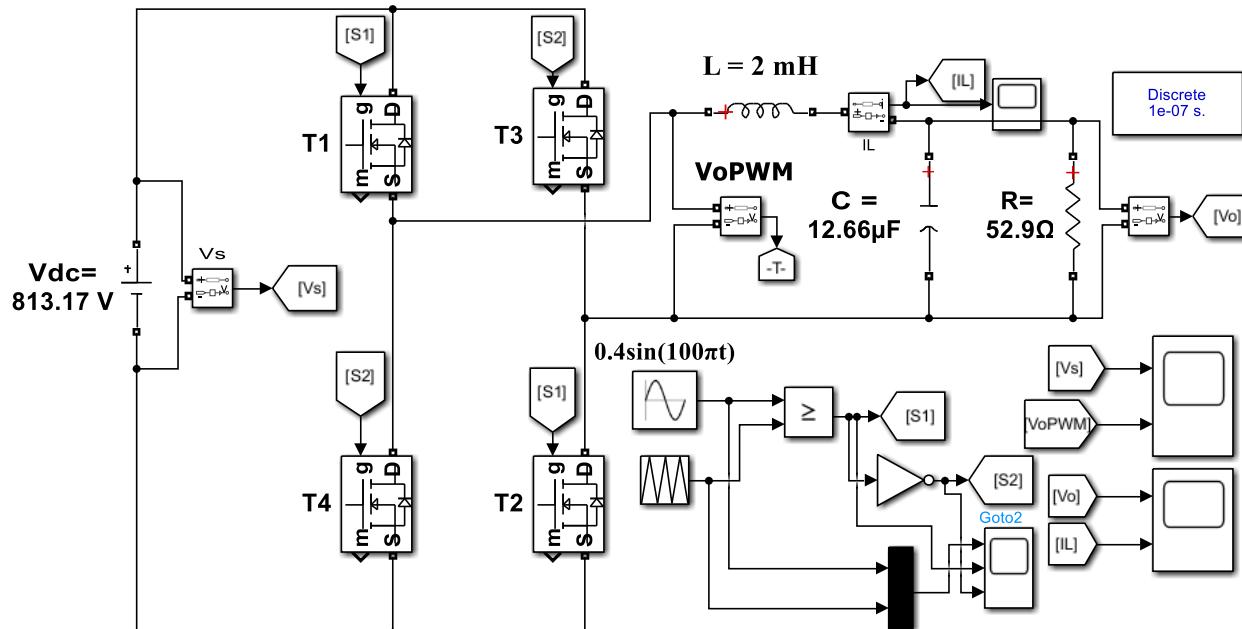
$$\omega = \frac{1}{\sqrt{LC}} \Rightarrow C = \frac{1}{4\pi^2 f^2 L} \Rightarrow C = 12.66 \mu F \quad (3)$$

2. Simulation of Single Phase Full-Bridge VSI

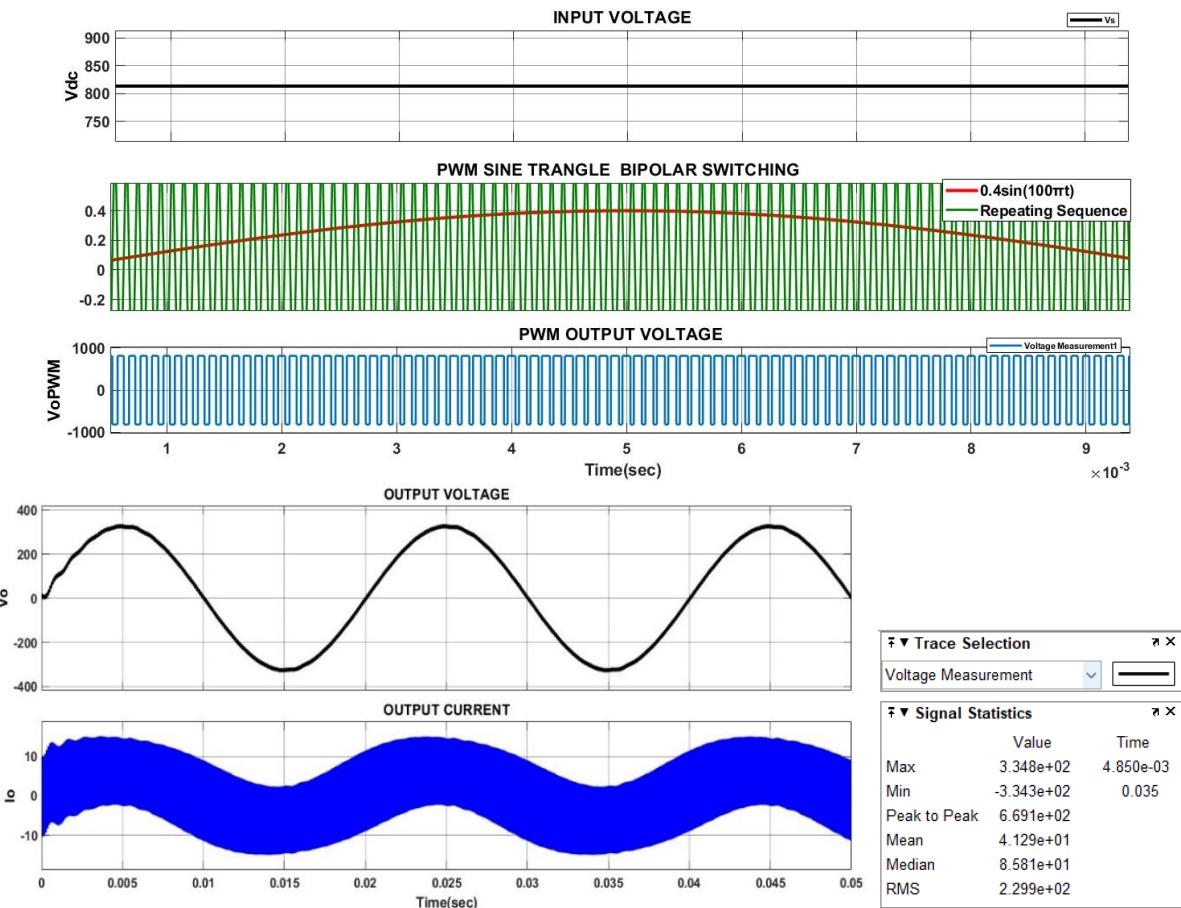
➤ SIMULATION CONFIGURATION:

❖ At $m = 0.4$

➤ SIMULATED CIRCUIT:



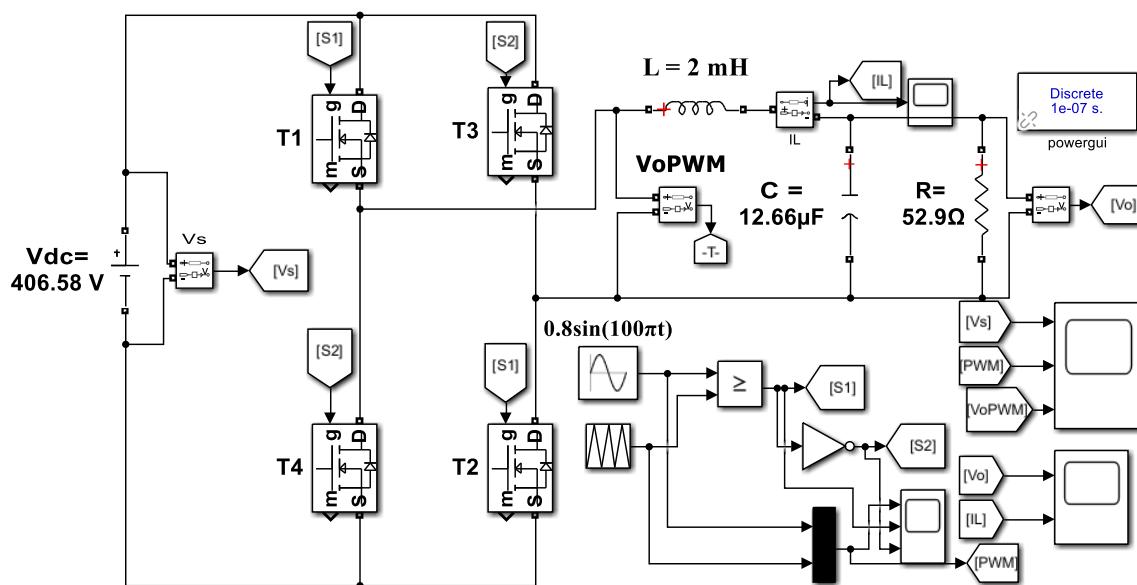
➤ WAVEFORMS OBTAINED:



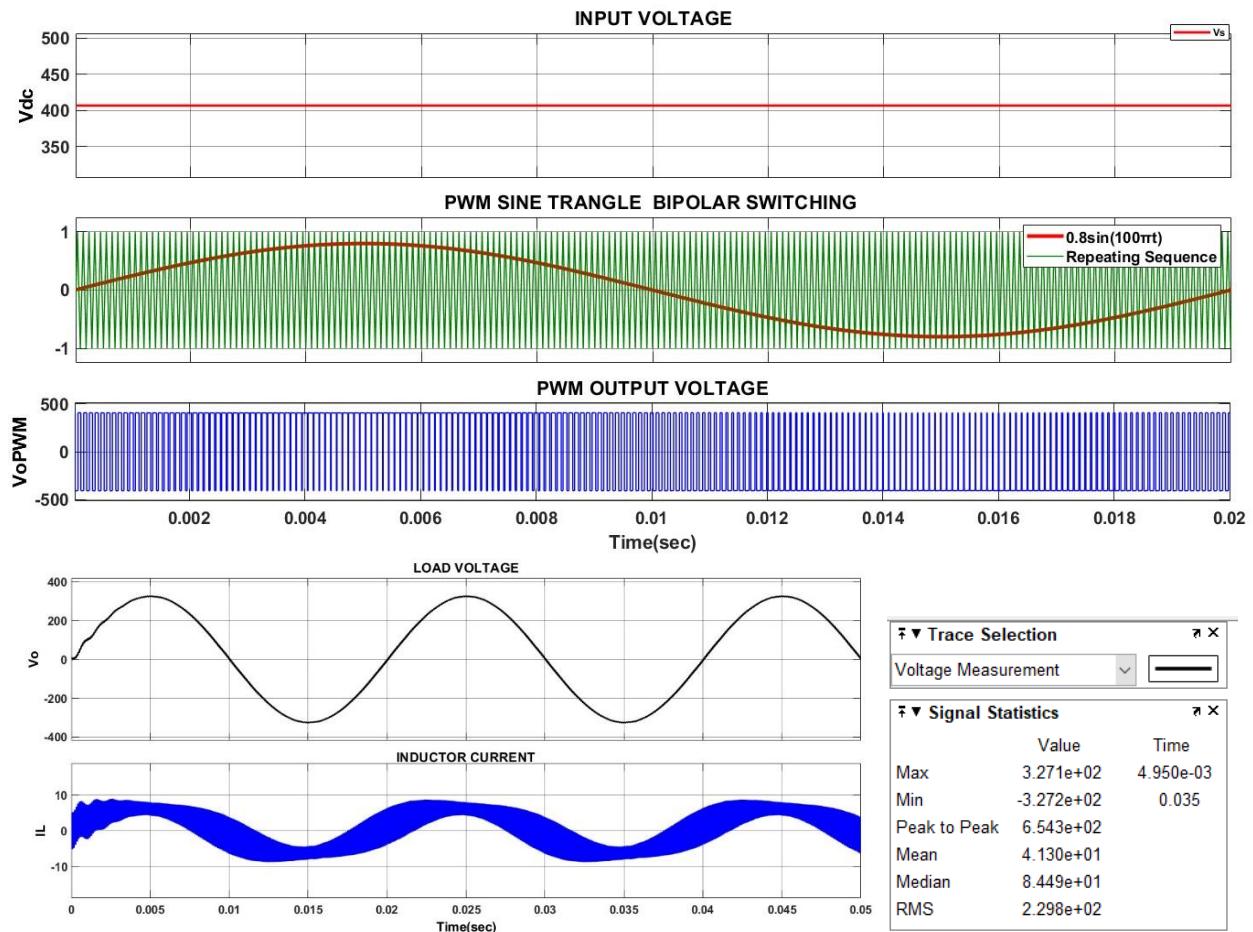
➤ **OBSERVATION:** RMS value of $V_o = 299.9 \text{ V}$

❖ At $m = 0.8$

➤ **SIMULATED CIRCUIT:**

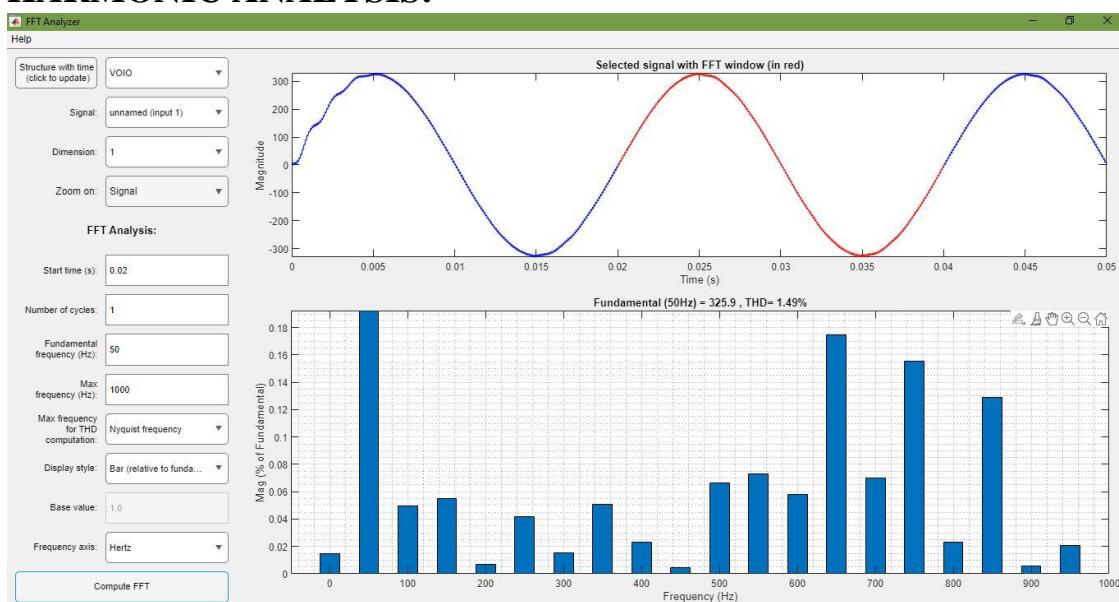


➤ WAVEFORMS OBTAINED:



➤ OBSERVATION: RMS value of $V_o = 299.9 \text{ V}$

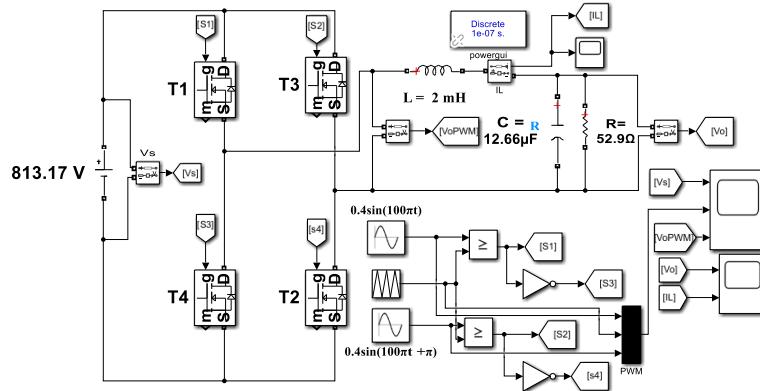
➤ HARMONIC ANALYSIS:



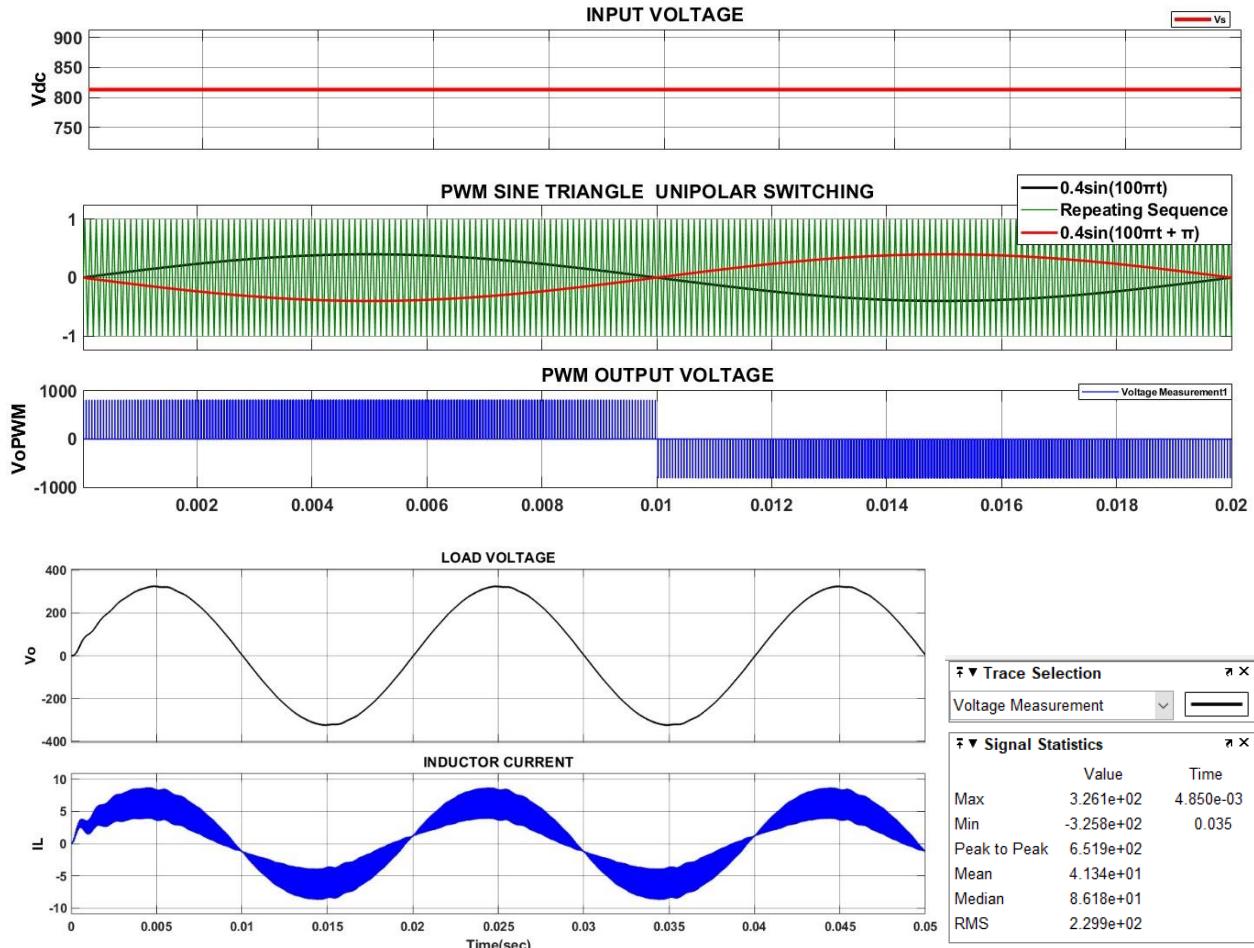
3. Single Phase Full-Bridge VSI with Unipolar PWM Switching

❖ At $m = 0.4$

➤ SIMULATED CIRCUIT:



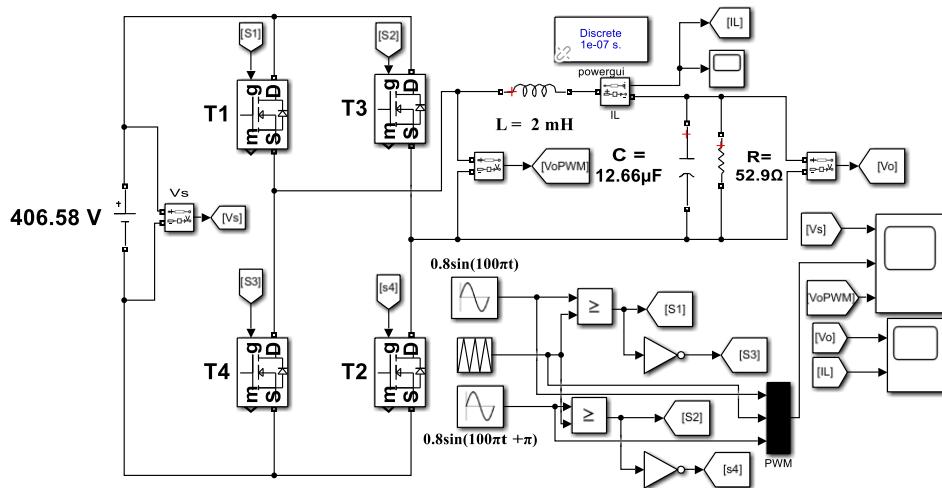
➤ WAVEFORMS OBTAINED:



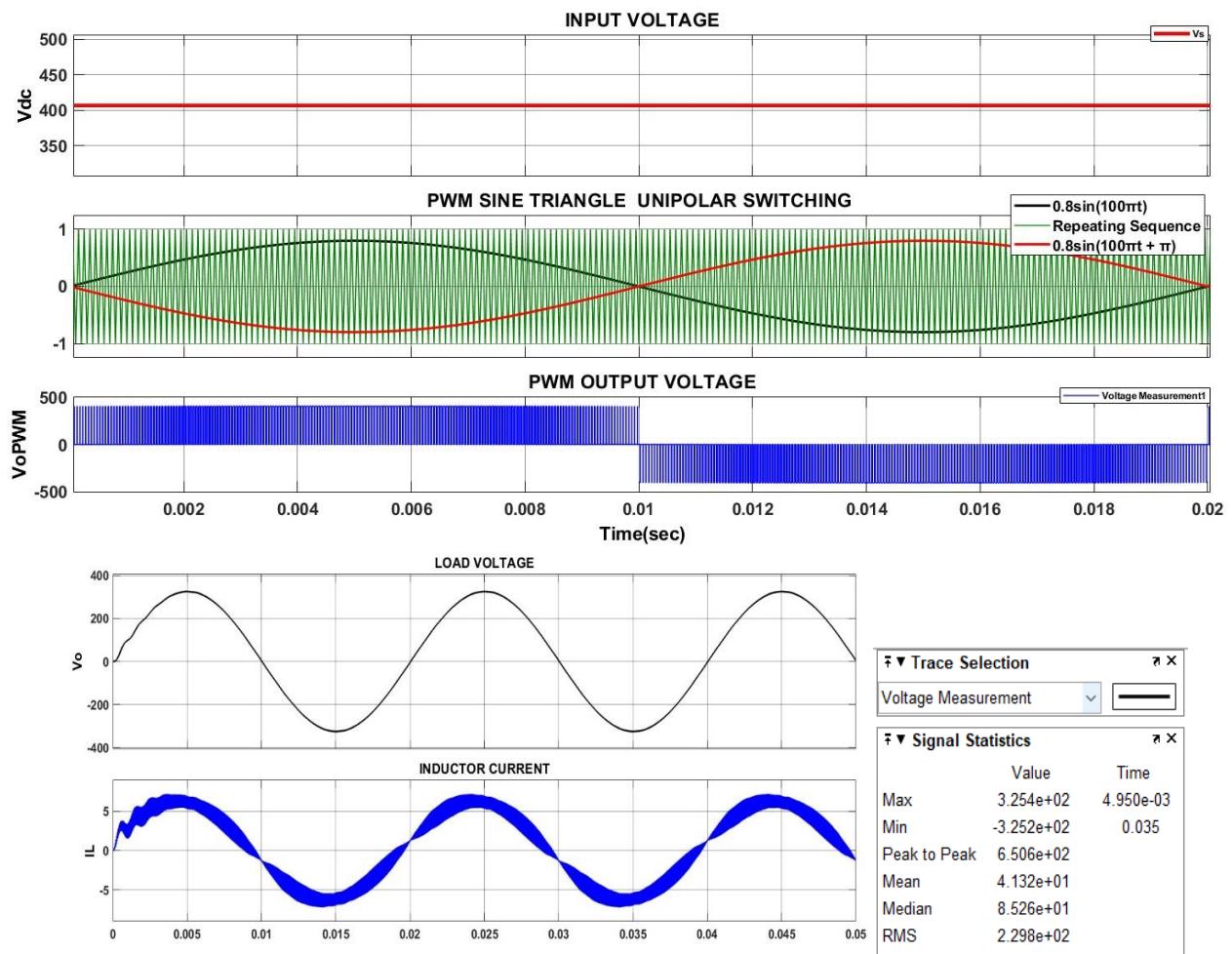
➤ OBSERVATION: RMS value of $Vo = 299.9$ V

❖ At $m = 0.8$

➤ SIMULATED CIRCUIT:

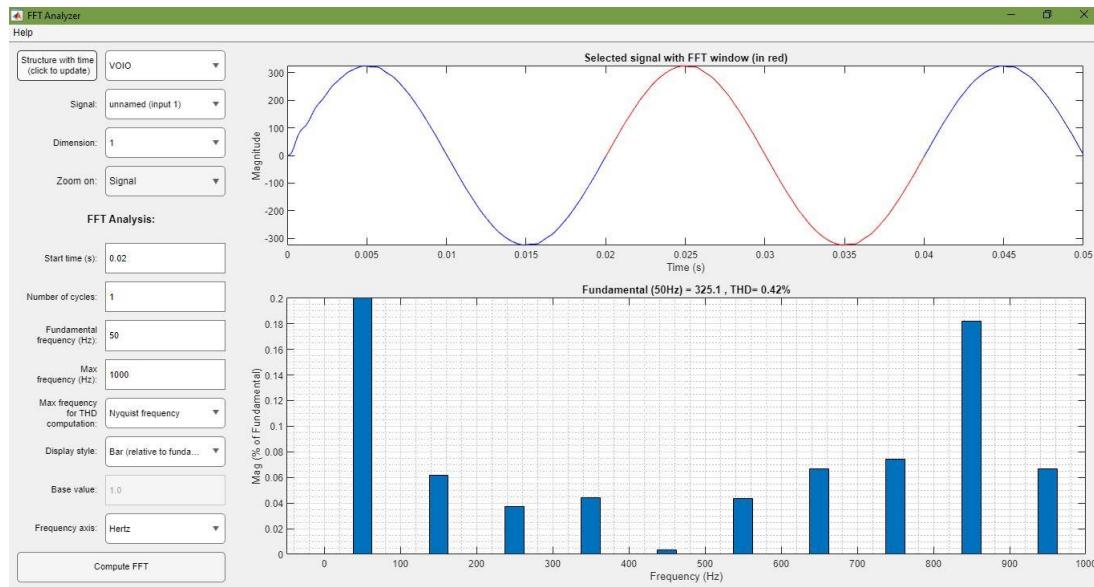


➤ WAVEFORMS OBTAINED:



➤ OBSERVATION: RMS value of $V_o = 299.8 \text{ V}$

➤ HARMONIC ANALYSIS:



4. Three Phase Full-Bridge VSI

➤ CIRCUIT DIAGRAM:

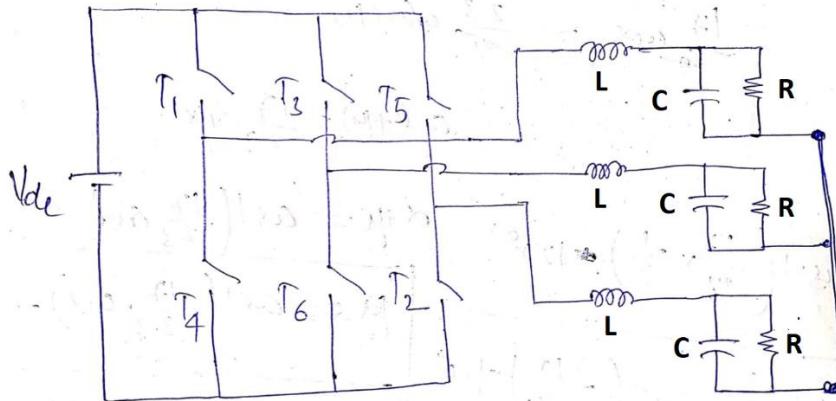


Fig 2: Three Phase full bridge VSI

➤ DESIGN PARAMETERS CALCULATION:

1). INPUT VOLTAGE:

We know that for three phase full bridge inverter the line to line RMS voltage of fundamental component of output voltage can be given as

$$V_{LL1} = \frac{\sqrt{3}m_a V_{dc}}{2\sqrt{2}} \Rightarrow V_{dc} = \frac{2\sqrt{2} V_{LL1}}{\sqrt{3}m_a} \quad (5)$$

Using the given RMS values as $V_{LL1} = 400 V$,

$$\text{And at } m_a = 0.4, \quad V_{dc} = \frac{2\sqrt{2} \cdot 400}{\sqrt{3} \cdot 0.4} = 1632.993 V \quad (6)$$

$$\text{And at } m_a = 0.8, \quad V_{dc} = \frac{2\sqrt{2} \cdot 400}{\sqrt{3} \cdot 0.8} = 816.4966 V \quad (7)$$

2). LOAD RESISTANCE:

Since the output power is given as $P = 10 \text{ kW}$, so by using power expression,

$$P = \frac{V_o^2}{R} \Rightarrow R = \frac{V_o^2}{P} = \frac{400^2}{10000} = 16 \Omega \quad (8)$$

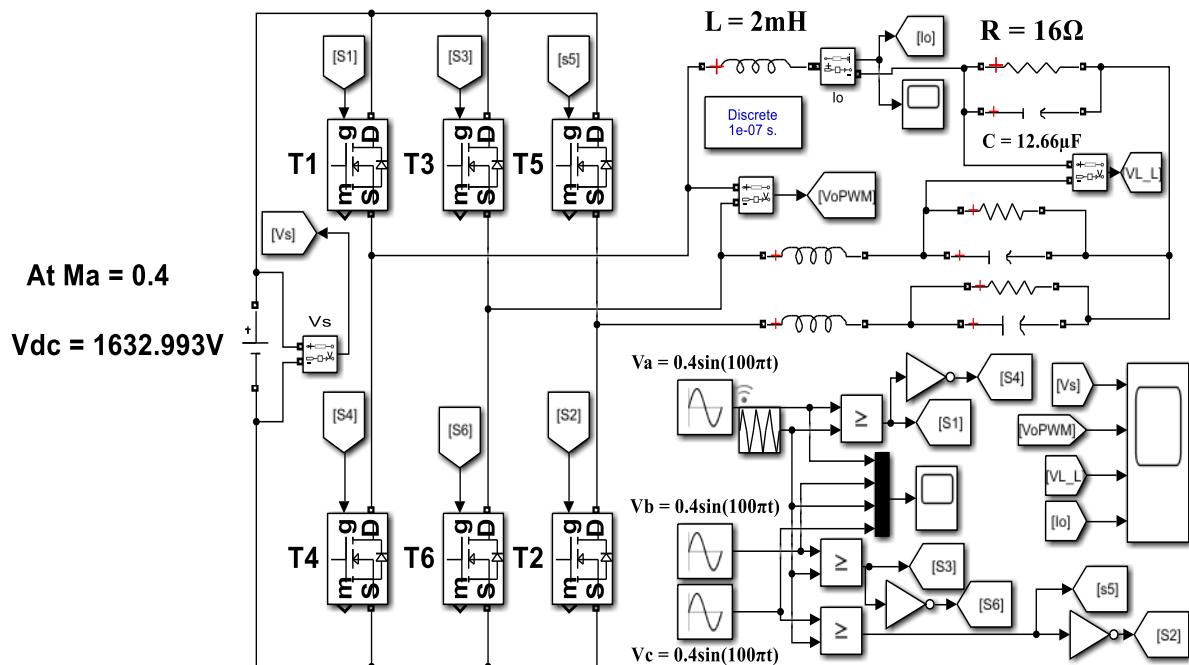
3). FILTER INDUCTOR & CAPACITOR:

Assuming $L = 2 \text{ mH}$, and since frequency is 10 kHz, so by using,

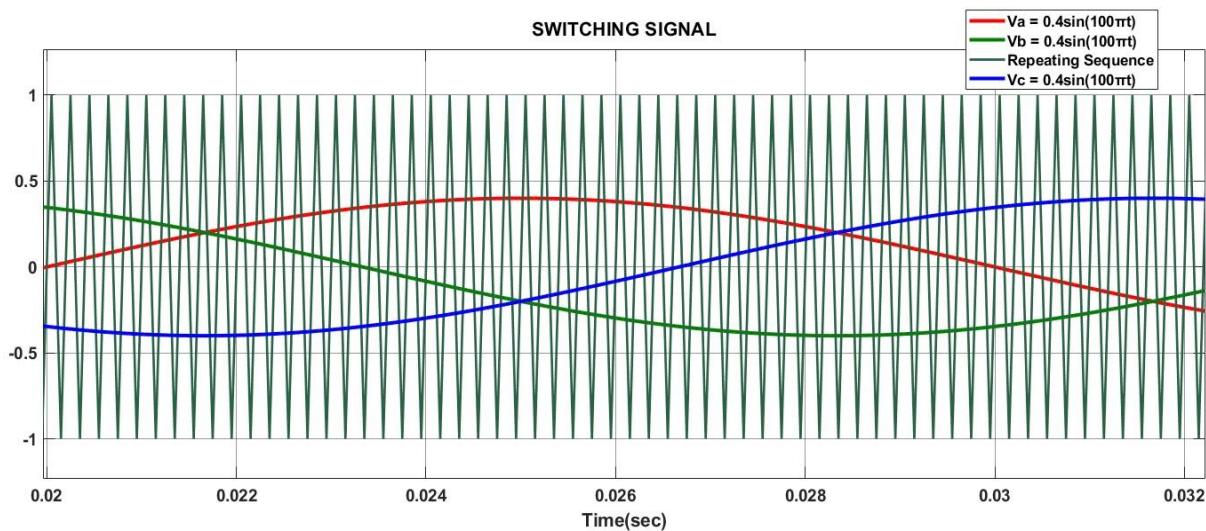
$$\omega = \frac{1}{\sqrt{LC}} \Rightarrow C = \frac{1}{4\pi^2 f^2 L} \Rightarrow C = 12.66 \mu\text{F} \quad (9)$$

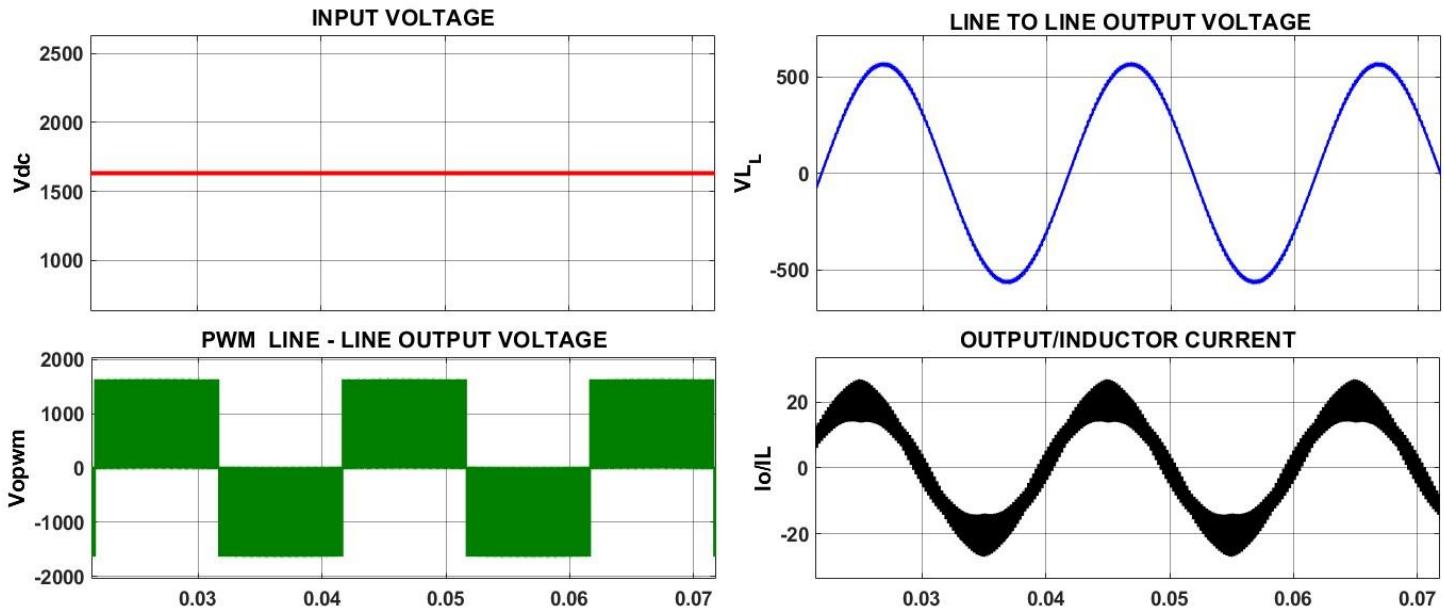
5. Simulation of Three Phase Full-Bridge VSI

➤ SIMULATED CIRCUIT:

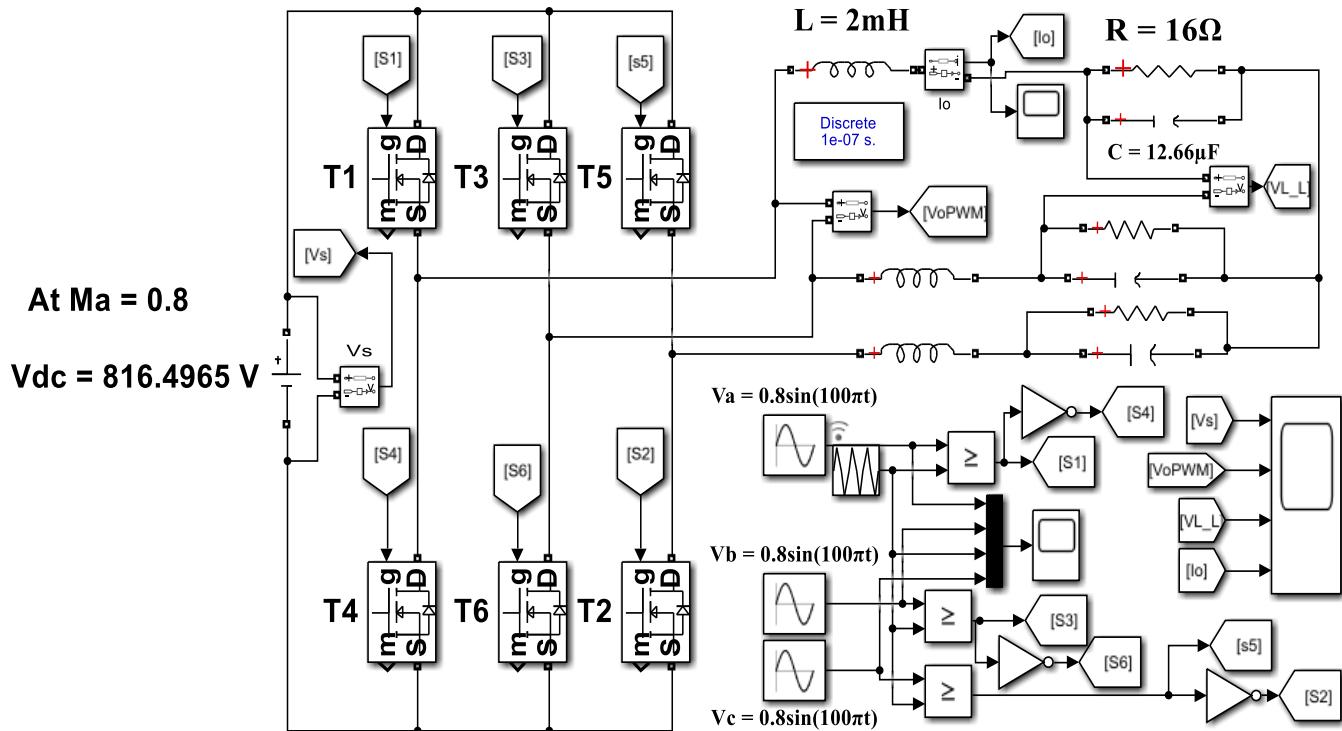


➤ WAVEFORMS OBTAINED:

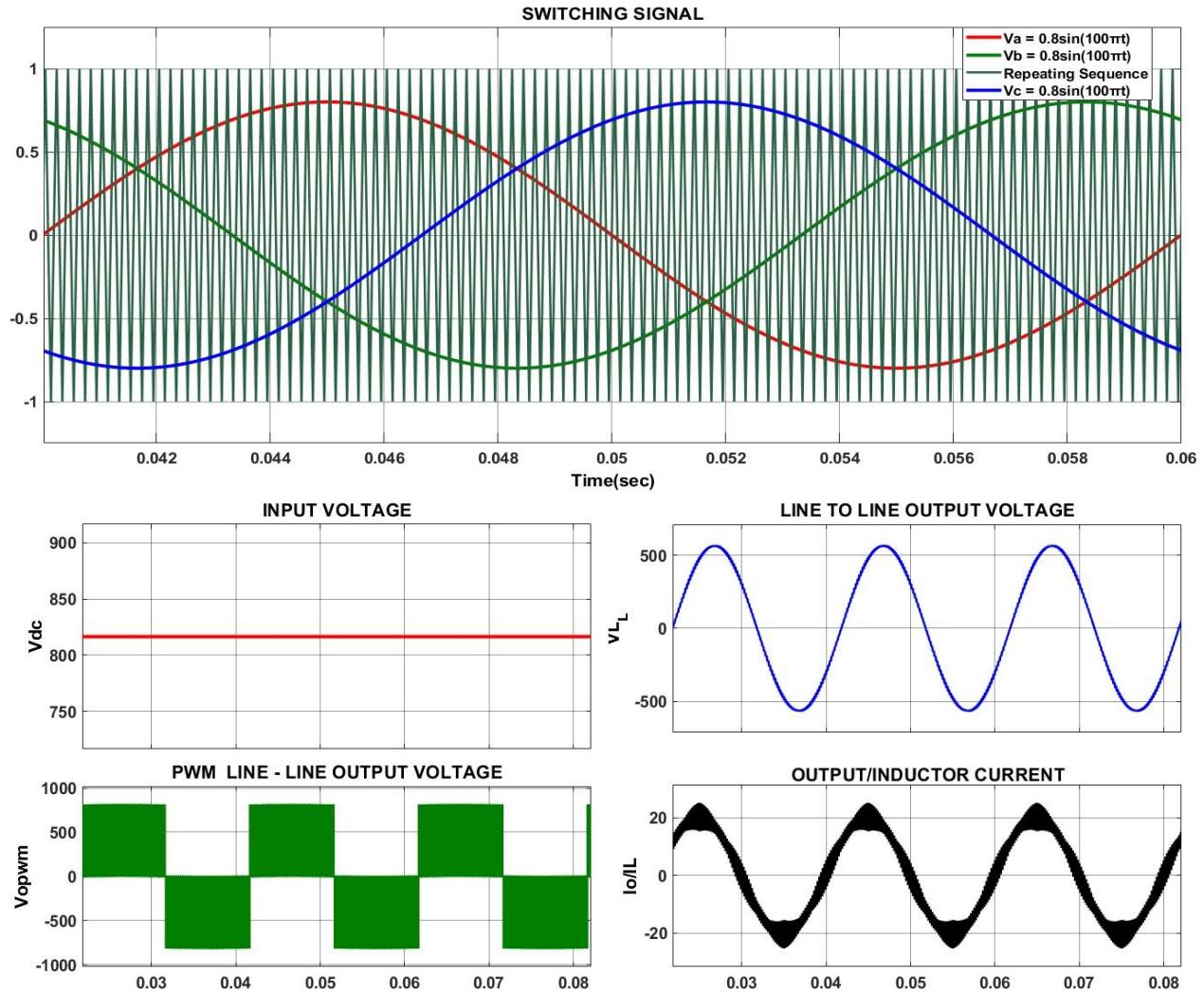




➤ SIMULATED CIRCUIT:



➤ WAVEFORMS OBTAINED:



➤ OBSERVATION:

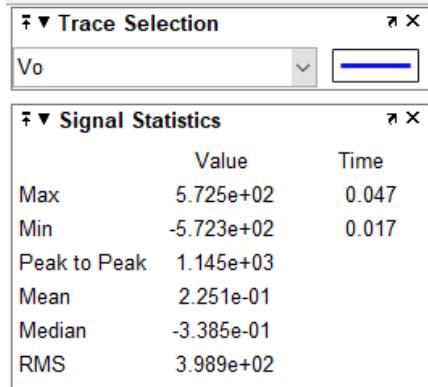


Fig 3: V_o at $m_a = 0.4$

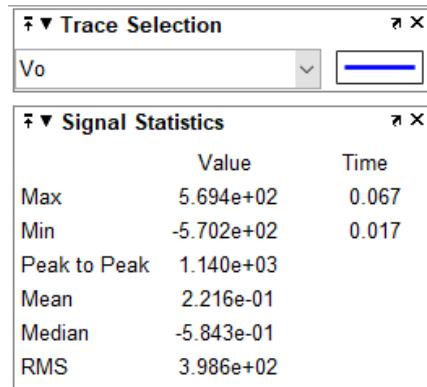


Fig 4: V_o at $m_a = 0.8$

➤ RMS value of V_o at $m = 0.4$: **398.9 V**

➤ RMS value of V_o at $m = 0.8$: **398.6 V**

Experiment - 07

To study the operation of Three phase Uncontrolled & Controlled Rectifiers using MATLAB/SIMULINK

NAME: SHIVRAJ VISHWAKARMA

ROLL. NO. 224102112

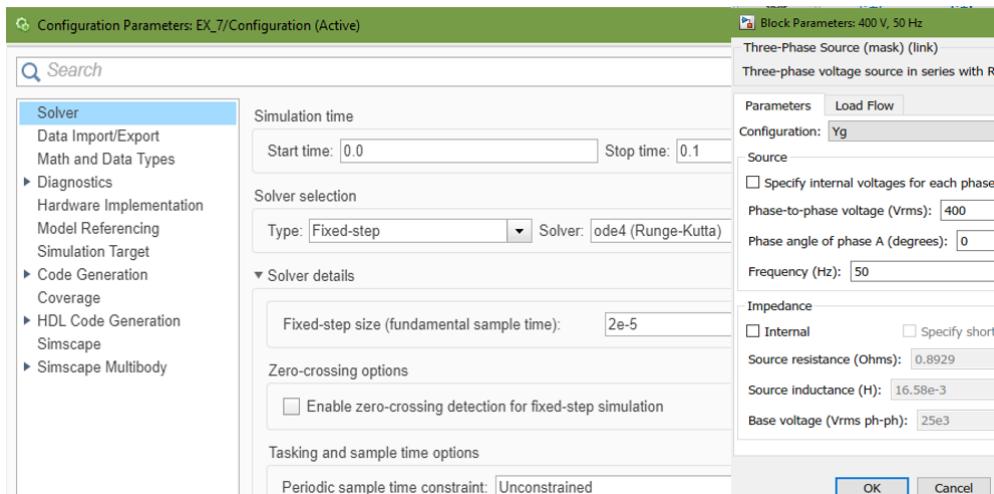
1. —DESIGN PARAMETERS:

Parameter	Value
Input voltage	400 V, 50 Hz, Three-phase
Source Inductance	10 mH
Load Parameters	20 Ω in series with 20 mH Inductance

2. PROCEDURE:

1. Simulate a Three-phase uncontrolled rectifier (with six diodes) with no source inductance. Take the snapshots for source voltages, source currents, load voltage, and load current. Note down the average value, peak-peak ripple in load voltage and load current.
2. Repeat step 2 with source inductance. Observe the change in load voltage and source current waveforms. Note down the commutation angle 'u' and match with the theoretical value.
3. Simulate a Three-phase controlled rectifier (with six Thyristors) with no source inductance. Consider four values of Firing angles: $\alpha = 30$ deg, $\alpha = 60$ deg, $\alpha = 120$ deg, and $\alpha = 150$ deg. Take the snapshots for source voltages, source currents, load voltage, and load current. Note down the average value, peak-peak ripple in load voltage and load current.
4. Repeat step 2 with source inductance. Observe the change in load voltage and source current waveforms. Note down the commutation angle 'u' and match with the theoretical value.

3. SIMULATION CONFIGURATION:



1. Full-Bridge Uncontrolled Rectifier without Ls

➤ CIRCUIT DIAGRAM:

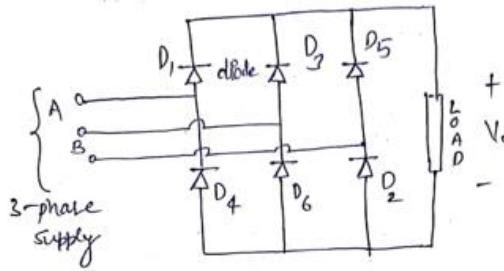
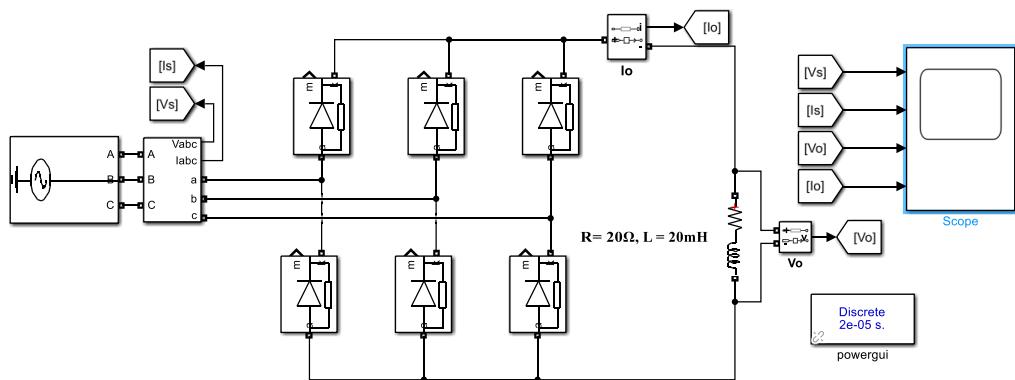
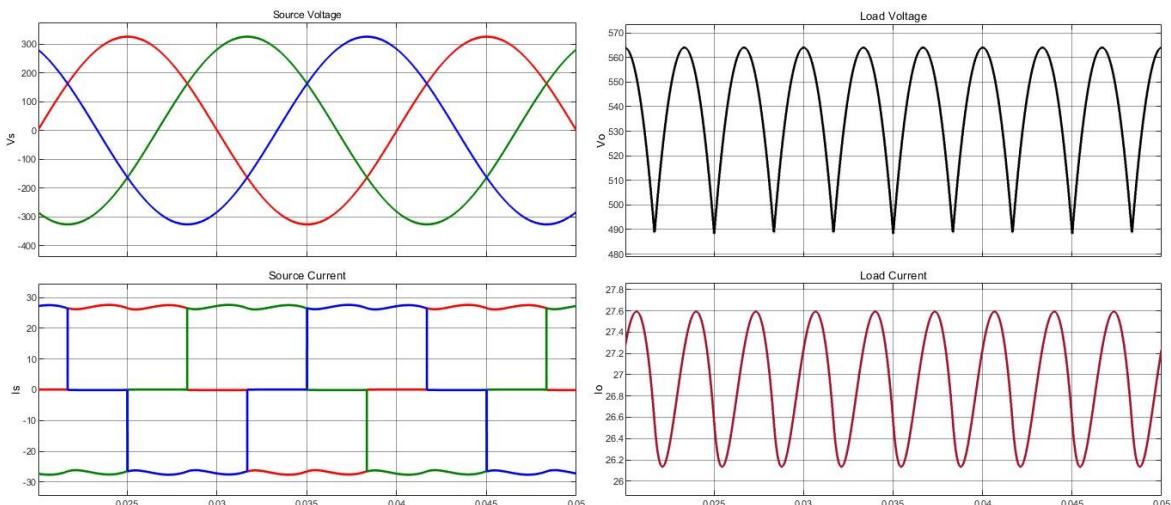


Fig 1: Three phase full wave Uncontrolled Rectifier

➤ SIMULATED CIRCUIT:



➤ WAVEFORMS OBTAINED:



Trace Selection			Trace Selection		
Vo	Value	Time	Io	Value	Time
Max	5.640e+02	0.030	Max	2.759e+01	0.041
Min	4.883e+02	0.045	Min	2.613e+01	0.025
Peak to Peak	7.577e+01		Peak to Peak	1.459e+00	
Mean	5.385e+02		Mean	2.693e+01	
Median	5.448e+02		Median	2.699e+01	
RMS	5.390e+02		RMS	2.693e+01	

➤ **OBSERVATION:**

LOAD	Peak – Peak Ripple in I_o
$R = 20 \Omega$ Case 1	16.16 A
$R = 20 \Omega, L = 20 \text{ mH}$ Case 2	12.22 A
$R = 20 \Omega, L = 200 \text{ mH}$ Case 3	2.154 A

2. Full-Bridge Uncontrolled Rectifier with $L_s = 10 \text{ mH}$

➤ **CIRCUIT DIAGRAM:**

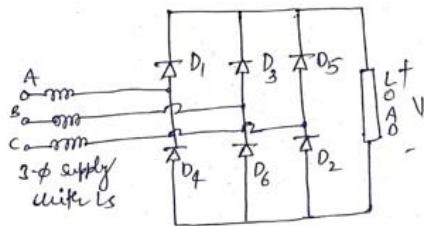
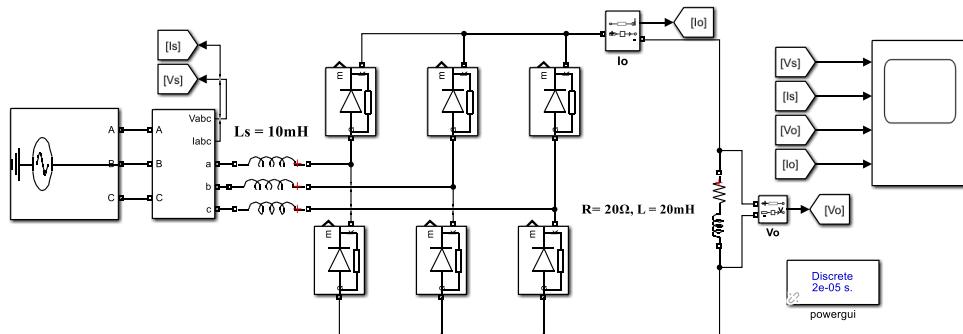
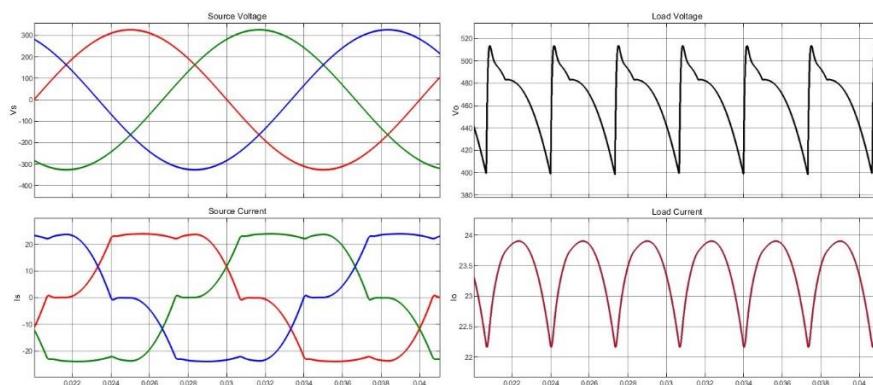


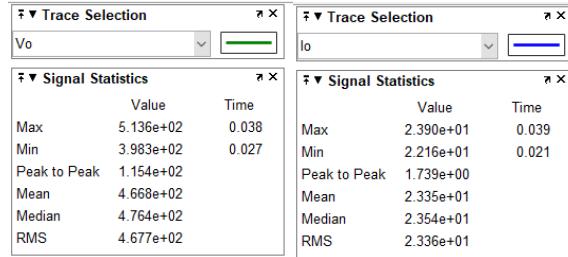
Fig 2: Three phase full wave Uncontrolled Rectifier with source inductance

SIMULATED CIRCUIT:



➤ **WAVEFORMS OBTAINED:**





➤ OBSERVATION:

Average value of Vo	466.8 V
Peak – Peak value of Vo	115.4 V
Average value of Io	23.35 A
Peak – Peak value of Io	1.739 A

➤ Calculation of Commutation Angle:

We can use the equation (5) derived in section 4 (controlled rectifier case) to find the value of commutation angle μ by taking $\alpha = 0 \Rightarrow \cos(\alpha + \mu) = \cos\alpha - \frac{2\omega L_s}{V_{max}} I_o$

$$\Rightarrow \mu = \cos^{-1}\left\{1 - \frac{2\omega L_s}{V_{ml}} I_o\right\}$$

Where I_o is average load current; $L_s = 10 \text{ mH}$ is source inductance; $\omega = 100\pi$ is source voltage frequency, $V_{ml} = 400\sqrt{2}$,

➤ **Obtained Result:** using $I_{o_{avg}} = 23.35 \text{ A}$, $\mu = \cos^{-1}(0.74065) \Rightarrow \mu = 42.21^\circ$

➤ **Theoretical value:**

By equation (7), taking $\alpha = 0$ for Diode Rectifier we get,

$$\mu = 2 \tan^{-1}\left\{\sqrt{\frac{3\omega L_s}{\pi R}}\right\} \Rightarrow \mu = 2 \tan^{-1}\left(\sqrt{\frac{3}{20}}\right) \Rightarrow \mu = 42.34^\circ$$

3. Full-Bridge Controlled Rectifier without Ls

➤ CIRCUIT DIAGRAM:

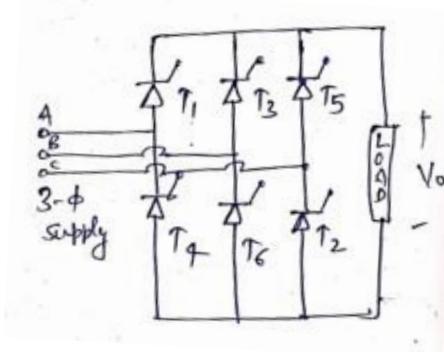
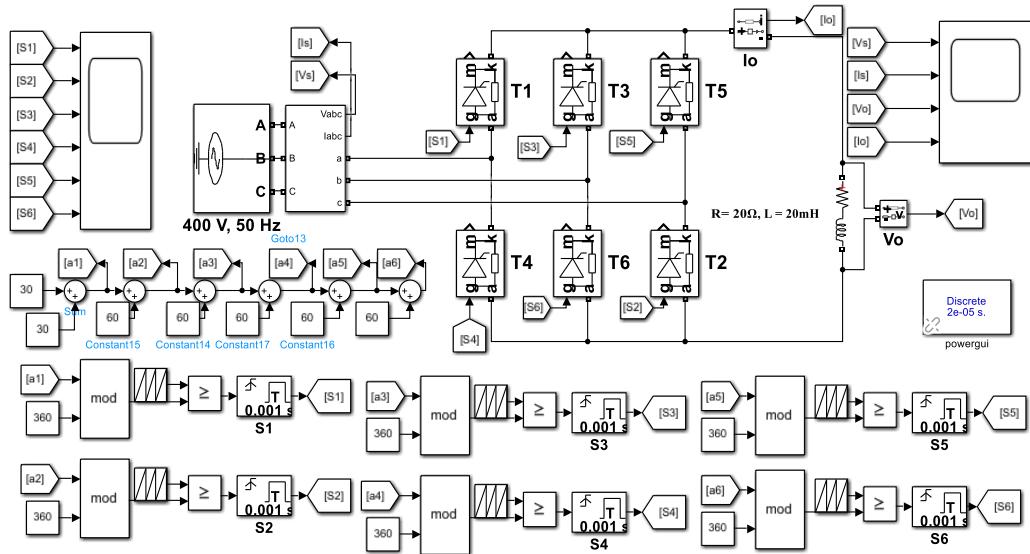


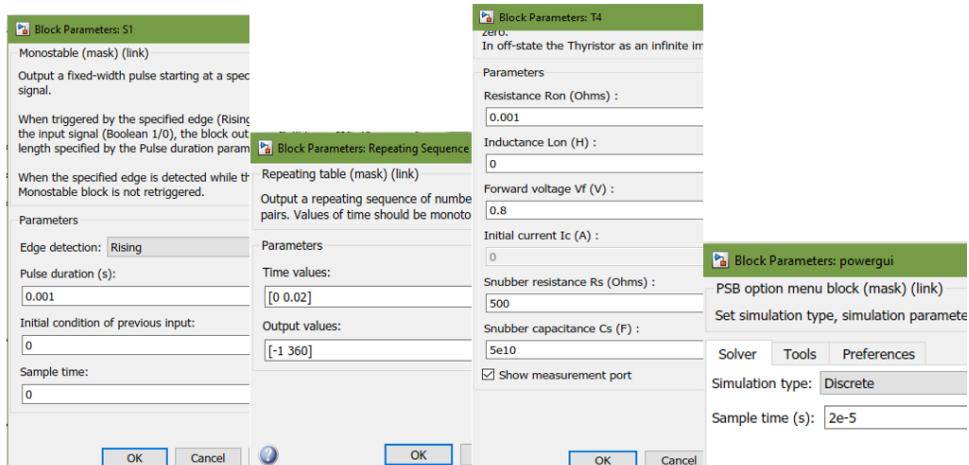
Fig 3: Three phase full wave Controlled Rectifier

1. At $\alpha = 30^\circ$,

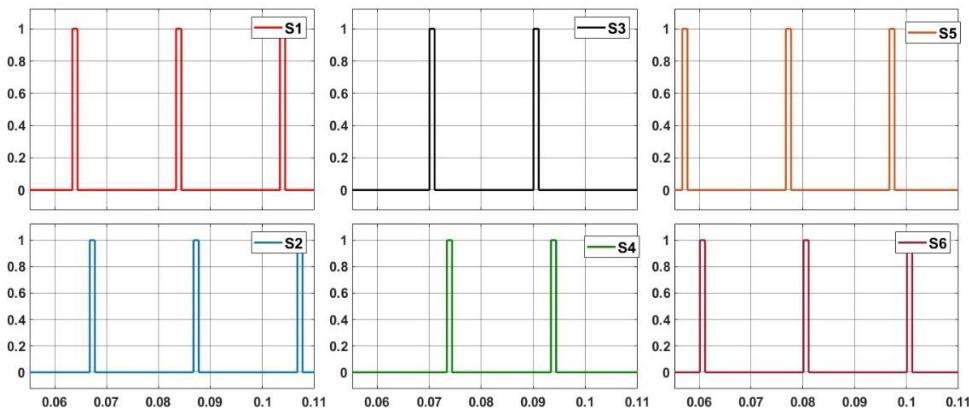
➤ SIMULATED CIRCUIT:



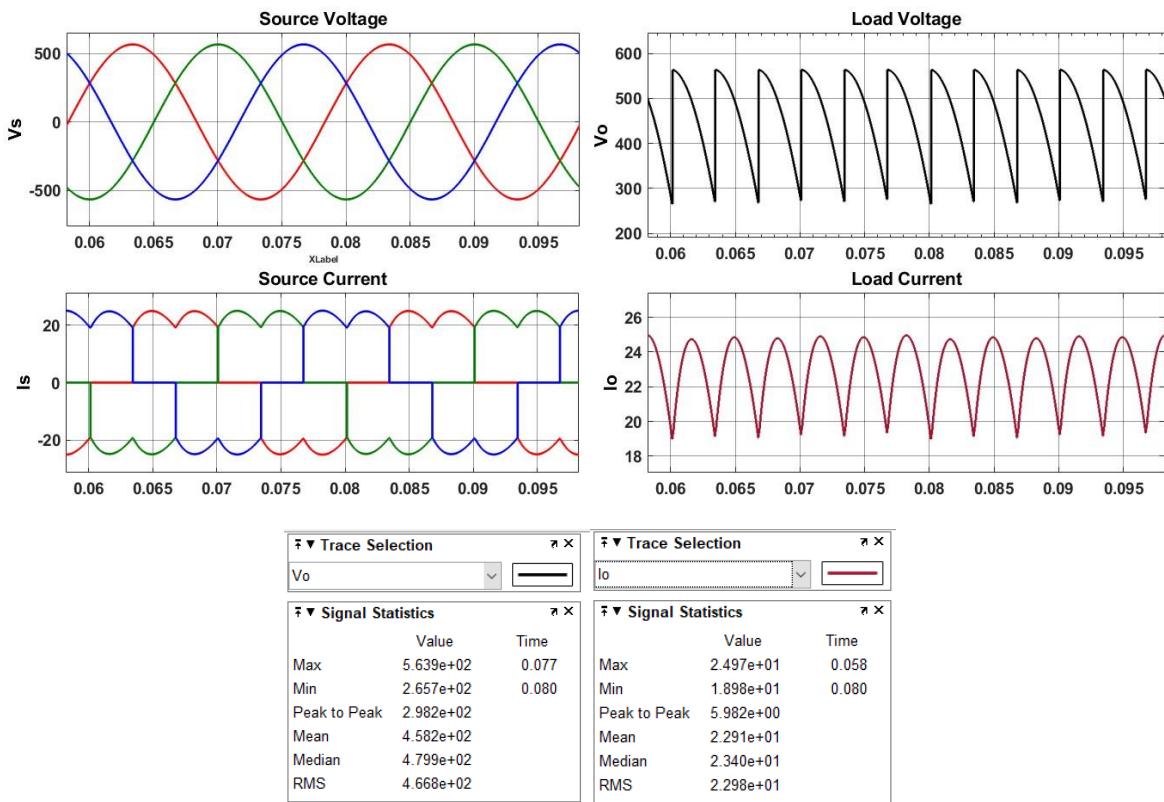
➤ SIMULATION CONFIGURATION:



➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:

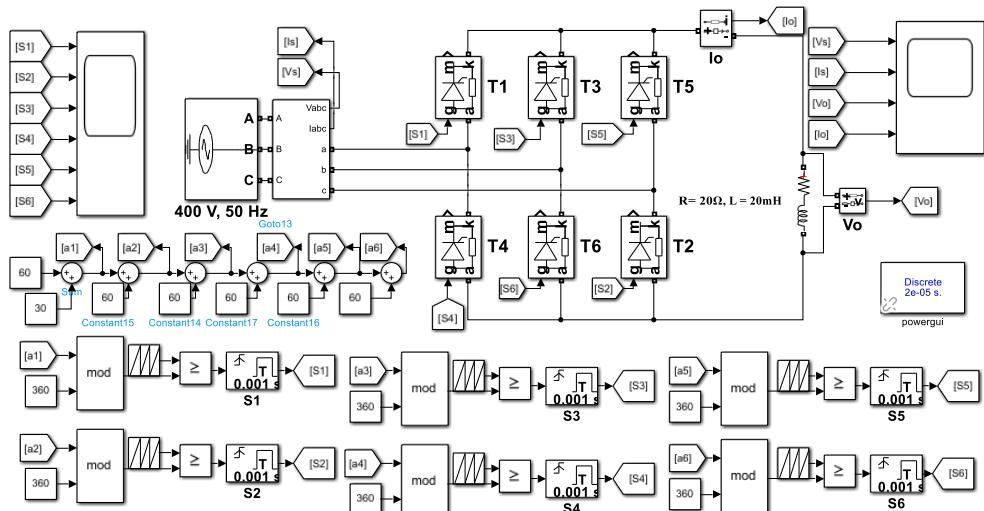


➤ OBSERVATION:

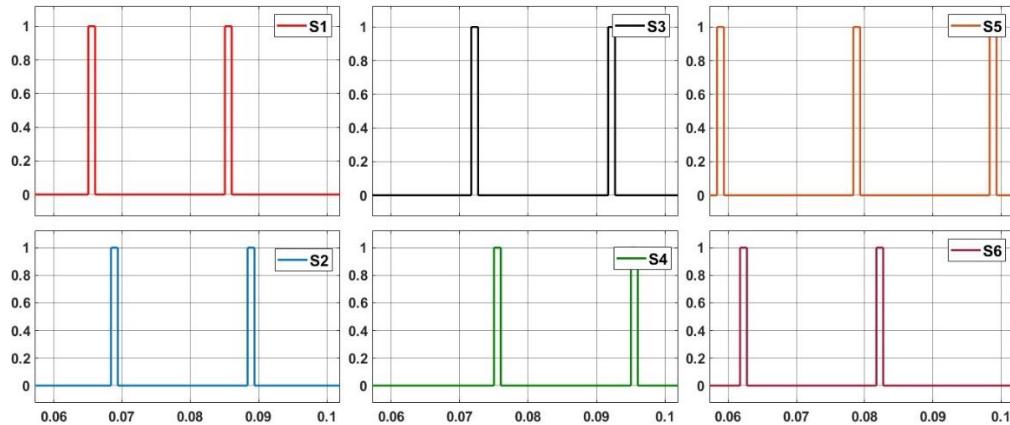
Average value of Vo	458.2 V
Peak – Peak value of Vo	298.2 V
Average value of Io	22.91 A
Peak – Peak value of Io	5.982 A

2. At $\alpha = 60^\circ$,

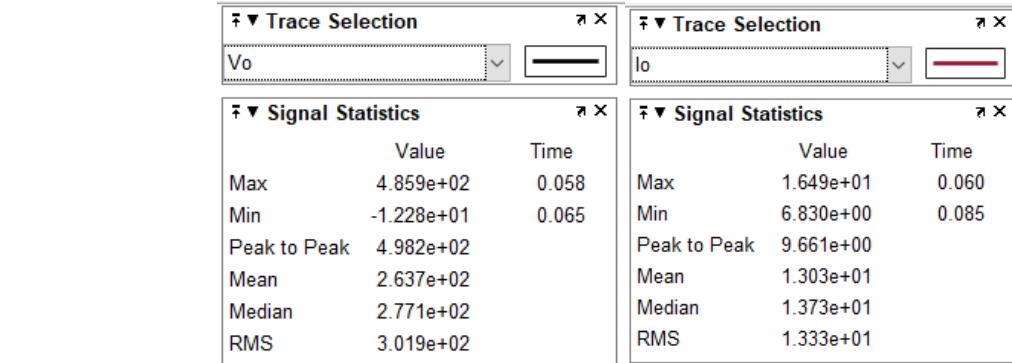
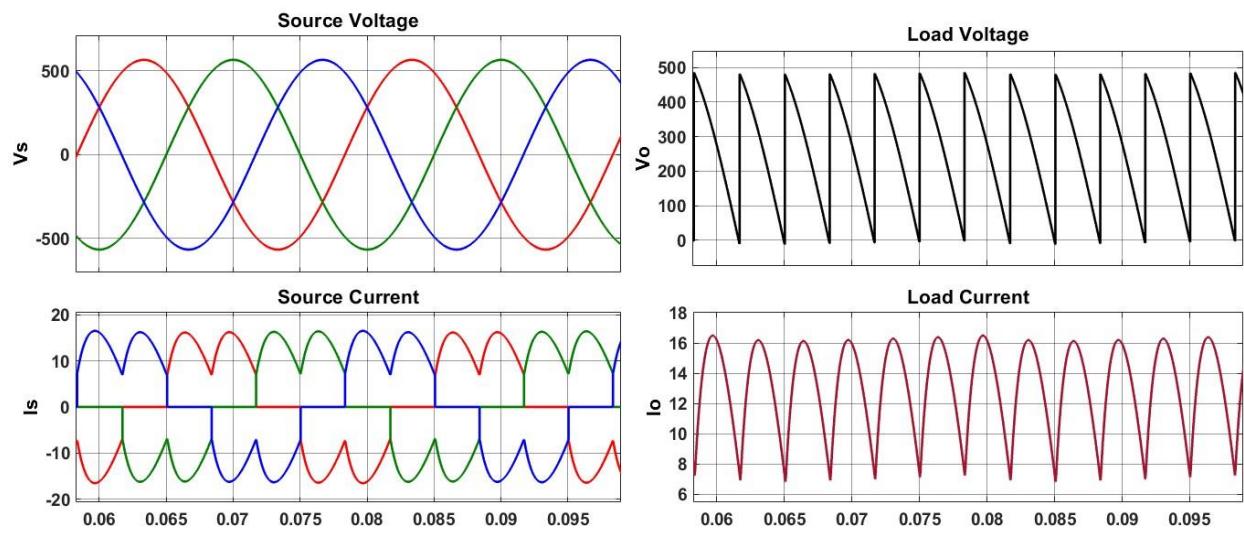
➤ SIMULATED CIRCUIT:



➤ **SWITCHING PULSE:**



➤ **WAVEFORMS OBTAINED:**

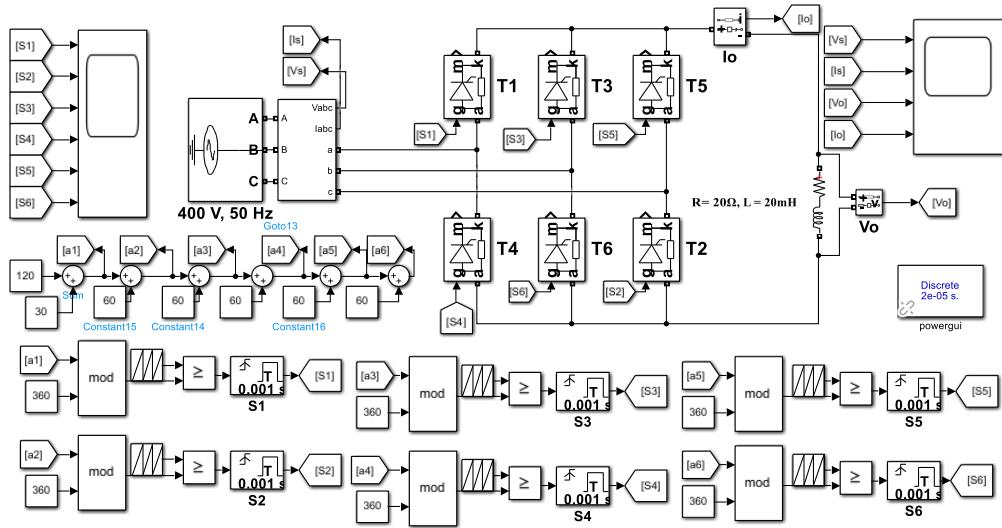


➤ **OBSERVATION:**

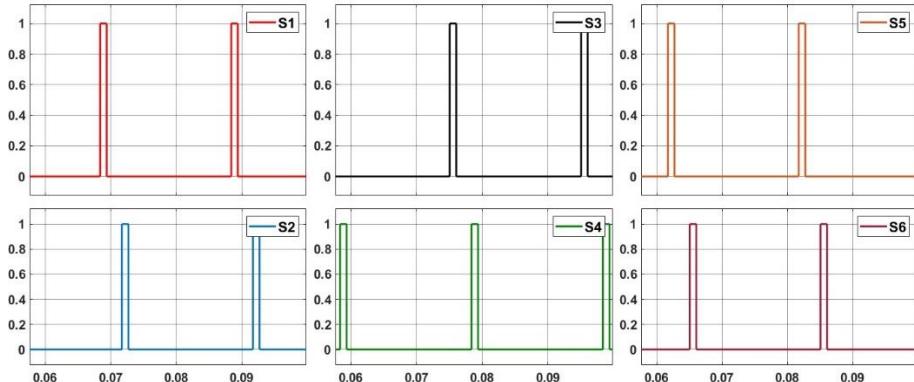
Average value of Vo	263.7 V
Peak – Peak value of Vo	498.2 V
Average value of Io	13.03 A
Peak – Peak value of Io	9.661 A

3. At $\alpha = 120^\circ$,

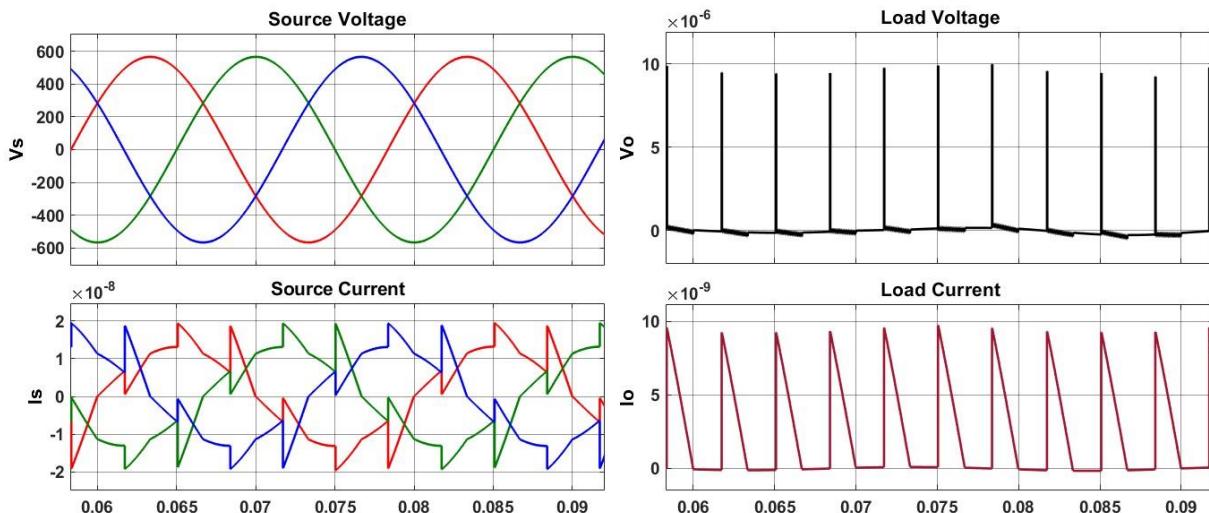
➤ SIMULATED CIRCUIT:

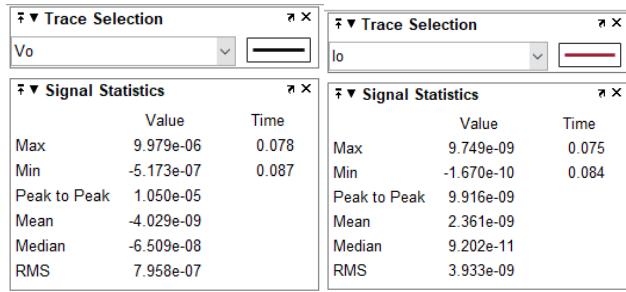


➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:



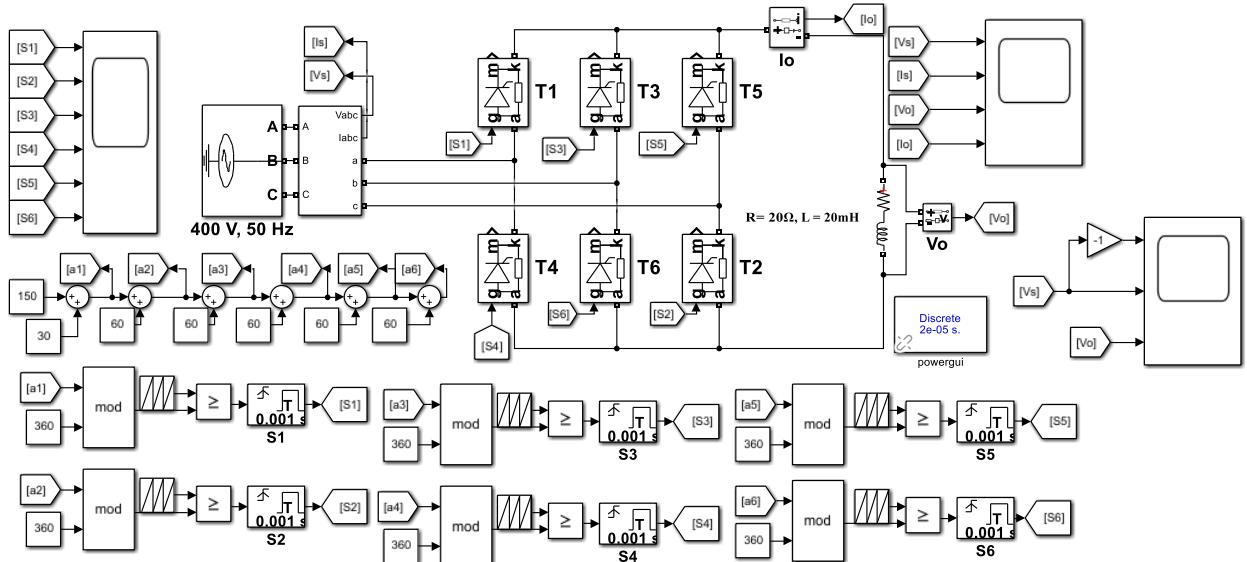


➤ OBSERVATION:

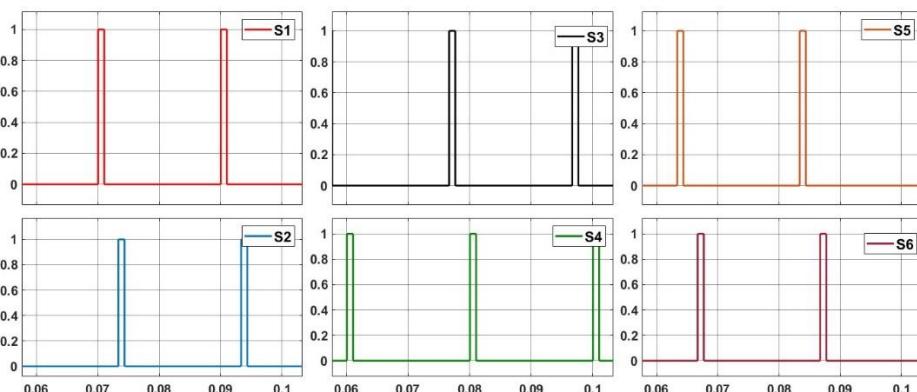
Average value of Vo	$-4.029 \times 10^{-9} \text{ V} \approx 0 \text{ V}$
Peak – Peak value of Vo	$1.05 \times 10^{-5} \text{ V} \approx 0 \text{ V}$
Average value of Io	$2.361 \times 10^{-9} \text{ A} \approx 0 \text{ A}$
Peak – Peak value of Io	$9.916 \times 10^{-9} \text{ A} \approx 0 \text{ A}$

4. At $\alpha = 150^\circ$,

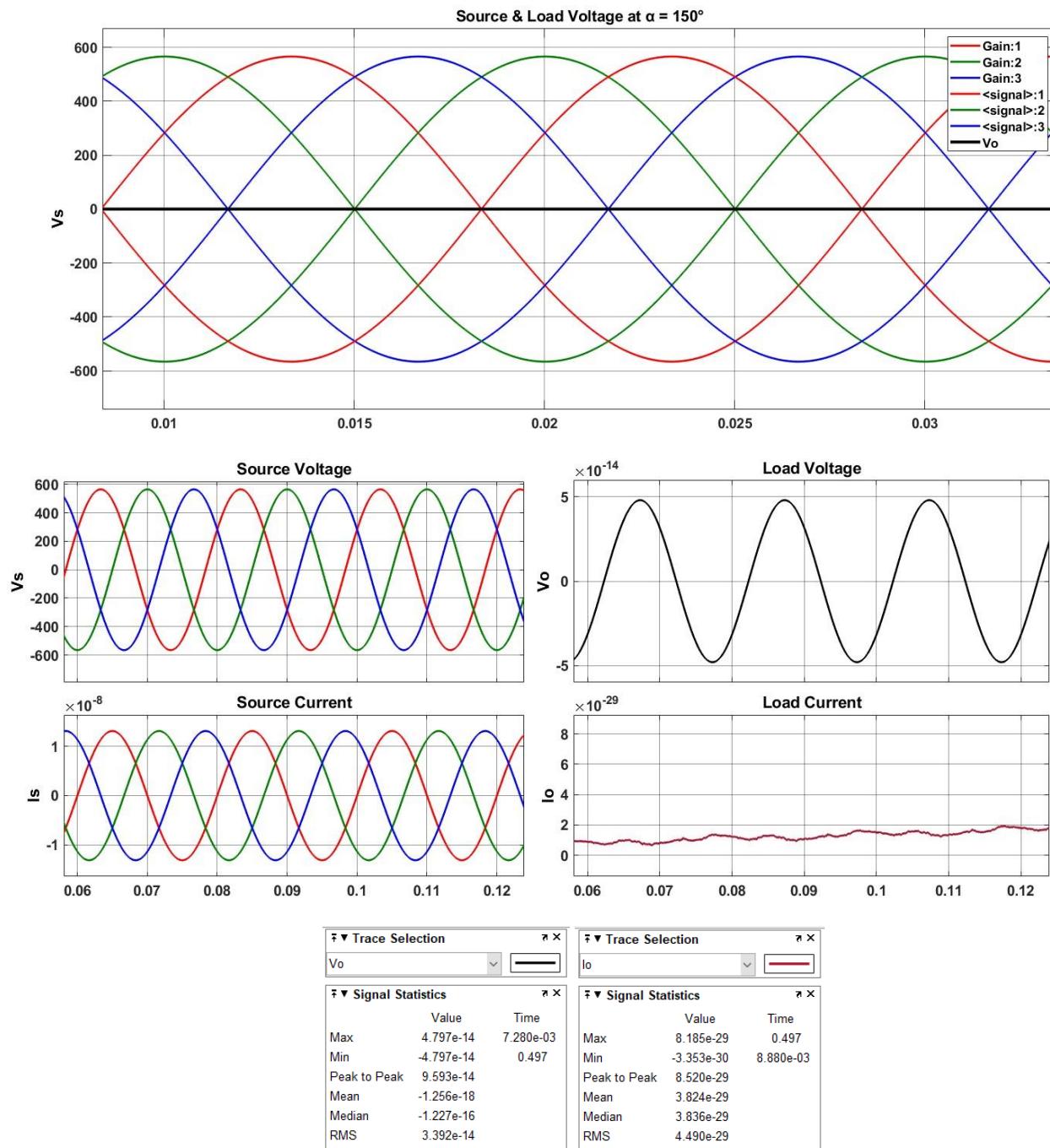
➤ SIMULATED CIRCUIT:



➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:



➤ OBSERVATION:

Average value of Vo	$-1.256 \times 10^{-18} \text{ V} \approx 0 \text{ V}$
Peak – Peak value of Vo	$9.593 \times 10^{-14} \text{ V} \approx 0 \text{ V}$
Average value of Io	$3.824 \times 10^{-29} \text{ A} \approx 0 \text{ A}$
Peak – Peak value of Io	$8.520 \times 10^{-29} \text{ A} \approx 0 \text{ A}$

➤ Clarification for the waveform of output at $\alpha = 120^\circ$ & 150° :

- For the given load the output voltage and current is coming approximately 0 because this load is not an ideal load the inductance is very low.
- If we apply an ideal load here or a constant current load then we will be getting some output waveform according to the firing angle & it will be comparable with our source waveform.

4. Full-Bridge Controlled Rectifier with $L_s = 10 \text{ mH}$

➤ CIRCUIT DIAGRAM:

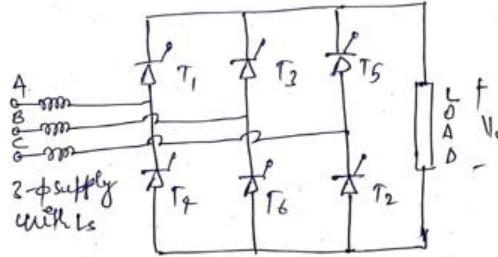


Fig 4: Three phase full wave Controlled Rectifier with source inductance

➤ Calculation of Commutation Angle:-

Until now, the current commutation between thyristors has been considered to be instantaneous. This condition is not valid in real cases due to the presence of the source/Line inductance L . During the commutation, the current through the thyristors cannot change instantaneously, and for this reason, during the commutation angle μ , all thyristors are conducting simultaneously.

Therefore, during the commutation, the following relationship for the load voltage holds

$$v_o = 0 \quad \forall \quad \alpha \leq \omega t \leq \alpha + \mu \quad (1)$$

The effect of the commutation on the supply current, the voltage waveforms, and the thyristor current waveforms can be observed in below Fig.

During the commutation, the following expression holds

$$L \frac{di_s}{dt} = v_s = V_{max} \sin(\omega t) \quad \alpha \leq \omega t \leq \alpha + \mu \quad (2)$$

Integrating above Eq. over the commutation interval yields

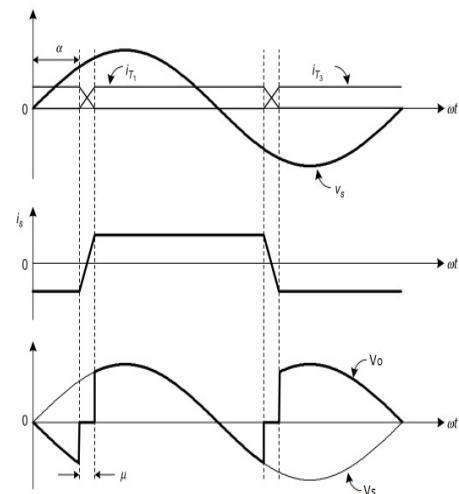
$$\int_{-I_o}^{I_o} di_s = \frac{V_{max}}{L} \int_{\alpha/\omega}^{\alpha+\mu/\omega} \sin(\omega t) dt \quad (3)$$

$$I_{o\text{avg}} = \frac{V_m}{2\omega L_s} \{ \cos \alpha - \cos(\alpha + \mu) \} \quad (4)$$

From above Eq., the following relationship for the commutation angle μ is obtained:

$$\cos(\alpha + \mu) = \cos \alpha - \frac{2\omega L_s}{V_{max}} I_o \quad (5)$$

For three Phase full wave-controlled rectifier the expression for the average load voltage is given by



$$V_{o(avg)} = \frac{1}{\pi} \left\{ \int_{60+\alpha}^{60+\alpha+\mu} -\frac{3}{2} V_{BO} d(\omega t) + \int_{60+\alpha+\mu}^{120+\alpha} V_{AB} d(\omega t) \right\}$$

$$= \frac{3V_{ml}}{2\pi} [\cos(\alpha + \mu) + \cos\alpha] \quad (6)$$

Since $I_{O_{avg}} = \frac{V_{O_{avg}}}{R}$ therefore using equation (4) and (6) we get

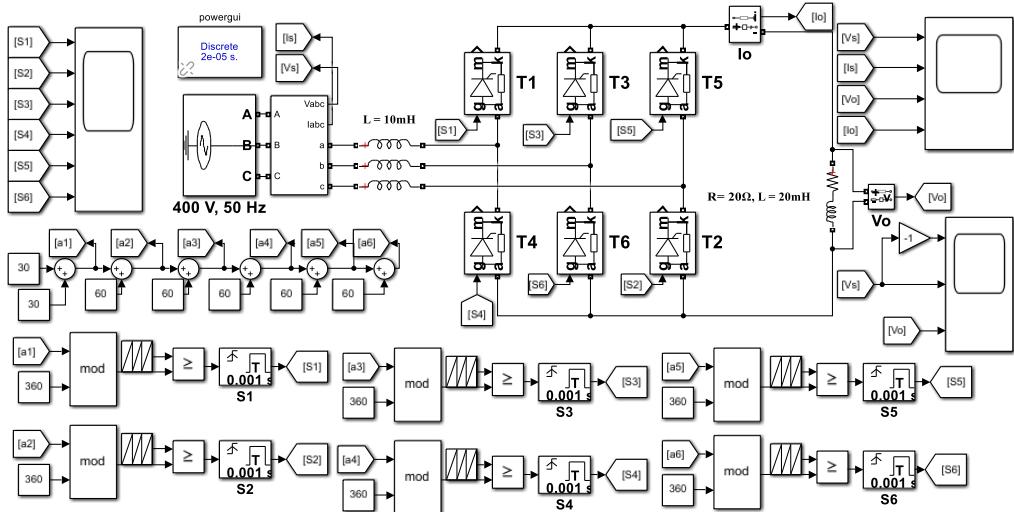
$$\frac{\cos\alpha - \cos(\alpha + \mu)}{\cos\alpha + \cos(\alpha + \mu)} = \frac{3\omega L_S}{\pi R} \quad (7)$$

Using given parameter as source inductance $L_S = 10 \text{ mH}$; $\omega = 100\pi$, & $R = 20 \Omega$, & equation 7

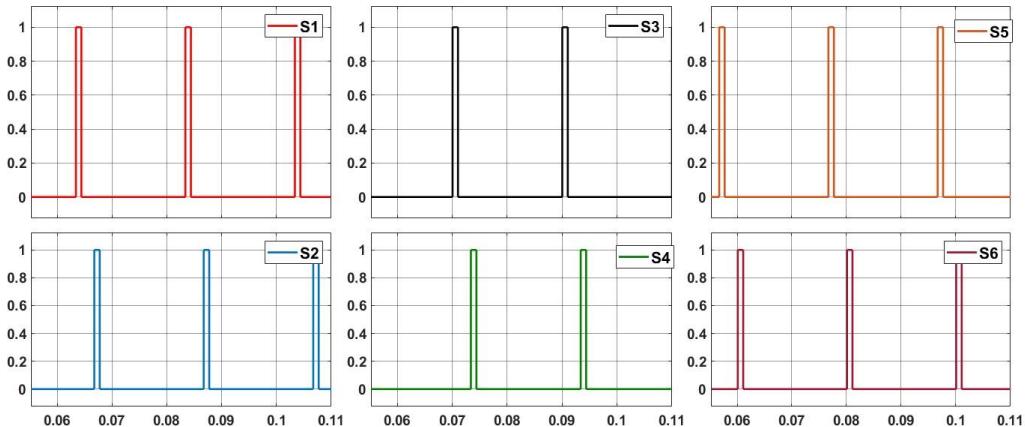
$$\mu = \{\cos^{-1}\left(\frac{17}{23}\cos\alpha\right) - \alpha\}^\circ \quad (8)$$

1. At $\alpha = 30^\circ$,

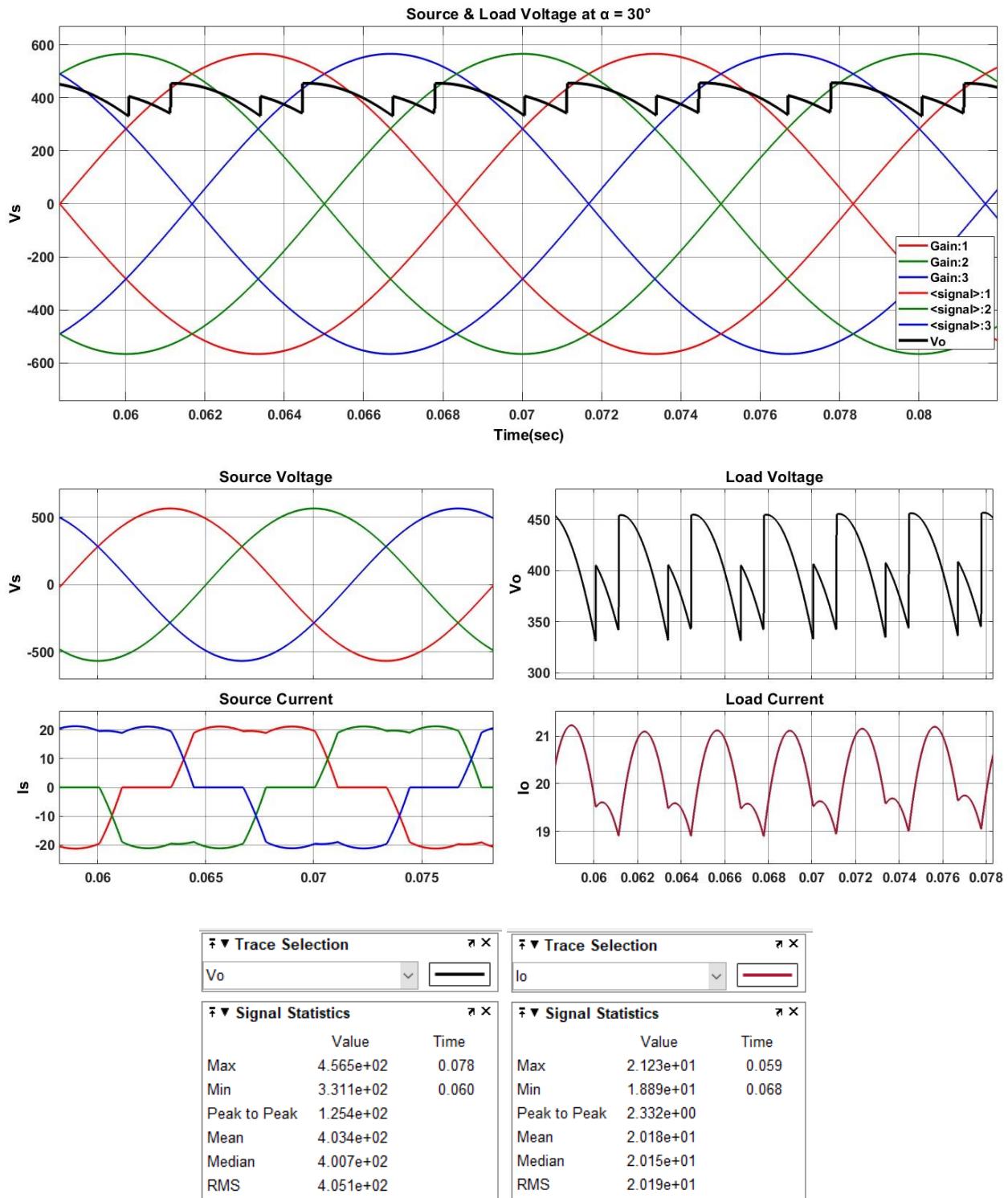
➤ **SIMULATED CIRCUIT:**



➤ **SWITCHING PULSE:**



➤ **WAVEFORMS OBTAINED:**



➤ **Calculation of Commutation Angle:-**

➤ **Obtained Result:**

Using equation (5) & given parameter as

source inductance $L_s = 10 \text{ mH}$; $\omega = 100\pi$, $V_{max} = 400\sqrt{2}$, & $R = 20 \Omega$, $\alpha = 30^\circ$, $I_o = 20.18 \text{ A}$ we get,

$$\cos(30^\circ + \mu) = \left\{ \frac{\sqrt{3}}{2} - \frac{20.18\pi}{200\sqrt{2}} \right\} \Rightarrow \mu = \cos^{-1}(0.641882) - 30^\circ \Rightarrow \mu = 20.068^\circ$$

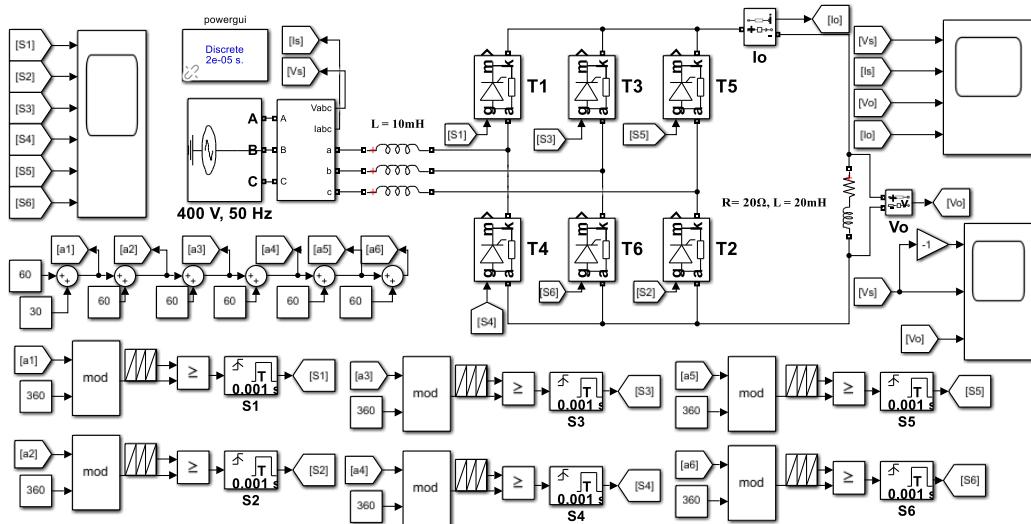
➤ Theoretical value:

By using equation (8) with the value of $\alpha = 30^\circ$ we get,

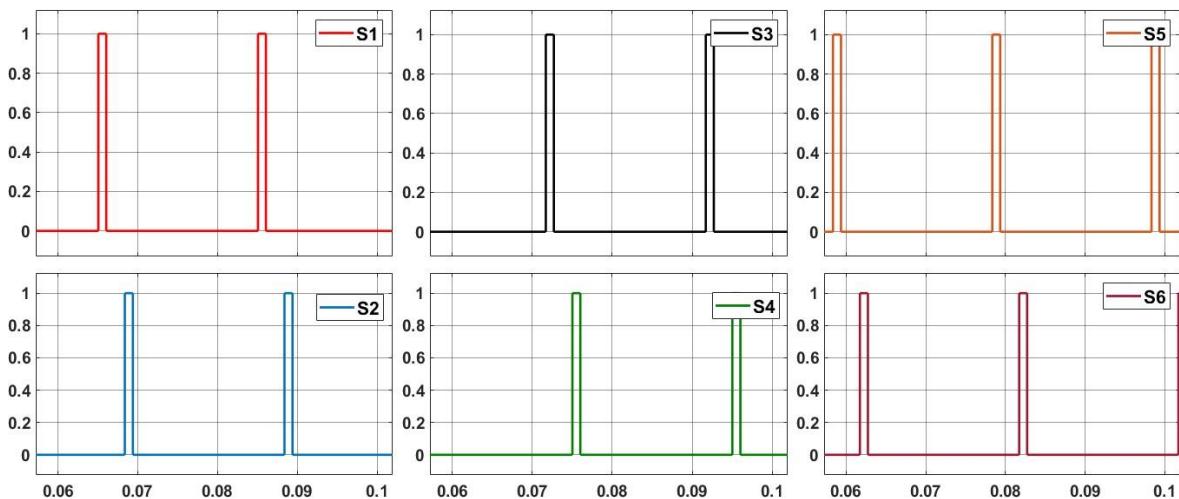
$$\mu = \left\{ \cos^{-1} \left(\frac{17}{23} \cos \alpha \right) - \alpha \right\}^\circ \Rightarrow \mu = 20.20^\circ$$

➤ $\alpha = 60^\circ$

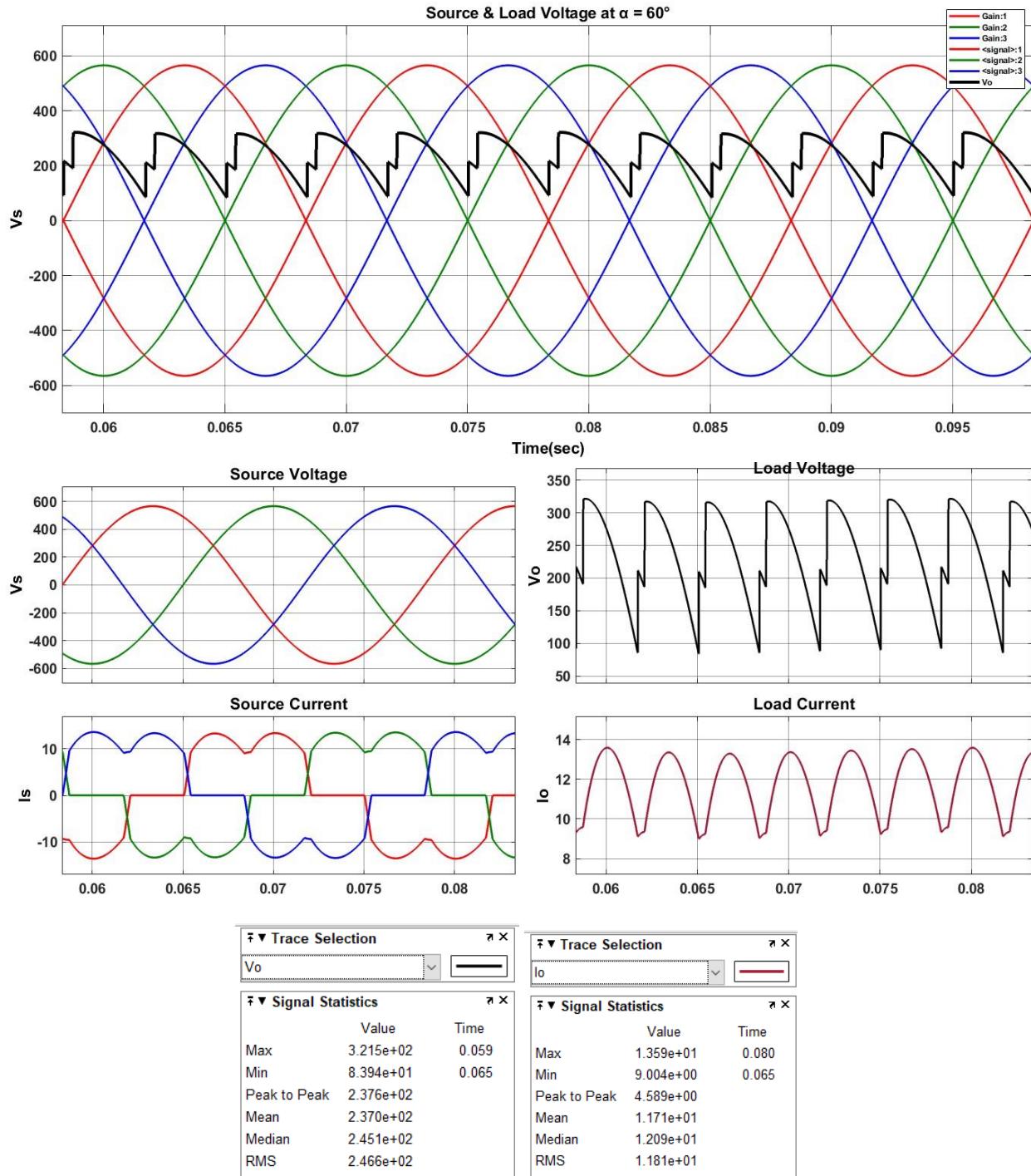
➤ SIMULATED CIRCUIT:



➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:



➤ Calculation of Commutation Angle:-

➤ Obtained Result:

Using equation (5) & given parameter as

source inductance $L_s = 10 \text{ mH}$; $\omega = 100\pi$, $V_{max} = 400\sqrt{2}$, & $R = 20 \Omega$, $\alpha = 60^\circ$, $I_o = 11.71 \text{ A}$ we get,

$$\cos(60^\circ + \mu) = \left\{ \frac{1}{2} - \frac{11.71\pi}{200\sqrt{2}} \right\} \Rightarrow \mu = \cos^{-1}(0.3699) - 60^\circ \Rightarrow \mu = 8.288^\circ$$

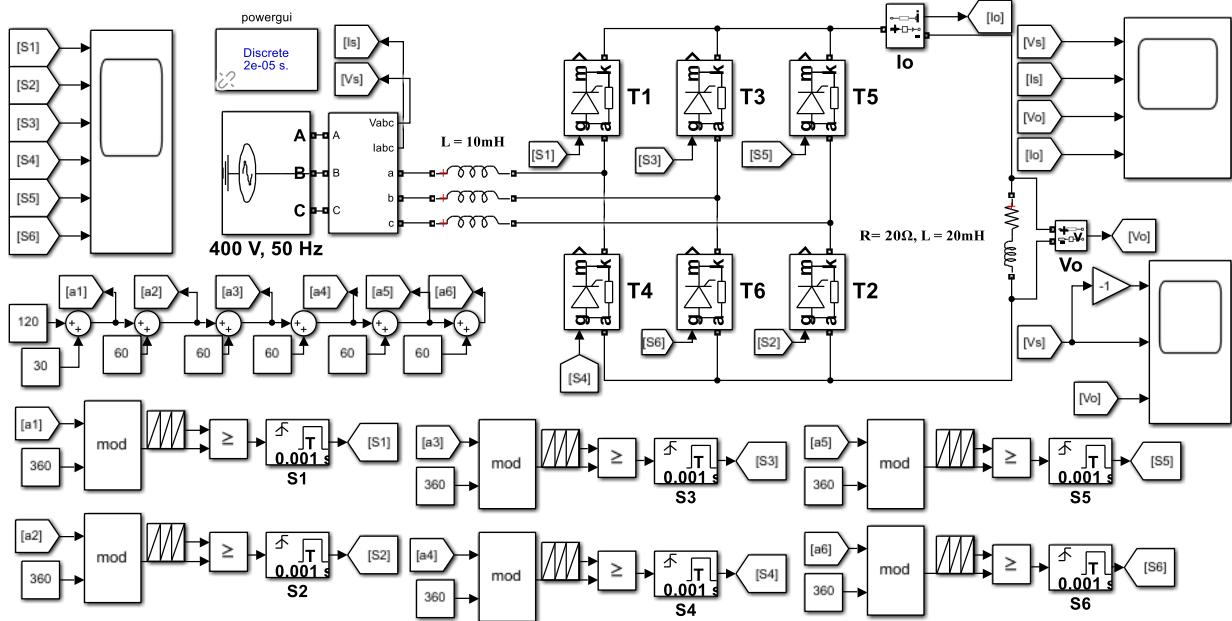
➤ Theoretical value:

By using equation (8) with the value of $\alpha = 30^\circ$ we get,

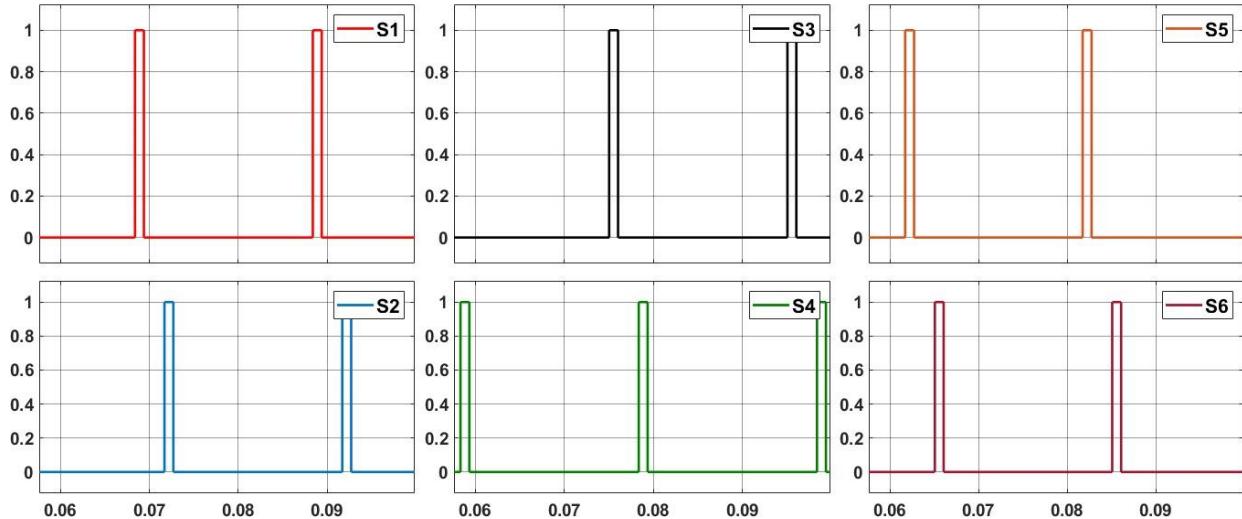
$$\mu = \{\cos^{-1}\left(\frac{17}{23}\cos\alpha\right) - \alpha\}^\circ \Rightarrow \mu = 8.31^\circ$$

➤ $\alpha = 120^\circ$

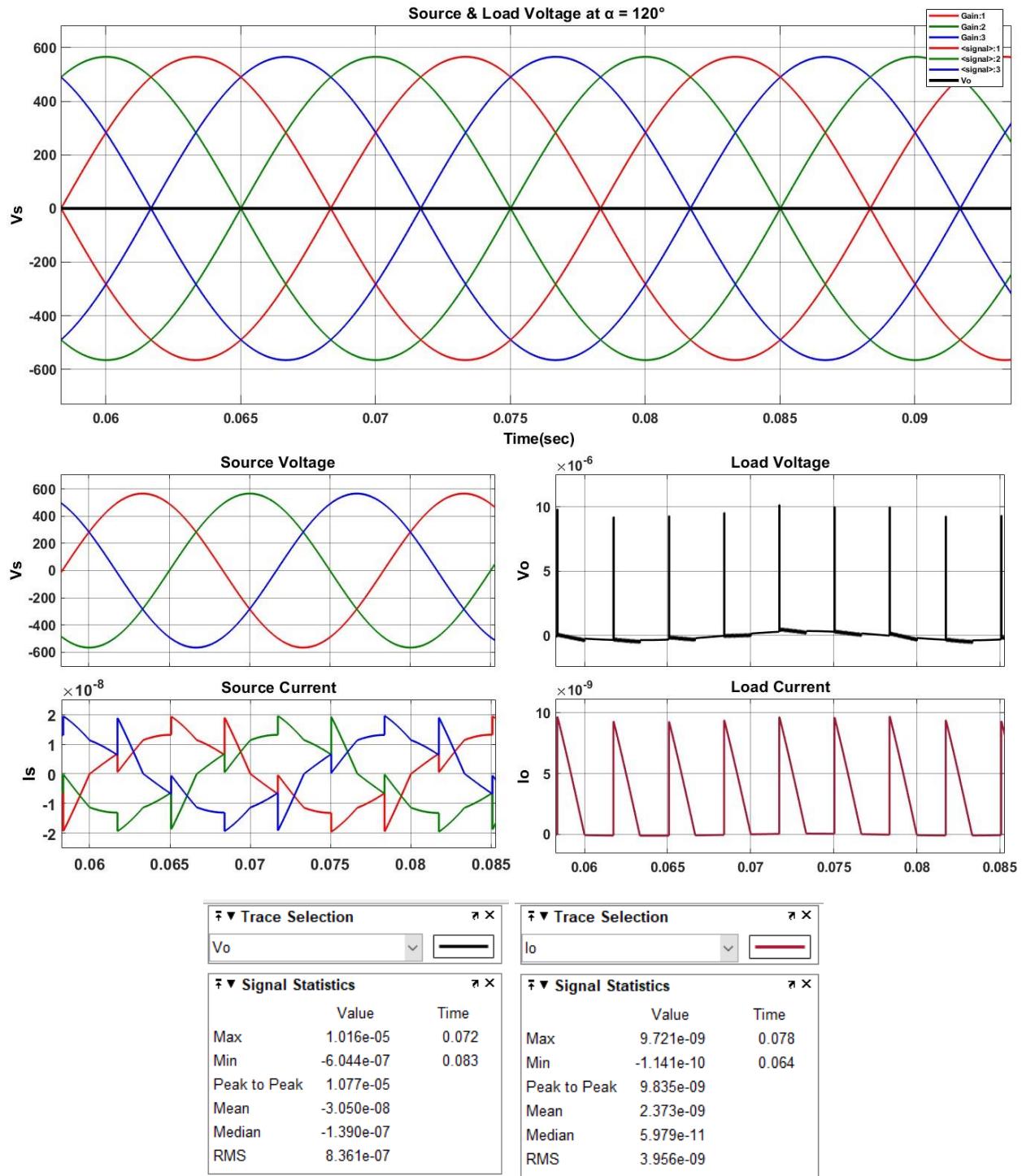
➤ SIMULATED CIRCUIT:



➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:



➤ Calculation of Commutation Angle:-

➤ Obtained Result:

Using equation (5) & given parameter as

source inductance $L_s = 10 \text{ mH}$; $\omega = 100\pi$, $V_{max} = 400\sqrt{2}$, & $R = 20 \Omega$, $\alpha = 120^\circ$, $I_0 = 2.373 \times 10^{-9} \approx 0 \text{ A}$ we get,

$$\cos(\alpha + \mu) = \{\cos \alpha\} \Rightarrow \mu = 0^\circ$$

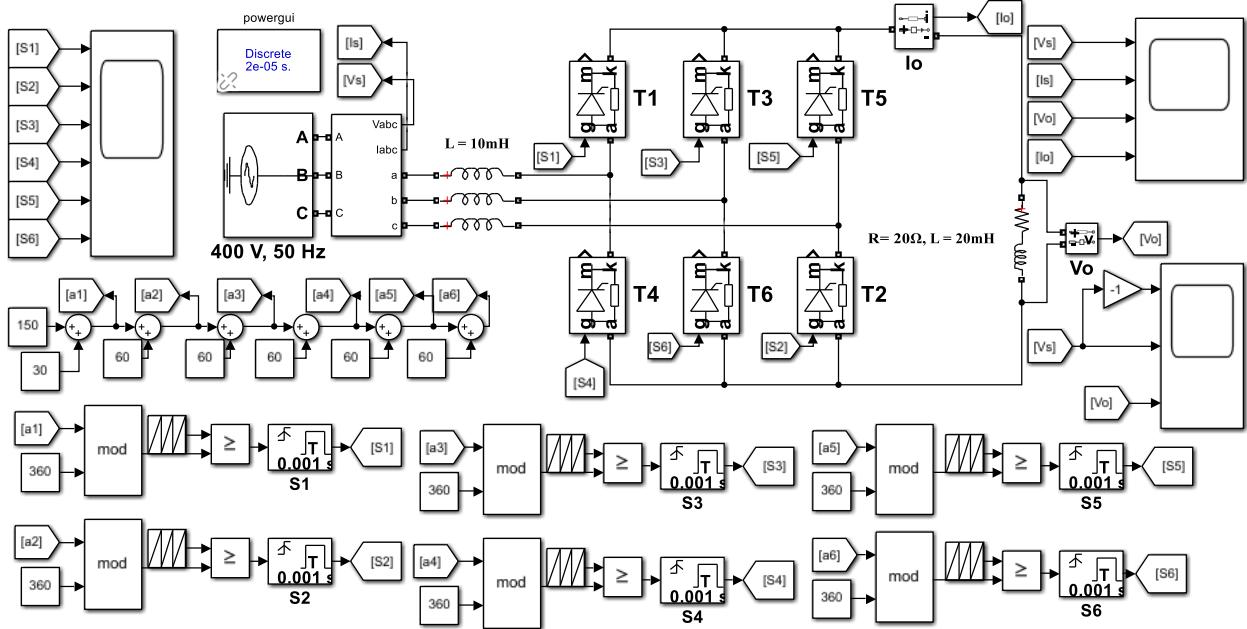
➤ Theoretical value:

By using equation (8) with the value of $\alpha = 30^\circ$ we get,

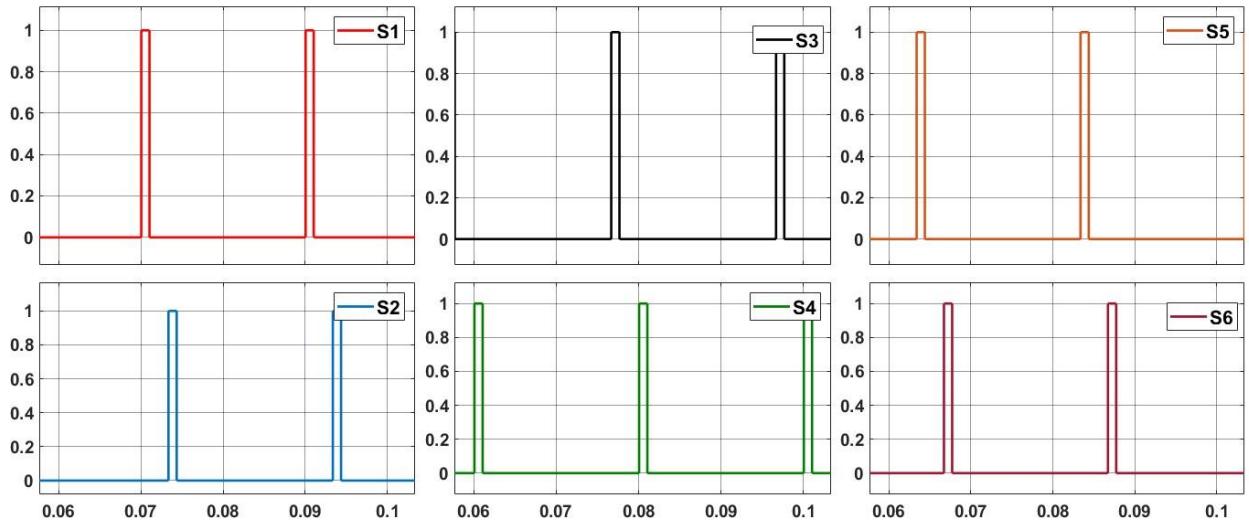
$$\mu = \{\cos^{-1}\left(\frac{17}{23}\cos\alpha\right) - \alpha\}^\circ \Rightarrow \mu = -8.31^\circ \text{ which is not possible hence } \mu = 0^\circ$$

➤ $\alpha = 150^\circ$

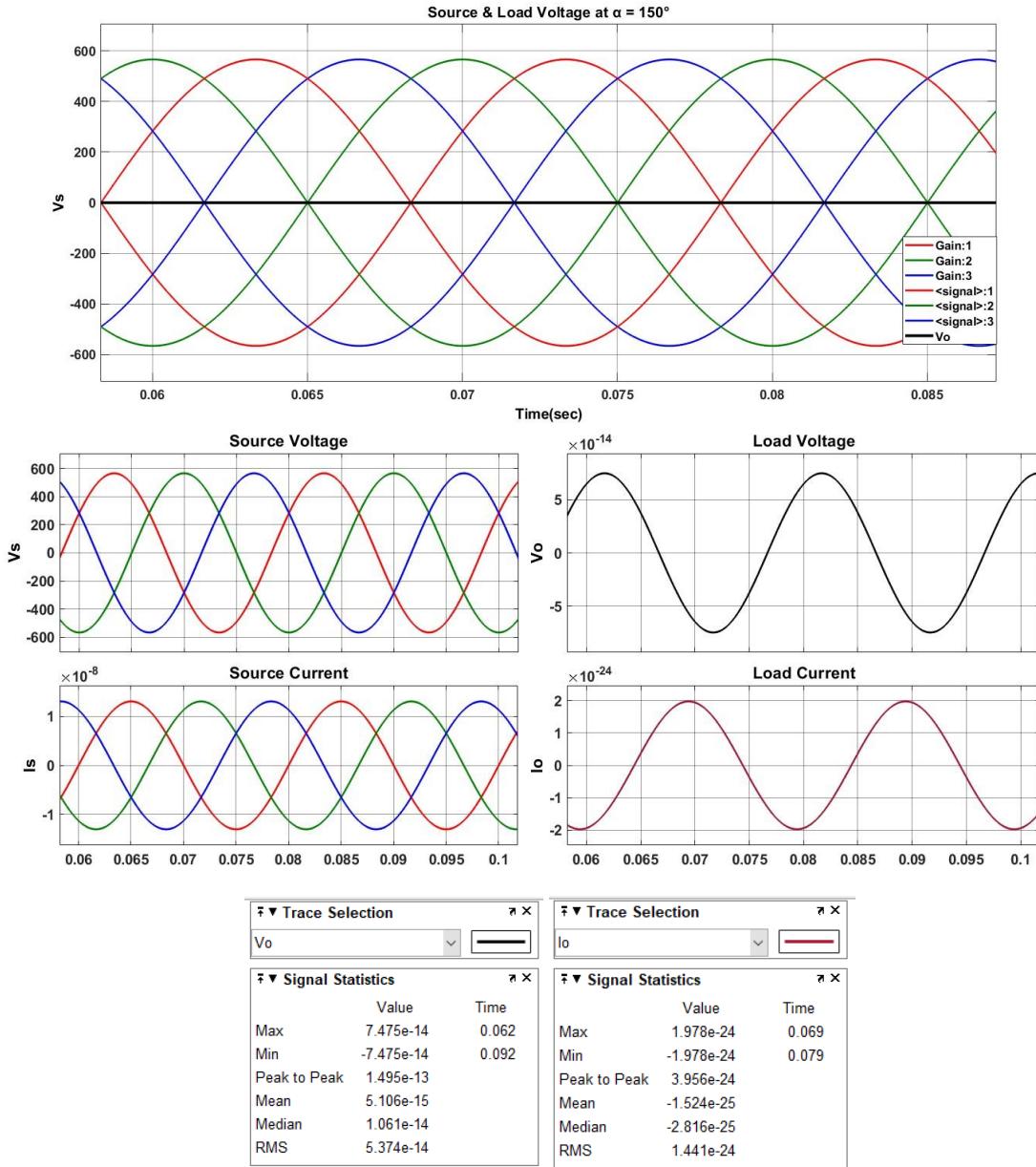
➤ SIMULATED CIRCUIT:



➤ SWITCHING PULSE:



➤ WAVEFORMS OBTAINED:



➤ Obtained Result:

Using equation (5) & given parameter as

source inductance $L_s = 10 \text{ mH}$; $\omega = 100\pi$, $V_{max} = 400\sqrt{2}$, & $R = 20 \Omega$, $\alpha = 150^\circ$, $I_o = -1.524 \times 10^{-25} \approx 0 \text{ A}$ we get,

$$\cos(\alpha + \mu) = \{\cos \alpha\} \Rightarrow \mu = 0^\circ$$

➤ Theoretical value:

By using equation (8) with the value of $\alpha = 150^\circ$ we get,

$$\mu = \{\cos^{-1} \left(\frac{17}{23} \cos \alpha \right) - \alpha\}^\circ \Rightarrow \mu = -20.2^\circ \text{ which is not possible hence } \mu = 0^\circ$$

Experiment - 06

To study the operation of single phase Uncontrolled & Controlled Rectifiers using MATLAB/SIMULINK

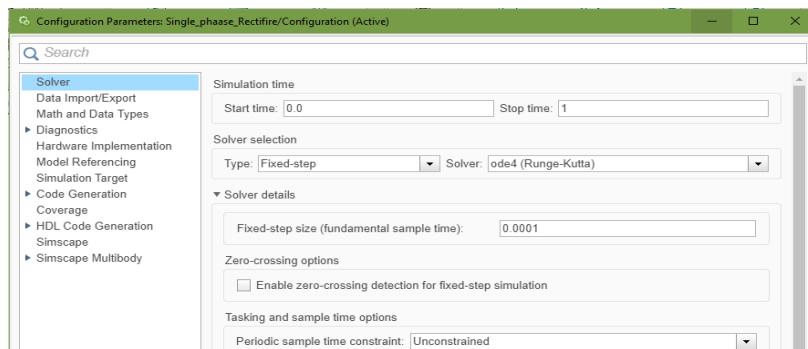
NAME: SHIVRAJ VISHWAKARMA

ROLL. NO. 224102112

1. DESIGN PARAMETERS

Parameter	Value
Input voltage	230 V, 50 Hz
Source Inductance	10 mH
Load Parameters	R Load Case (1) 20Ω
	RL Load Case (2) 20Ω in series with 20 mH Inductance
	RL Load Case (3) 20Ω in series with 200 mH Inductance

2. SIMULATION CONFIGURATION



1. Full-Bridge Uncontrolled Rectifier without Ls

3. CIRCUIT DIAGRAM

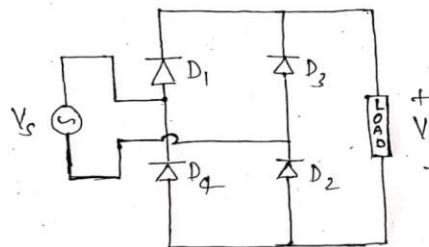
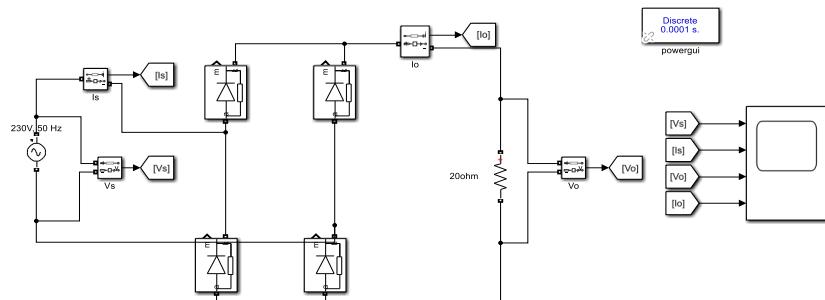


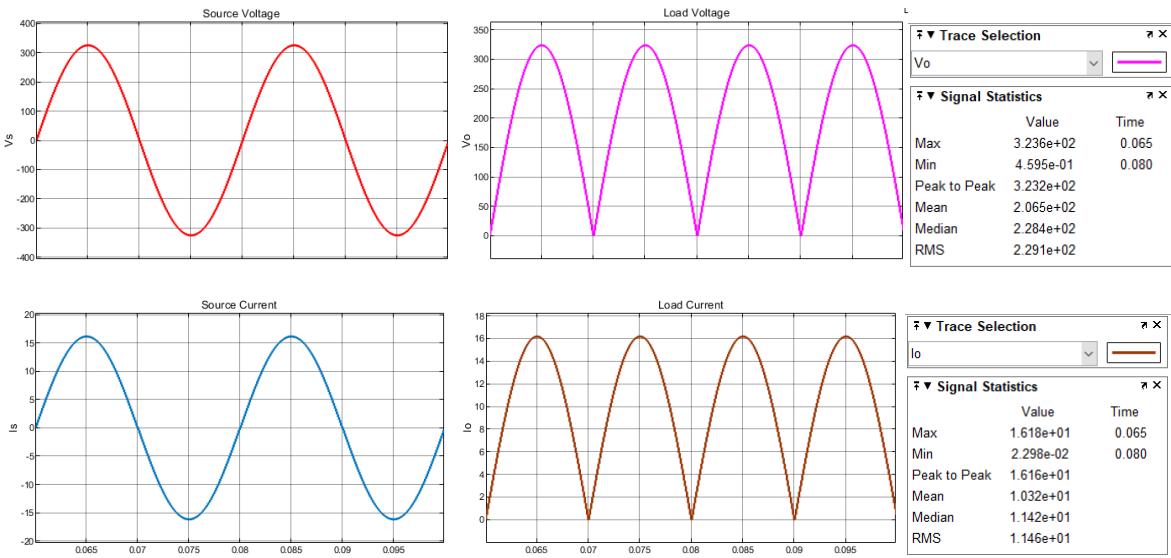
Fig 1: Circuit diagram of Uncontrolled full wave Rectifier

CASE-1, Load $R = 20\Omega$,

SIMULATED CIRCUIT:

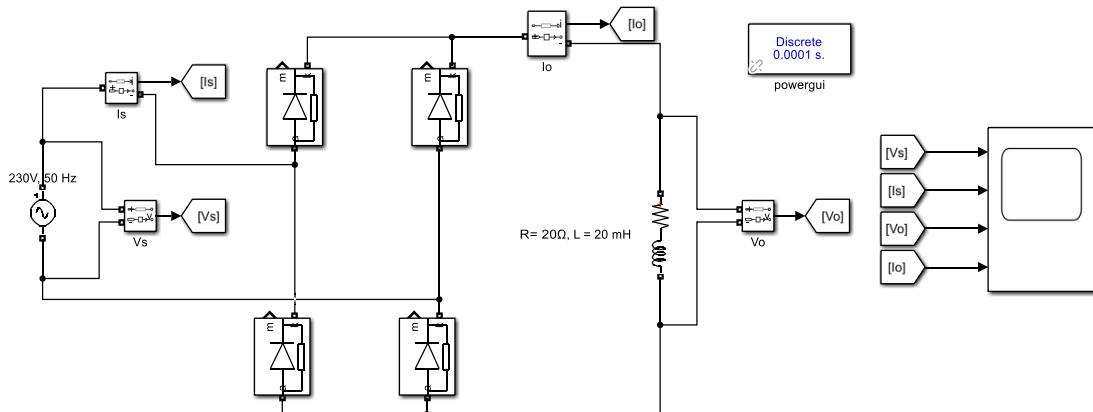


➤ WAVEFORMS OBTAINED:

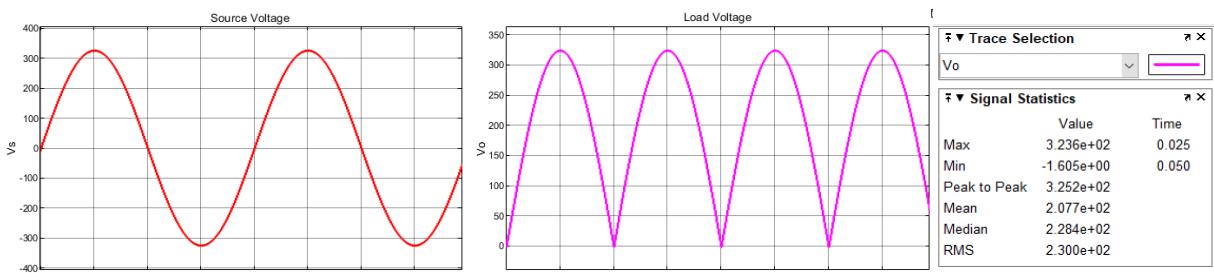


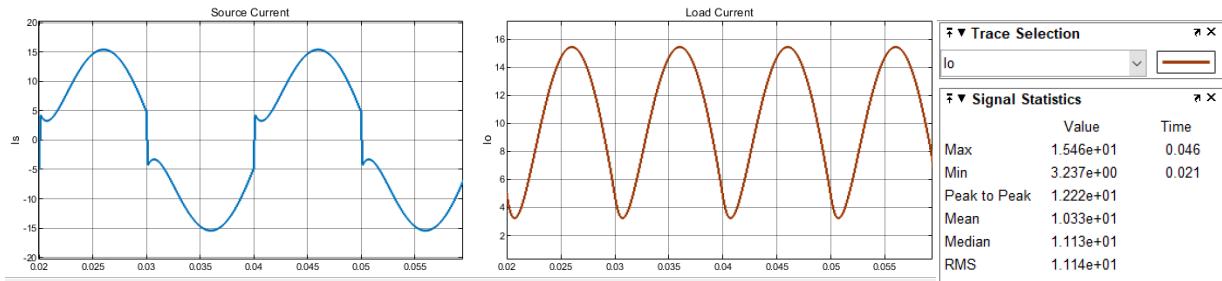
CASE-2, Load $R = 20\Omega$, $L = 20 \text{ mH}$

SIMULATED CIRCUIT:



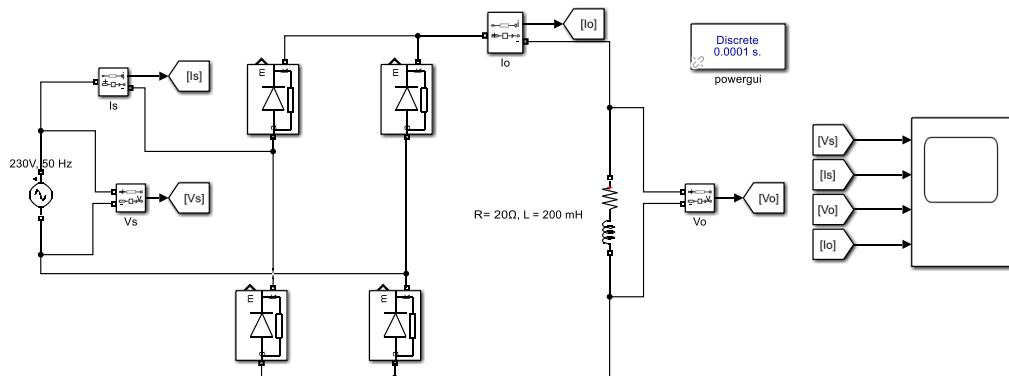
➤ WAVEFORMS OBTAINED:



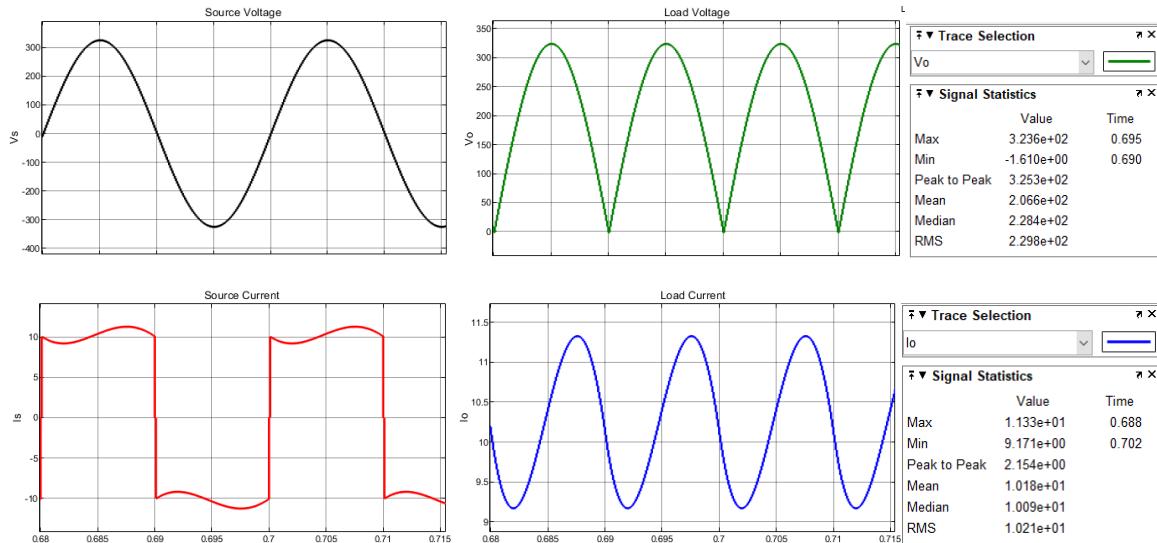


CASE-3, Load $R = 20\Omega$, $L = 200 \text{ mH}$

SIMULATED CIRCUIT:



➤ WAVEFORMS OBTAINED:



➤ OBSERVATION:

LOAD	Peak – Peak Ripple in Io
$R = 20 \Omega$ Case 1	16.16 A
$R = 20 \Omega$, $L = 20 \text{ mH}$ Case 2	12.22 A
$R = 20 \Omega$, $L = 200 \text{ mH}$ Case 3	2.154 A

2. Full-Bridge Uncontrolled Rectifier with $L_s = 10 \text{ mH}$

4. CIRCUIT DIAGRAM

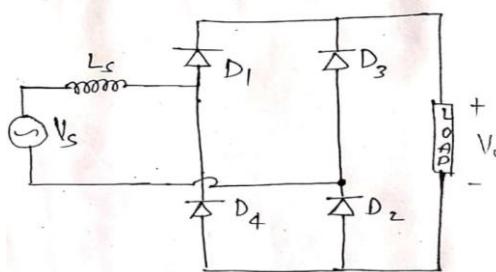
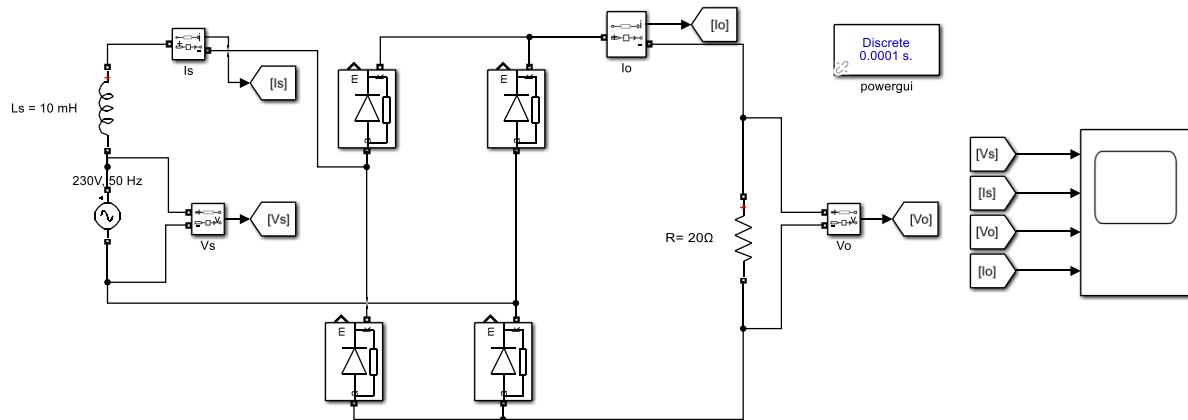


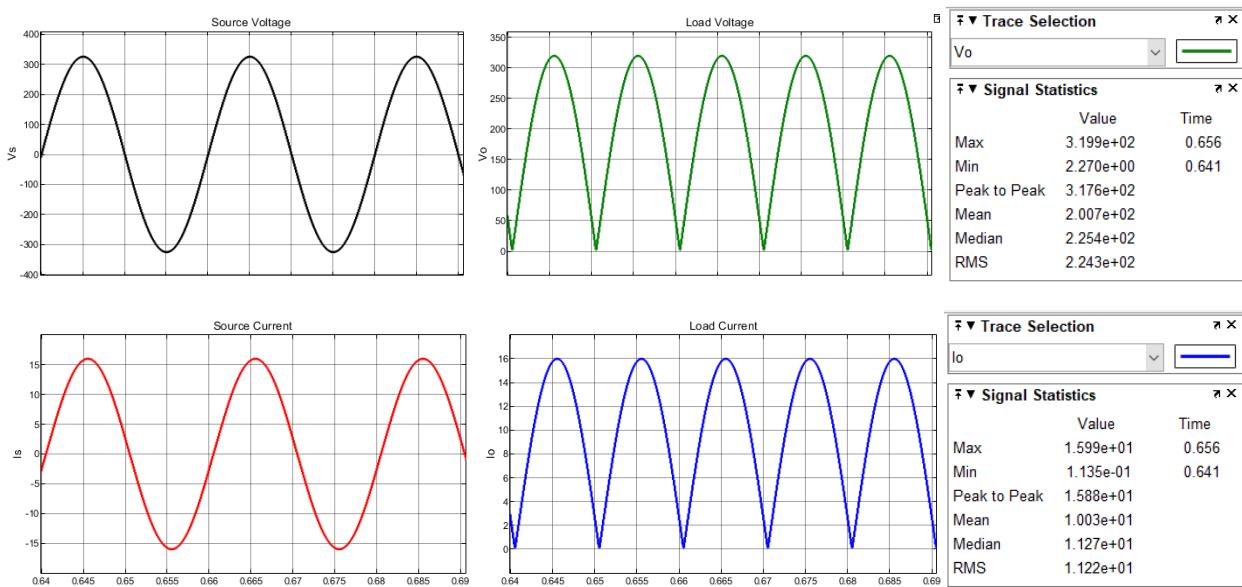
Fig 2: Circuit diagram of Uncontrolled full wave Rectifier with source inductance

CASE-1, Load $R = 20\Omega$,

➤ SIMULATED CIRCUIT:

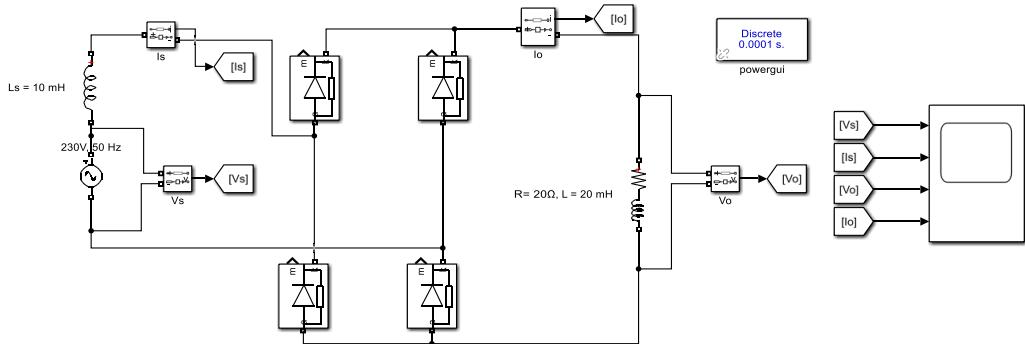


➤ WAVEFORMS OBTAINED:

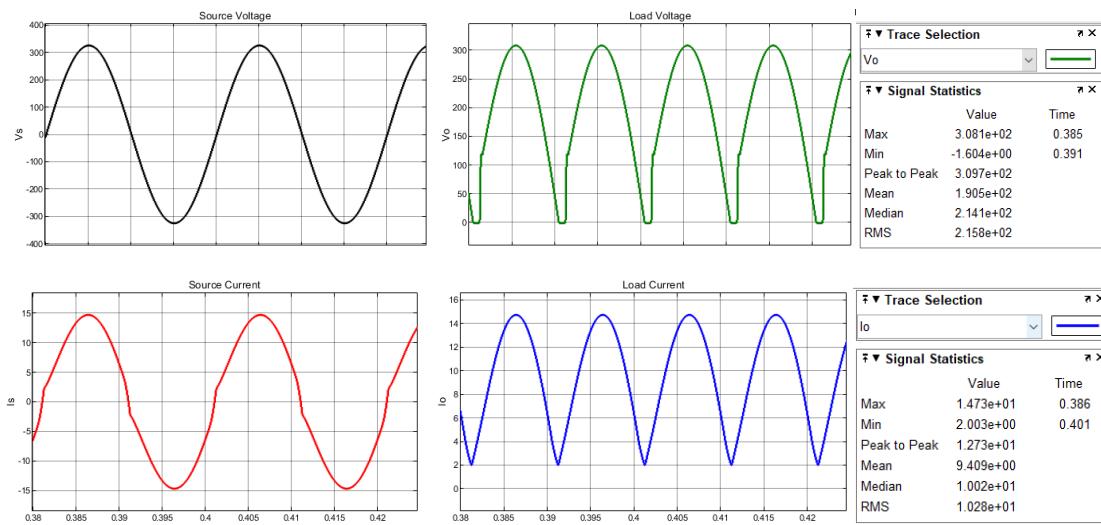


CASE-2, Load $R = 20\Omega$, $L = 20 \text{ mH}$

➤ SIMULATED CIRCUIT:

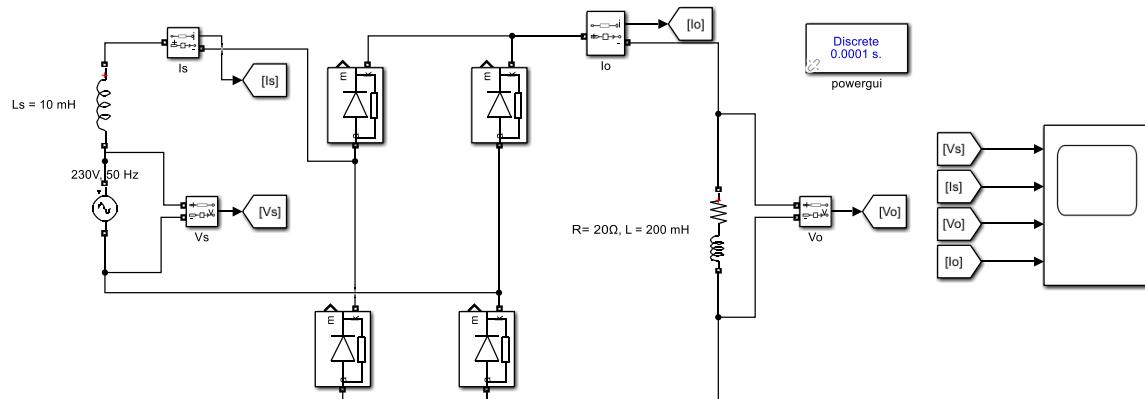


➤ WAVEFORMS OBTAINED:

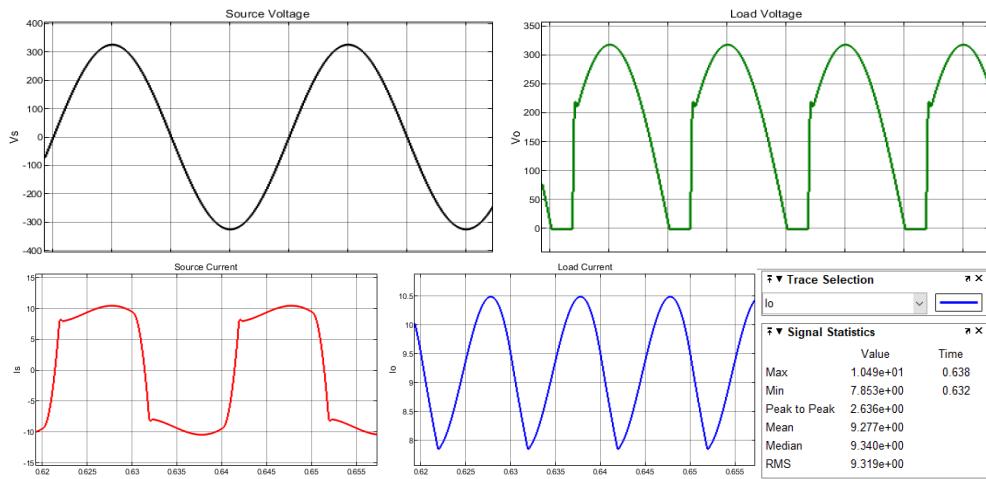


CASE-3, Load $R = 20\Omega$, $L = 200 \text{ mH}$

➤ SIMULATED CIRCUIT:



➤ WAVEFORMS OBTAINED:



➤ Calculation of Commutation Angle:

We can use the equation (5) derived in section 4 to find the value of commutation angle μ by taking $\alpha = 0$

$$\Rightarrow \cos(\alpha + \mu) = \cos\alpha - \frac{2\omega L_s}{V_{max}} I_o$$

$$\Rightarrow \mu = \cos^{-1}\left\{1 - \frac{2\omega L_s}{V_{max}} I_o\right\}$$

Where I_o is average load current; $L_s = 10 \text{ mH}$ is source inductance; $\omega = 100\pi$ is source voltage frequency, $V_{max} = 230\sqrt{2}$,

➤ Obtained Result: using $I_{o_{avg}} = 9.277 \text{ A}$, $\mu = \cos^{-1}(0.8207) \Rightarrow \mu = 34.835^\circ$

➤ Theoretical value:

By equation (7), taking $\alpha = 0$ for Diode Rectifier we get,

$$\mu = 2 \tan^{-1}\left\{\sqrt{\frac{2\omega L_s}{\pi R}}\right\} \Rightarrow \mu = 2 \tan^{-1}\left(\sqrt{\frac{1}{10}}\right) \Rightarrow \mu = 35.097^\circ$$

3. Full-Bridge Controlled Rectifier without L_s

➤ CIRCUIT DIAGRAM:

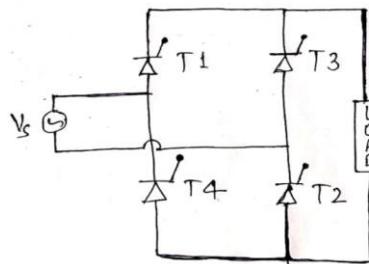


Fig 4: Circuit diagram of Controlled full wave Rectifier

➤ FIRING CIRCUIT DESIGN:

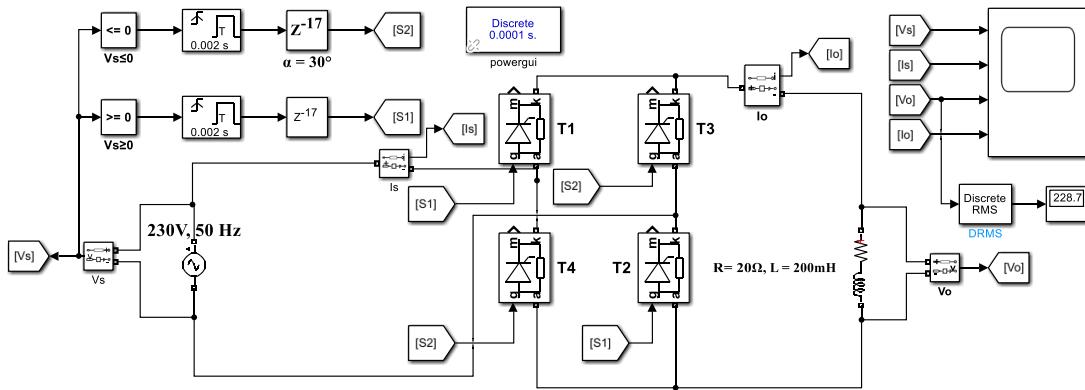
$$\text{Delay corresponding to } \alpha = \frac{\alpha T_s}{360^\circ * t_{sample}}$$

For $\alpha = 30^\circ$, delay = $16.66 \approx 17$

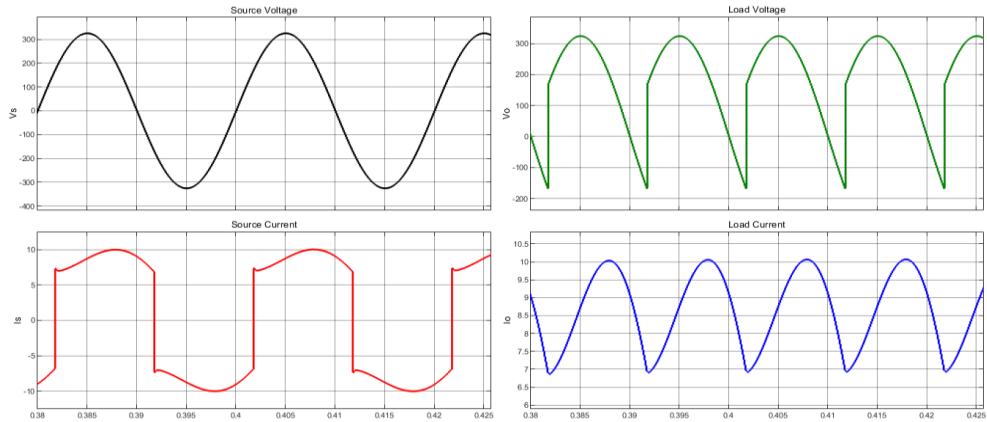
For $\alpha = 90^\circ$, delay = 50

1. At $\alpha = 30^\circ$,

➤ SIMULATED CIRCUIT:

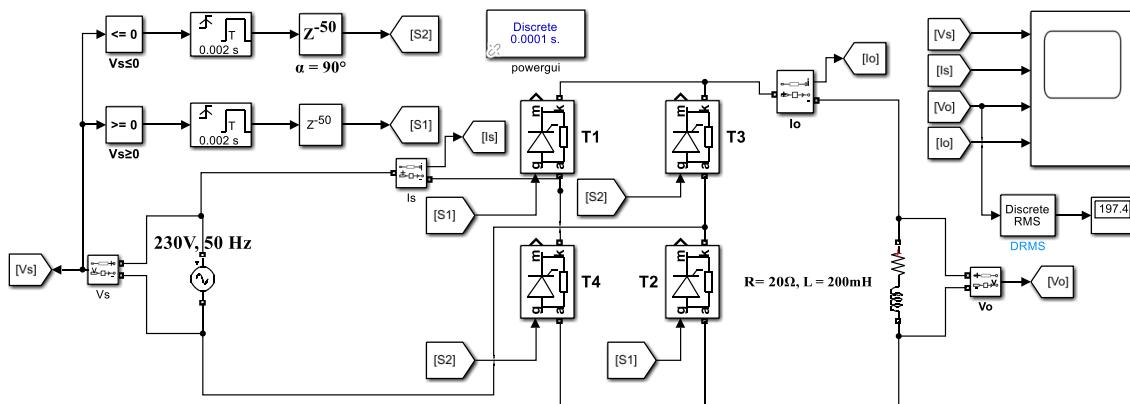


➤ WAVEFORMS OBTAINED:

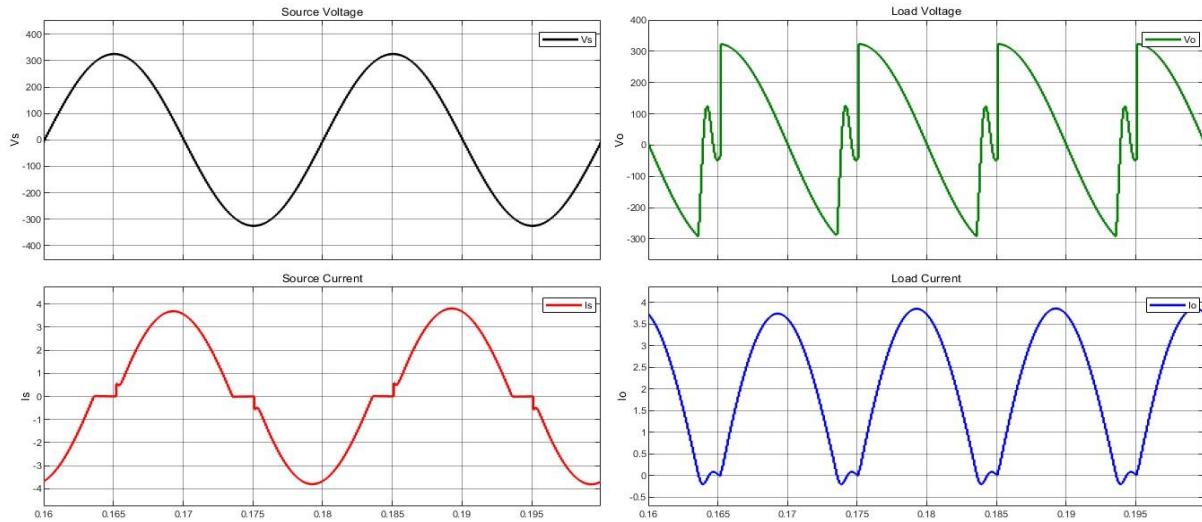


2. At $\alpha = 90^\circ$,

➤ SIMULATED CIRCUIT:



➤ **WAVEFORMS OBTAINED:**



➤ **OBSERVATION:**

With the change in α from 30° to 90° waveform of V_o and I_o both gets change as –

- For $\alpha = 30^\circ$, wave form of V_o have a large positive area and a very small negative area, but at $\alpha = 90^\circ$ the positive area(charging) gets reduced and negative area(discharging) gets increased.
- For $\alpha = 30^\circ$, wave form of I_o is continuous & smooth but at $\alpha = 90^\circ$ waveform also crosses zero and becomes negative for a very small duration.

Overall average value of both V_o and I_o gets reduced at $\alpha = 90^\circ$.

4. Full-Bridge Controlled Rectifier with $L_s = 10 \text{ mH}$

➤ **CIRCUIT DIAGRAM:**

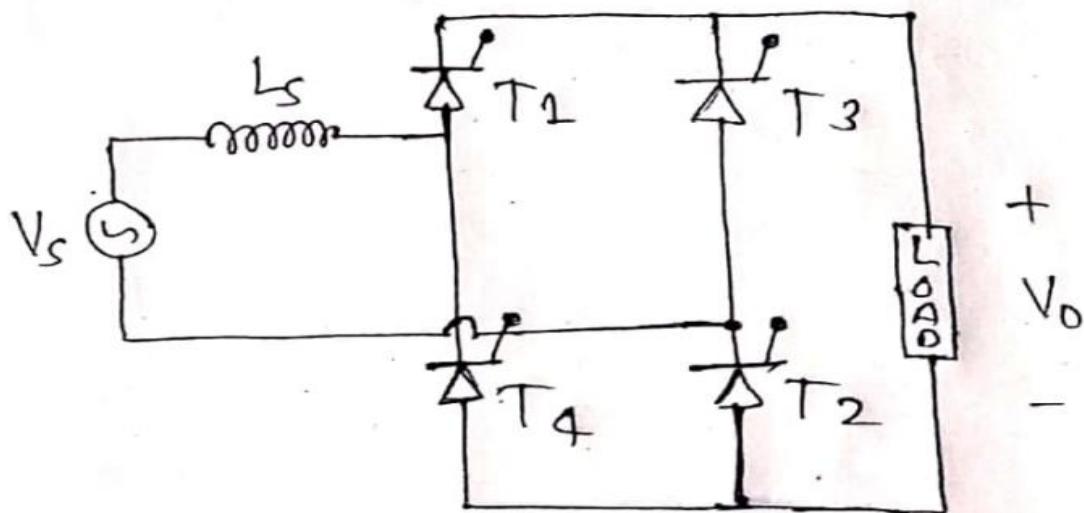
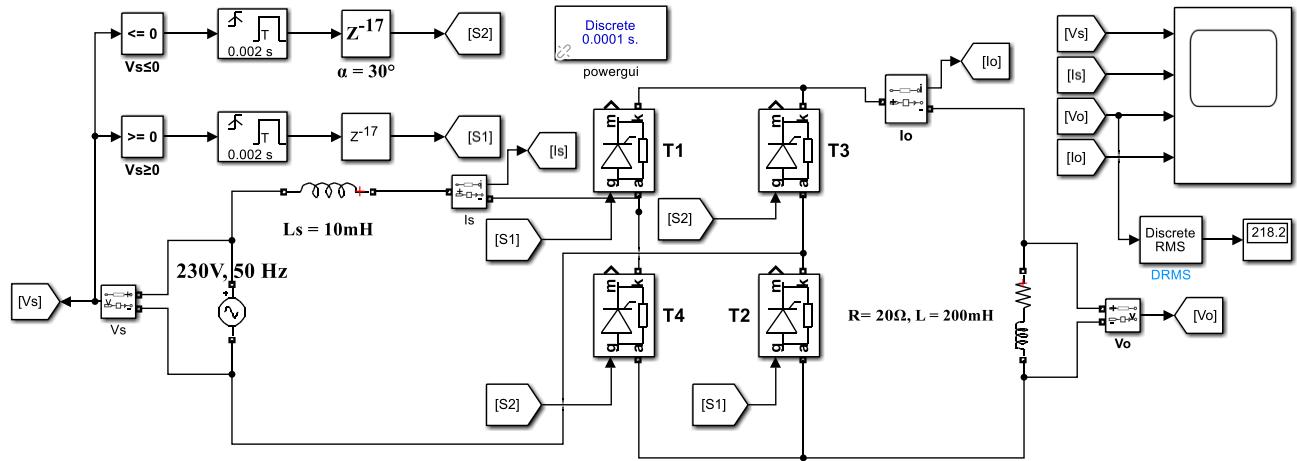


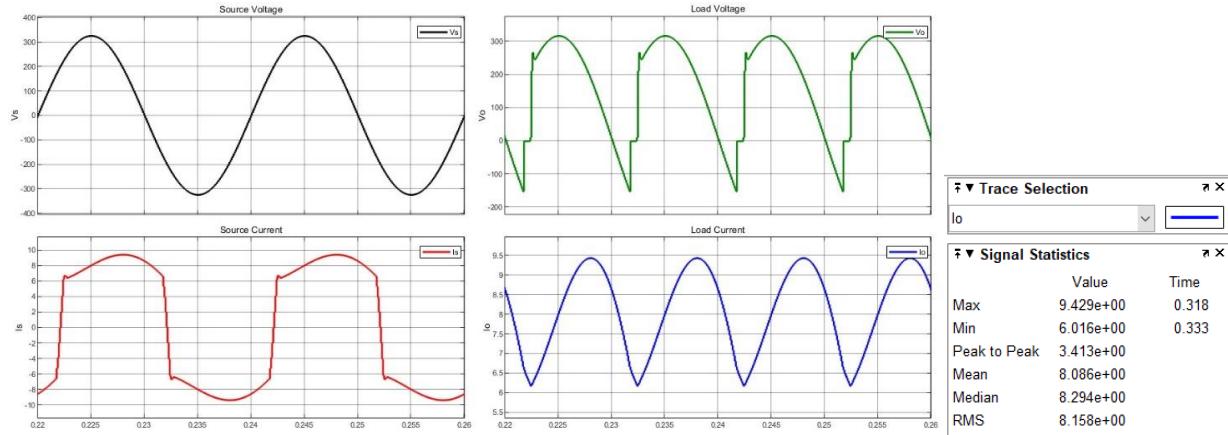
Fig 2: Circuit diagram of Uncontrolled full wave Rectifier with source inductance

1. At $\alpha = 30^\circ$,

➤ SIMULATED CIRCUIT:



WAVEFORMS OBTAINED:



➤ Calculation of Commutation Angle:-

Until now, the current commutation between thyristors has been considered to be instantaneous. This condition is not valid in real cases due to the presence of the source/Line inductance L . During the commutation, the current through the thyristors cannot change instantaneously, and for this reason, during the commutation angle μ , all four thyristors are conducting simultaneously.

Therefore, during the commutation, the following relationship for the load voltage holds

$$v_o = 0 \quad \forall \quad \alpha \leq \omega t \leq \alpha + \mu \quad (1)$$

The effect of the commutation on the supply current, the voltage waveforms, and the thyristor current waveforms can be observed in below Fig.

During the commutation, the following expression holds

$$L \frac{di_s}{dt} = v_s = V_{max} \sin(\omega t) \quad \alpha \leq \omega t \leq \alpha + \mu \quad (2)$$

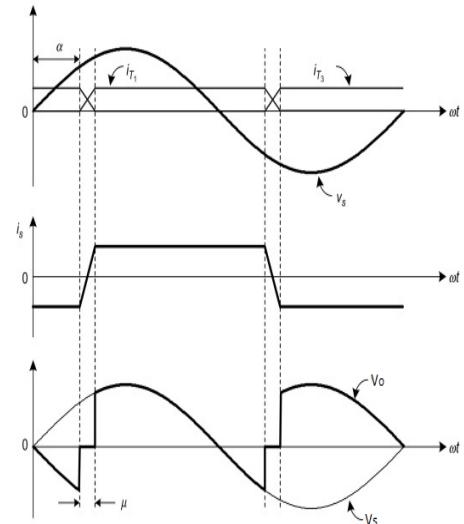
Integrating above Eq. over the commutation interval yields

$$\int_{-I_o}^{I_o} di_s = \frac{V_{max}}{L} \int_{\alpha/\omega}^{\alpha+\mu/\omega} \sin(\omega t) dt \quad (3)$$

$$I_{o_{avg}} = \frac{V_{max}}{2\omega L_s} \{ \cos \alpha - \cos(\alpha + \mu) \} \quad (4)$$

From above Eq., the following relationship for the commutation angle μ is obtained:

$$\cos(\alpha + \mu) = \cos \alpha - \frac{2\omega L_s}{V_{max}} I_o \quad (5)$$



the expression for the average load voltage is given by

$$V_{o_{avg}} = \frac{1}{\pi} \int_{\alpha+\mu}^{\pi+\alpha} V_m \sin(\omega t) d(\omega t) = \frac{V_{max}}{\pi} [\cos(\alpha + \mu) + \cos \alpha] \quad (6)$$

Since $I_{o_{avg}} = \frac{V_{o_{avg}}}{R}$ therefore using equation (4) and (6) we get

$$\frac{\cos \alpha - \cos(\alpha + \mu)}{\cos \alpha + \cos(\alpha + \mu)} = \frac{2\omega L_s}{\pi R} \quad (7)$$

➤ Obtained Result:

Using equation (5) & given parameter as source inductance $L_s = 10 \text{ mH}$; $\omega = 100\pi$, $V_{max} = 230\sqrt{2}$, & $R = 20 \Omega$, $\alpha = 30^\circ$, $I_o = 8.086 \text{ A}$ we get,

$$\cos(30^\circ + \mu) = \left\{ \frac{\sqrt{3}}{2} - \frac{8.086\pi\sqrt{2}}{230} \right\} \Rightarrow \mu = \cos^{-1}(0.709829) - 30^\circ \Rightarrow \mu = 14.78^\circ$$

➤ Theoretical value:

By using equation (7) with all the values we get,

$$\mu = \cos^{-1} \left(\frac{9\sqrt{3}}{22} \right) - 30^\circ \Rightarrow \mu = 14.88^\circ$$

EE561: Power Electronics Laboratory

Experiment - 05

Output Voltage Regulation of Buck Converter using Type-II Compensator

NAME: SHIVRAJ VISHWAKARMA

ROLL. NO. 224102112

➤ **Objective:** The objective of this experiment is to design a Type-II compensator for a buck converter to regulate its output voltage.

➤ **Parameters:**

Parameter	Value
Input voltage	24 V
Inductor	50 μ H
Capacitor	100 μ F
Load Resistance	2 Ω
Switching frequency	100 kHz
Desired Gain cross-over frequency of compensated system	100 Hz
Desired Phase Margin	120°

➤ **Procedure:**

- Simulate the buck converter in open-loop
- Find out the output voltage to duty ratio transfer function of the converter and find the dc gain and location of poles, zeros.
- Draw the bode plot of open loop system using MATLAB and note down the find the dc gain, gain cross-over frequency, phase cross-over frequency, gain-margin and phase margin.
- Design a Type-II compensator using K-factor method and write the transfer function of compensator. Draw the bode plot of compensator, and Loop-transfer function. note down the values & find the dc gain, gain cross-over frequency, phase cross-over frequency, gain-margin and phase-margin of the compensated system.
- Simulate the converter with the Type-II compensator and verify the working with step change in reference voltage from 10 V to 15 V.
- Take the snapshots of simulation results and prepare the report

➤ **Uncompensated Buck Converter (Open Loop):**

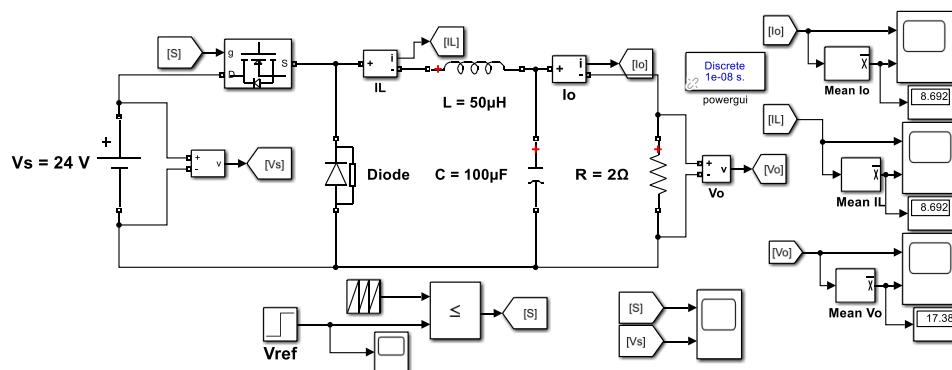


Fig. 1: Open loop Buck Converter simulation circuit

➤ WAVEFORMS:

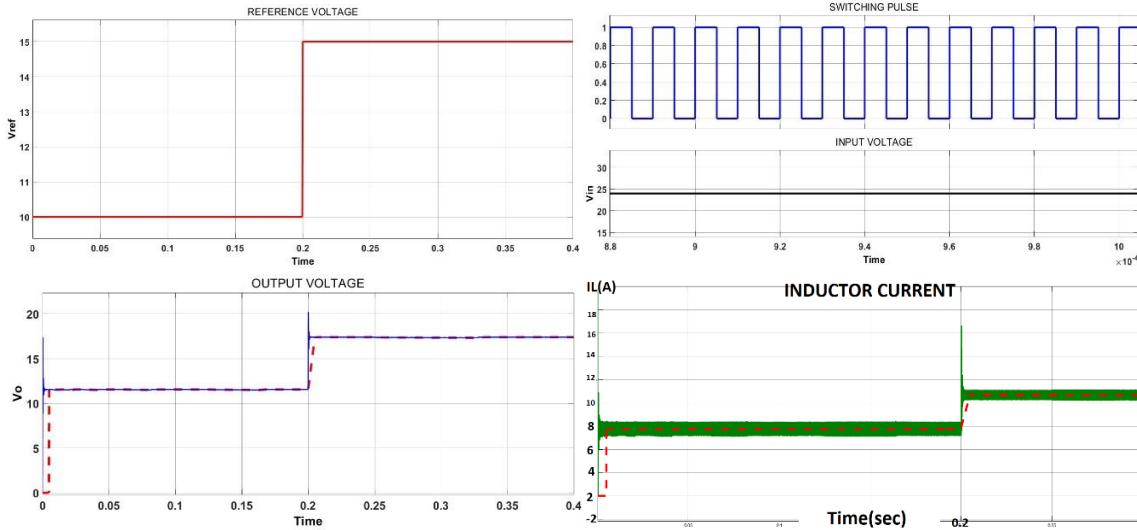


Fig. 2: Waveforms of V_o , I_L , Input & reference Voltage and switching pulse

➤ Uncompensated Buck Converter Design:

❖ Derivation of Transfer function using small signal analysis:

The transfer function is derived using small signal analysis comes as:

$$G_{vd}(s) = \frac{V_o(s)}{D(s)} = \frac{V_{in}}{s^2 LC + \frac{sL}{R} + 1} \Rightarrow G_{vd}(s) = \frac{V_{in}}{\left(\frac{s}{\omega_0}\right)^2 + \frac{s}{Q\omega_0} + 1} \quad (1)$$

$$\text{Were, } \omega = \frac{1}{\sqrt{LC}}; Q = R \sqrt{\frac{L}{C}}$$

By substituting the values of V_{in} , f_0 , W_o and Q the in equation (1) we get the open loop transfer function of Buck Converter as ($\omega_0 = 14142.13\text{rad/sec}$, $f_o = 2251.93$, $Q = 2.828$, $Q(\text{dB}) = 9.029$)

$$G_P = G_{vd}(s) = \frac{24}{5 \times 10^{-9}s^2 + 2.5 \times 10^{-5}s + 1} \quad (2)$$

➤ MATLAB CODE:

```

1) v = 24;
2) L = 50e-6;
3) C = 100e-6;
4) R = 2;
5) F = 100e3;
6) num = [v];
7) den = [L*C L/R 1];
8) y= tf (num, den);
9) bode(y)

```

➤ BODE PLOT: –

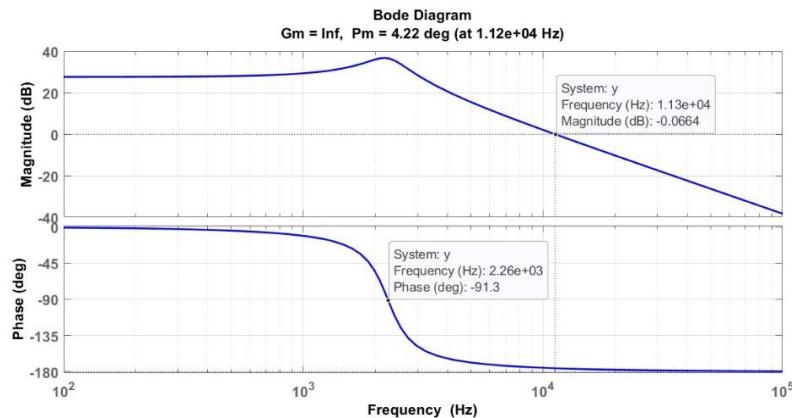


Fig. 3: Bode Plot of Uncompensated Buck Converter

- ✓ For the uncompensated buck converter, the DC gain K with the help of Bode plot is **27.6 DB** & the value of phase margin (PM) at ω_{gc} is **4.22°** & GM is ∞ , cross-over frequency is 1.12×10^4 Hz.

➤ Design of TYPE- II Compensator:

- Desired the gain cross-over frequency 100 Hz.
- Phase Margin (P.M.) $\{\phi_m\}$ is given 120° & the phase Angle of uncompensated Buck converter (G_p) at ω_{gc} is 0.907° by the help of this we can easily find the phase boost for that given system $\phi_b = \phi_m - \phi_p - 90^\circ \Rightarrow \phi_b = 120^\circ + 0.907^\circ - 90^\circ \Rightarrow \phi_b = 30.907^\circ$
- Now the value of K will be calculated using the formula for type II compensator as

$$K = \tan\left\{45^\circ + \frac{\phi_b}{2}\right\} \Rightarrow K = 1.76415 \quad (3)$$

- Transfer Function of TYPE -II compensator is given by

$$a(s) = \frac{G_{MB}\left(1 + \frac{\omega_z}{s}\right)}{\left(1 + \frac{s}{\omega_p}\right)} \quad (4)$$

Where,

$$G_{MB} = \frac{1}{|G_p(j\omega_c)|} = 0.04168, \omega_z = \frac{\omega_c}{K} = 356.1594 \text{ rad/sec} \& \omega_p = K * \omega_c = 1108.45 \text{ rad/sec}$$

Now the Transfer Function of TYPE- II Compensator will be

$$a(s) = \frac{46.2s + 16454.6}{s^2 + 1108.45s} \quad (5)$$

➤ MATLAB CODE:

```

10) Gmb= (0.04168);
11) n = [1108.45 394784.88];
12) d = [1 1108.45 0];
13) a=tf(gmb*(n), d);
14) bode(a)

```

➤ BODE PLOT:

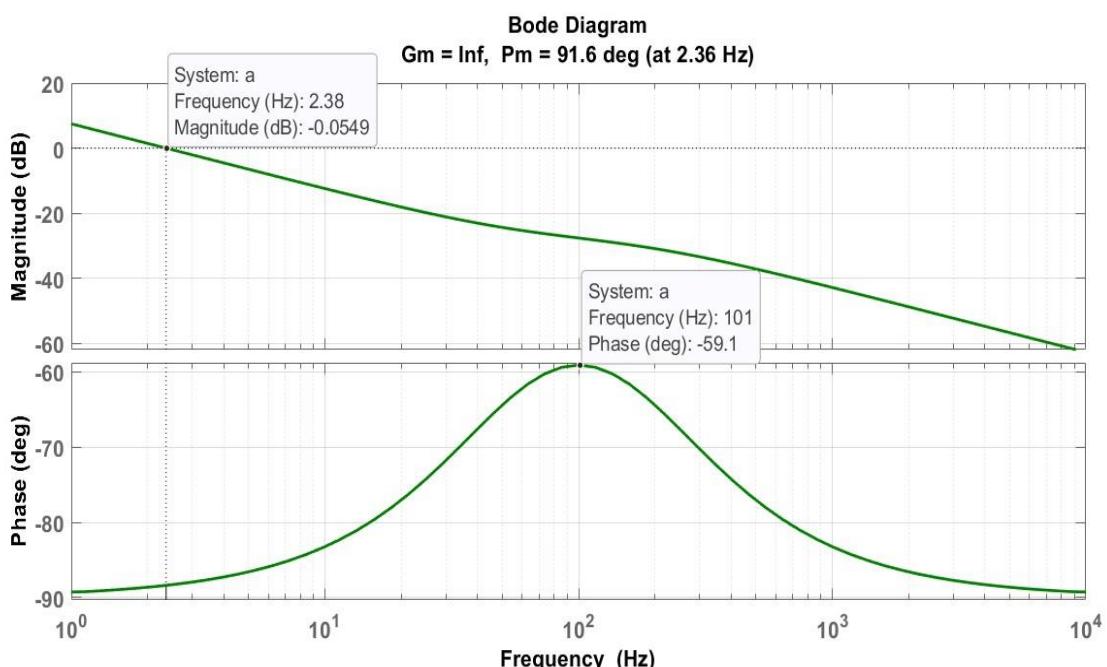


Fig. 4: Bode Plot of TYPE-II Compensator

➤ Buck Converter with TYPE-II Compensator:

$$a(s) \cdot G_{vd}(s) = \frac{24G_{MB} \left(1 + \frac{\omega_L}{s} \right)}{(5 \cdot 10^{-9} s^2 + 2.5 \cdot 10^{-5} s + 1) \left(1 + \frac{s}{\omega_p} \right)} \quad (6)$$

By substituting the values in equation, the loop transfer function is obtained as:

$$a(s) \cdot G_{vd}(s) = \frac{1108.8s + 3.9491 \cdot 10^5}{(5 \cdot 10^{-9} s^4 + 3.054 \cdot 10^{-5} s^3 + 1.028 s^2 + 1108.45 s)} \quad (7)$$

➤ MATLAB CODE:

```

15) b= tf(y*a);
16) bode(b)
17) margin(b)

```

➤ BODE PLOT:

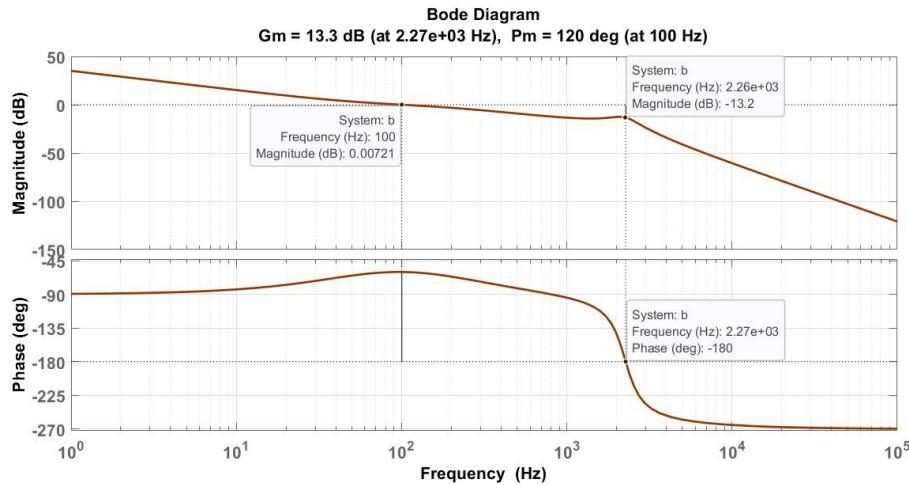


Fig. 5: Bode Plot of Compensated Buck Converter

➤ COMBINED BODE PLOT:

```

1) v = 24;
2) L = 50e-6;
3) C = 100e-6;
4) R = 2;
5) F = 100e3;
6) num = [v];
7) den = [L*C L/R 1];
8) y=tf (num, den);
9) bode(y)
10) margin(y)
11) hold on;
12) gmb= (0.04168);
13) n = [1108.45 394784.88];
14) d = [1 1108.45 0];
15) a=tf(gmb*(n), d);
16) bode(a)
17) margin(a)
18) hold on;
19) b= tf(y*a);
20) bode(b)
21) margin(b)

```

MATLAB Code

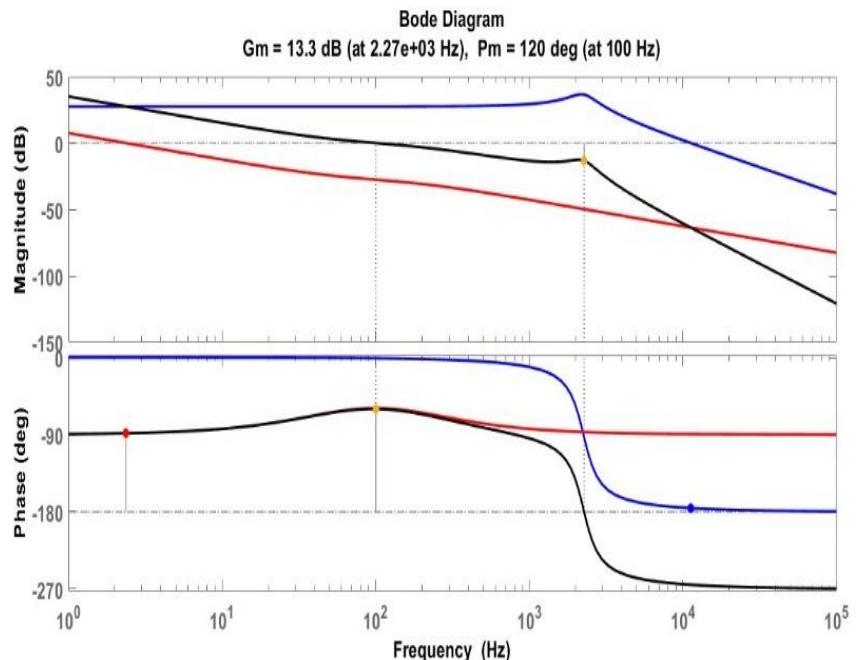


Fig. 6: Combined Bode Plot of whole system

➤ SIMULATEDCIRCUIT:

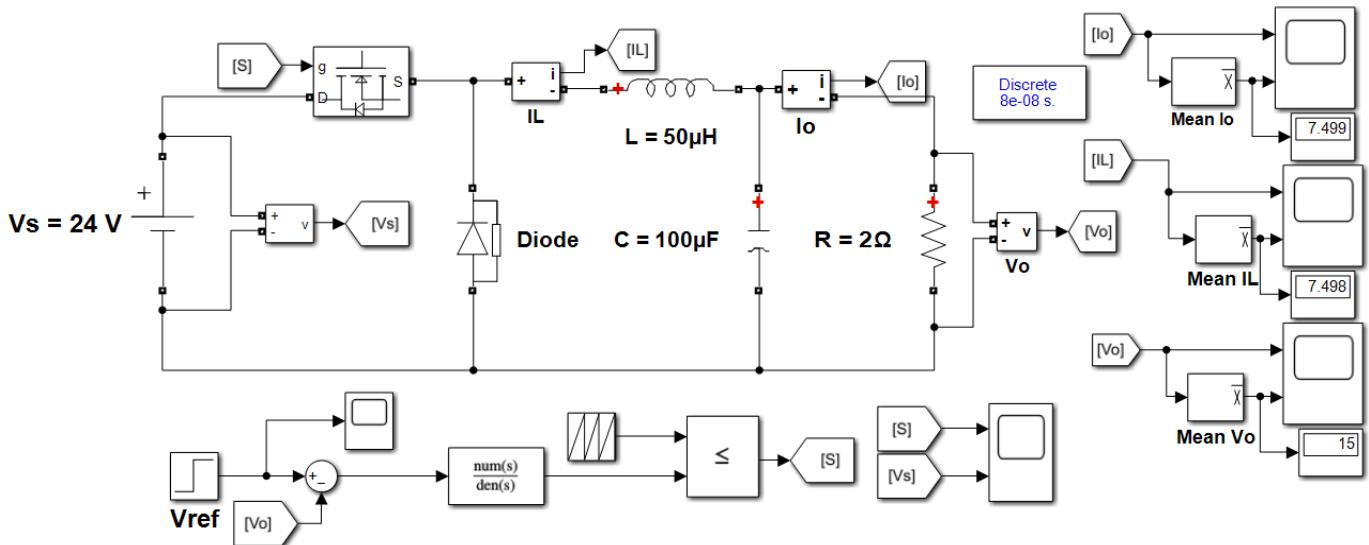


Fig. 7: Simulated Diagram of Compensated Buck Converter

➤ SIMULATION CONFIGURATION & PARAMETERS:

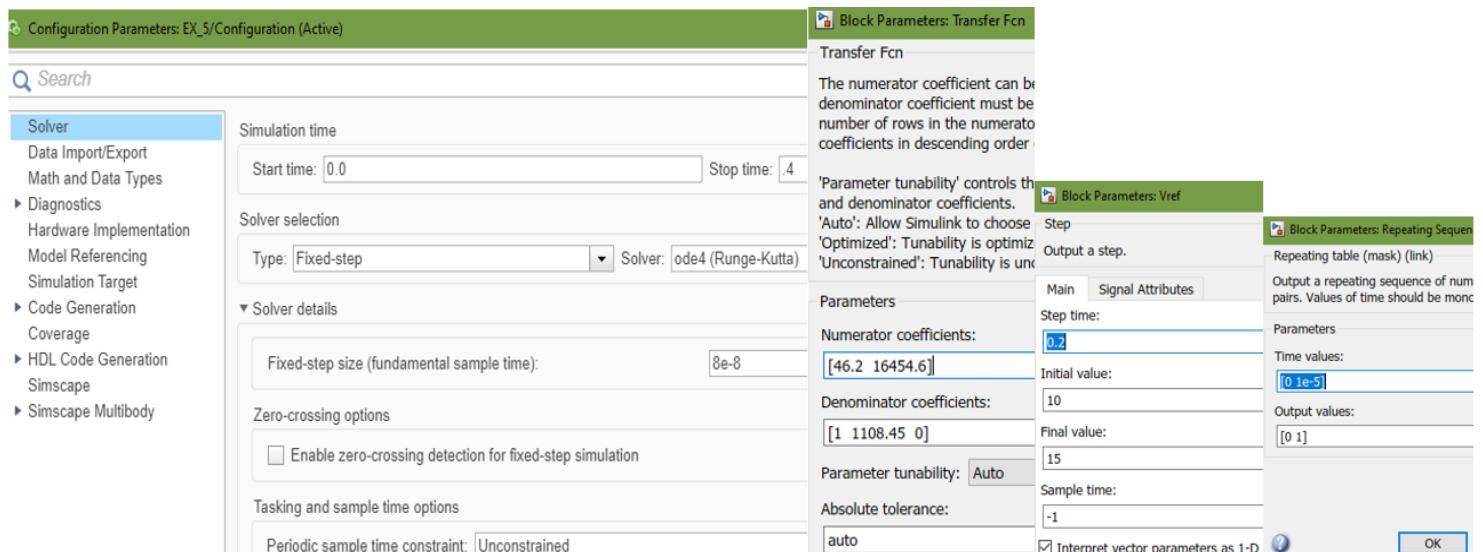


Fig. 8: Step Voltage, simulation configuration

➤ WAVEFORMS:

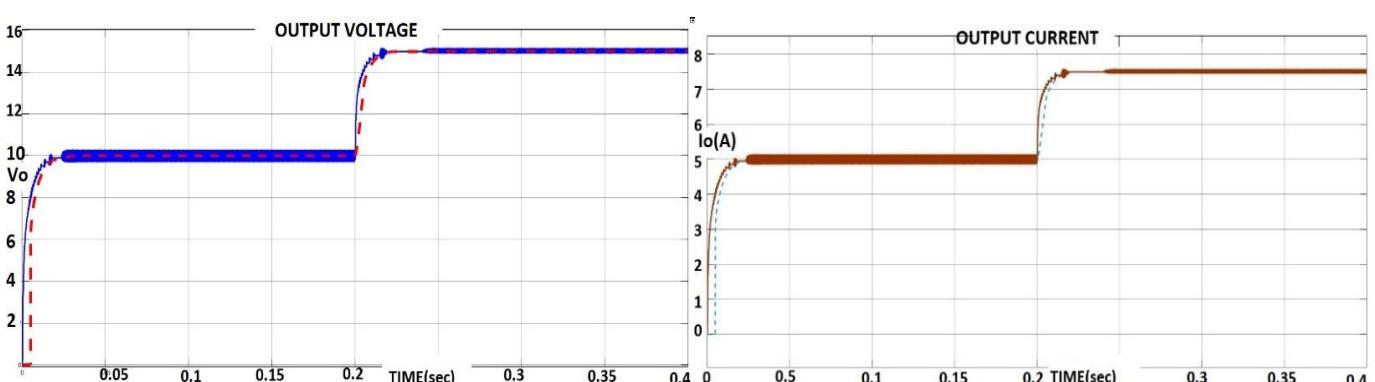


Fig. 9: Output Voltage, Output Current

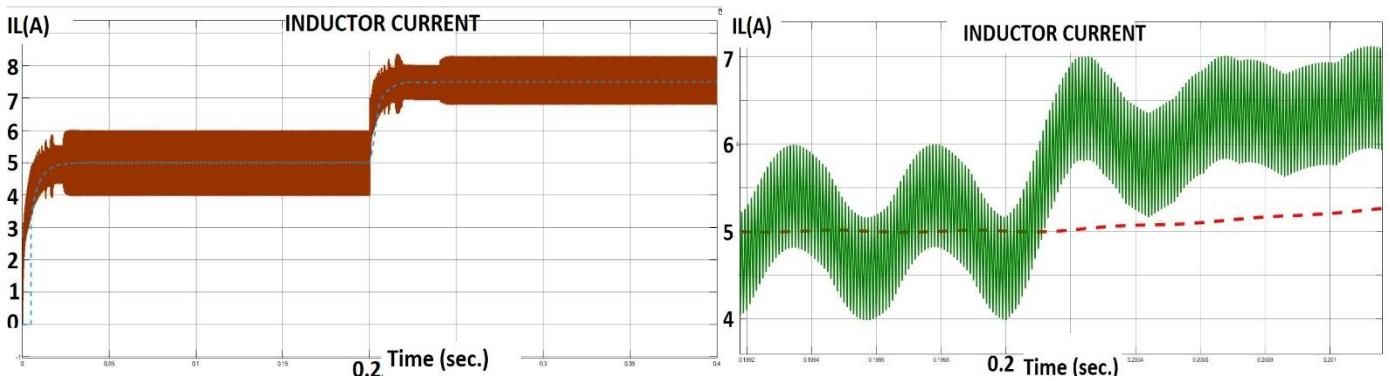


Fig. 8: Inductor Current & its Ripple

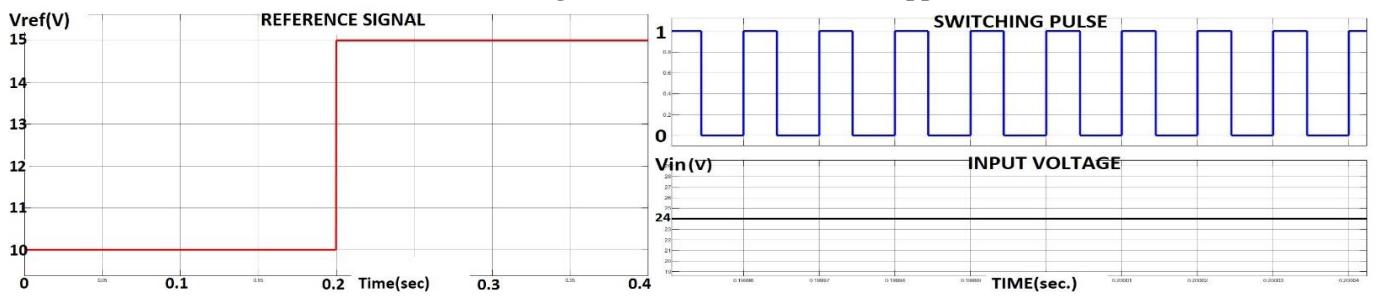


Fig. 9: Reference Voltage, Switching pulse & Input Voltage

Experiment 4

Design & Simulation of FLYBACK and SEPIC converter using MATLAB/SIMULINK

1. FLYBACK CONVERTER

1. **PARAMETERS:** Input voltage = 24 V, switching frequency = 20 kHz, Duty ratio = 1/3, Resistance = 5 Ω, Capacitance = 200 μF, Ripple inductor current 20% of the average inductor current, Turn ratio N1:N2 = 1:5

2. **CIRCUIT DIAGRAM:**

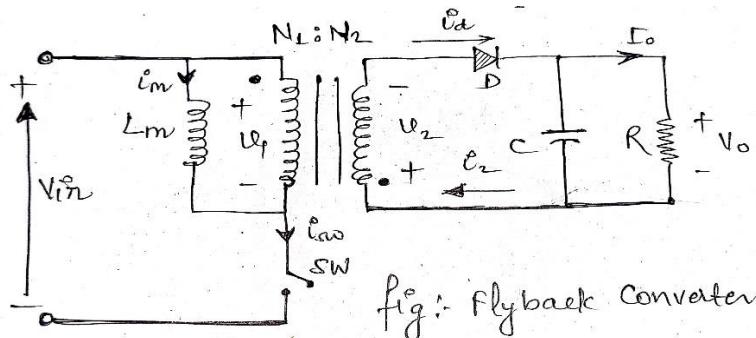


Fig:1 Circuit diagram of flyback converter

3. **THEORETICAL WAVEFORMS OF FLYBACK CONVERTER:**

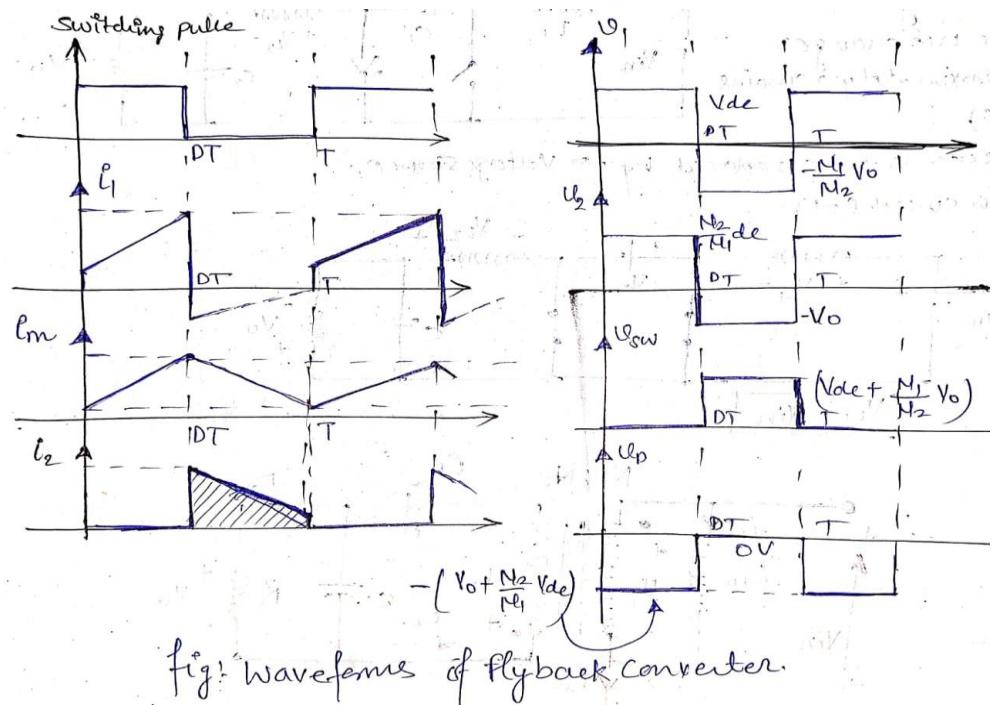


Fig 2: theoretical waveforms of flyback converter

4. **DESIGN PARAMETERS:**

- a. The value of output voltage =

$$V_0 = \frac{N_2}{N_1} V_I \frac{D}{1-D} = 60V$$

b. Magnetizing Inductance =

$$L_m = \frac{V_L D}{f \Delta I} = 22.2 \mu H$$

c. Switching time period = $T = \frac{1}{f} = 50 \mu sec$

4. SNAPSHOT OF THE SIMULATED CIRCUIT:

Considering diode $V_f = 0.8$ V, & solver configuration as-

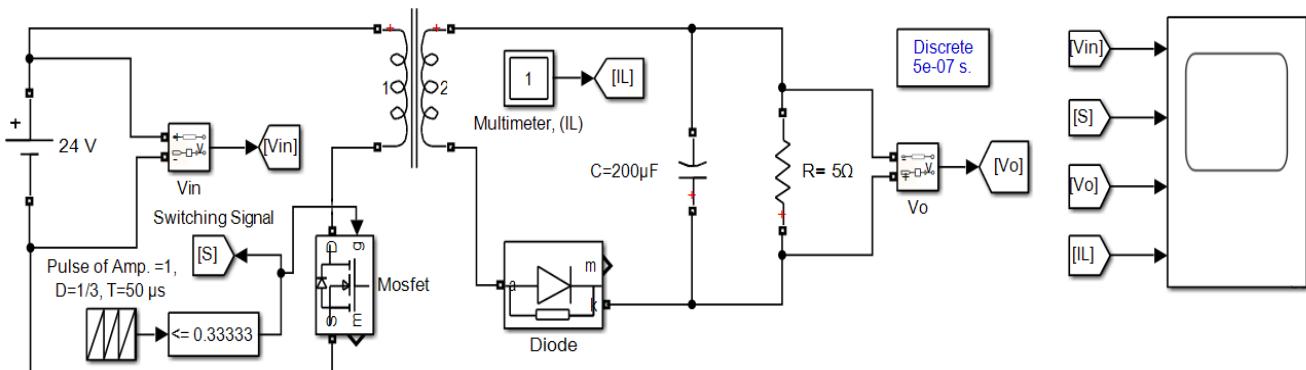
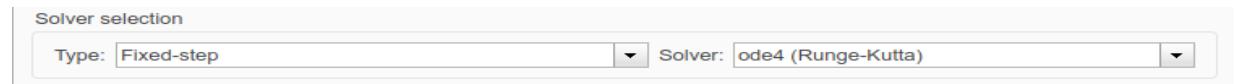
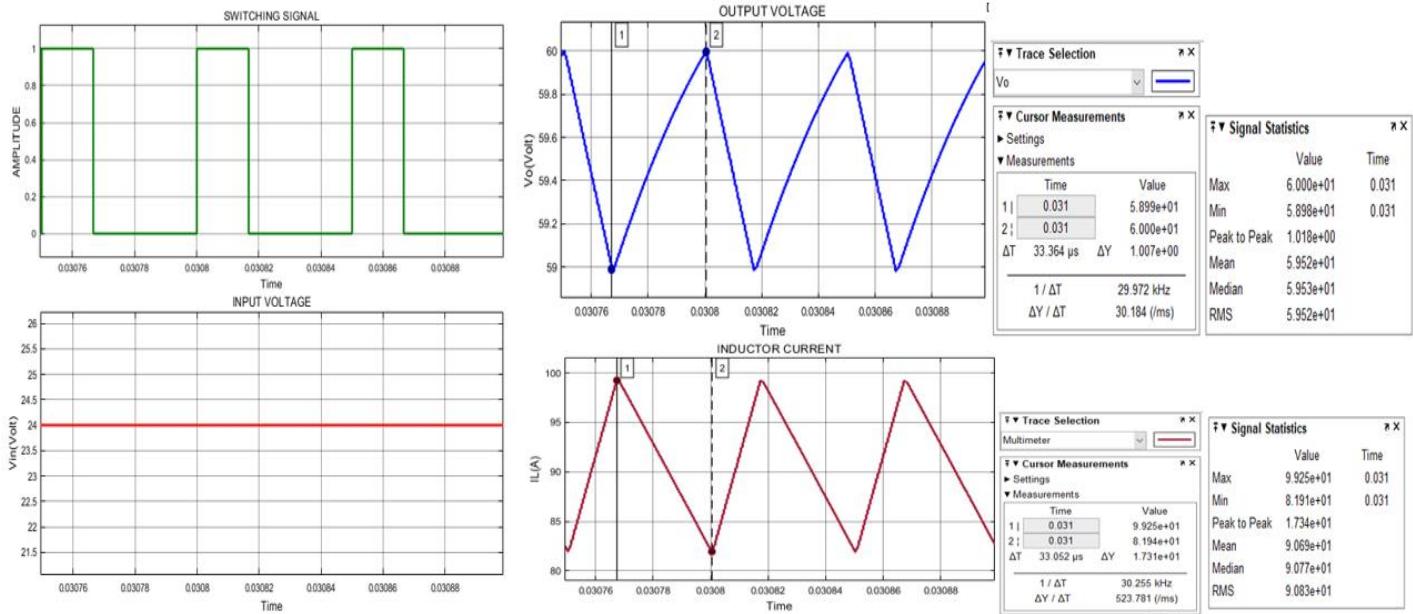


Fig 3: simulated circuit diagram

5. WAVEFORMS OBTAINED:



6. SIMULATION RESULT:

Parameter	Theoretical value	Practical value	Ripple value
Output voltage	60V	58.97V	.95V
Inductor avarage current	90A	86.41A	17.32A

2. SEPIC CONVERTER

7. **PARAMETERS:** Input voltage = 48 V, switching frequency = 50 kHz, Duty ratio = 2/3, Resistance = 10 Ω , Capacitance = 200 μF (both C1 & C2), Ripple inductor current 20% of the average value (for both L1 & L2).

8. CIRCUIT DIAGRAM:

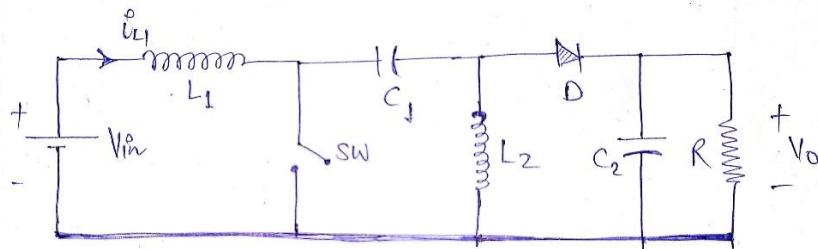


fig:- circuit diagram of SEPIC converter

Fig:1 Circuit diagram of SEPIC converter

9. THEORETICAL WAVEFORMS OF FLYBACK CONVERTER:

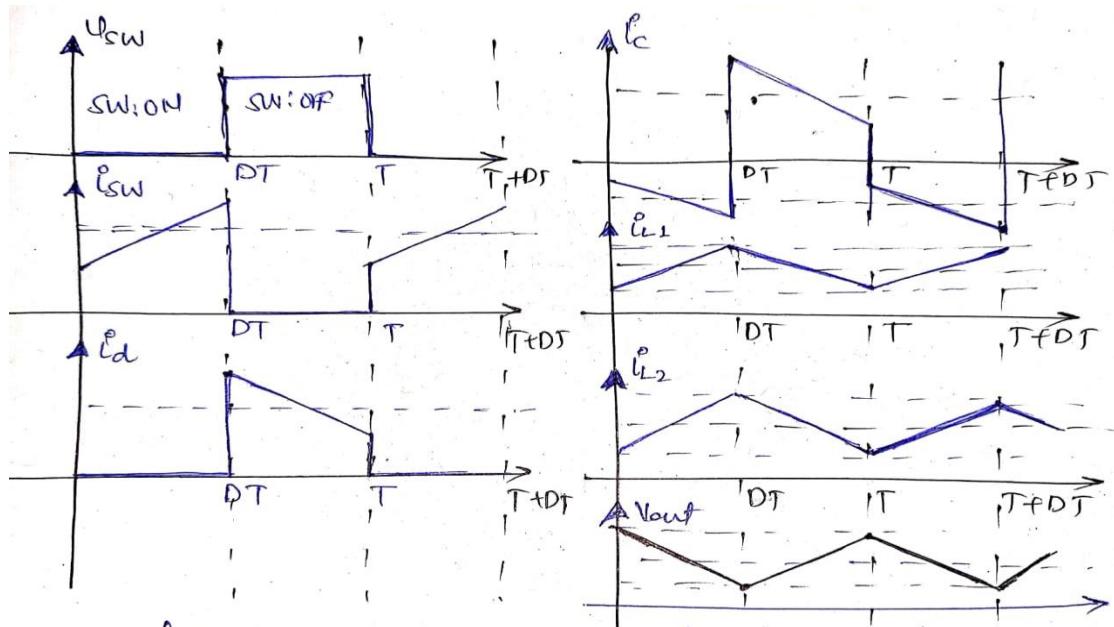


fig:- SEPIC converter waveforms.

Fig 2: theoretical waveforms of SEPIC converter

10. DESIGN PARAMETERS:

- d. The value of output voltage =

$$V_0 = \frac{V_s D}{1-D} = 96\text{V}$$

- e. Inductance of Inductors =

$$L_1 = \frac{V_s D}{\Delta I_{L_1} f} = 166.66 \mu\text{H}$$

$$L_2 = \frac{V_s D}{\Delta I_{L_2} f} = 333.33 \mu\text{H}$$

- f. Switching time period = $T = \frac{1}{f} = 20 \mu\text{sec}$

11. SNAPSHOT OF THE SIMULATED CIRCUIT:

Considering diode $V_f = 0.8$ V, & solver configuration as-

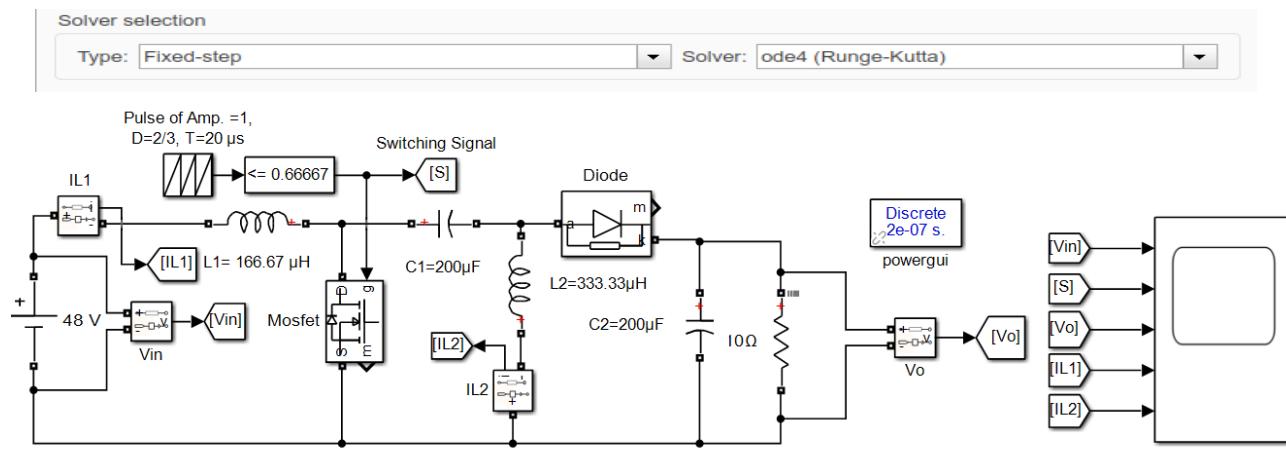
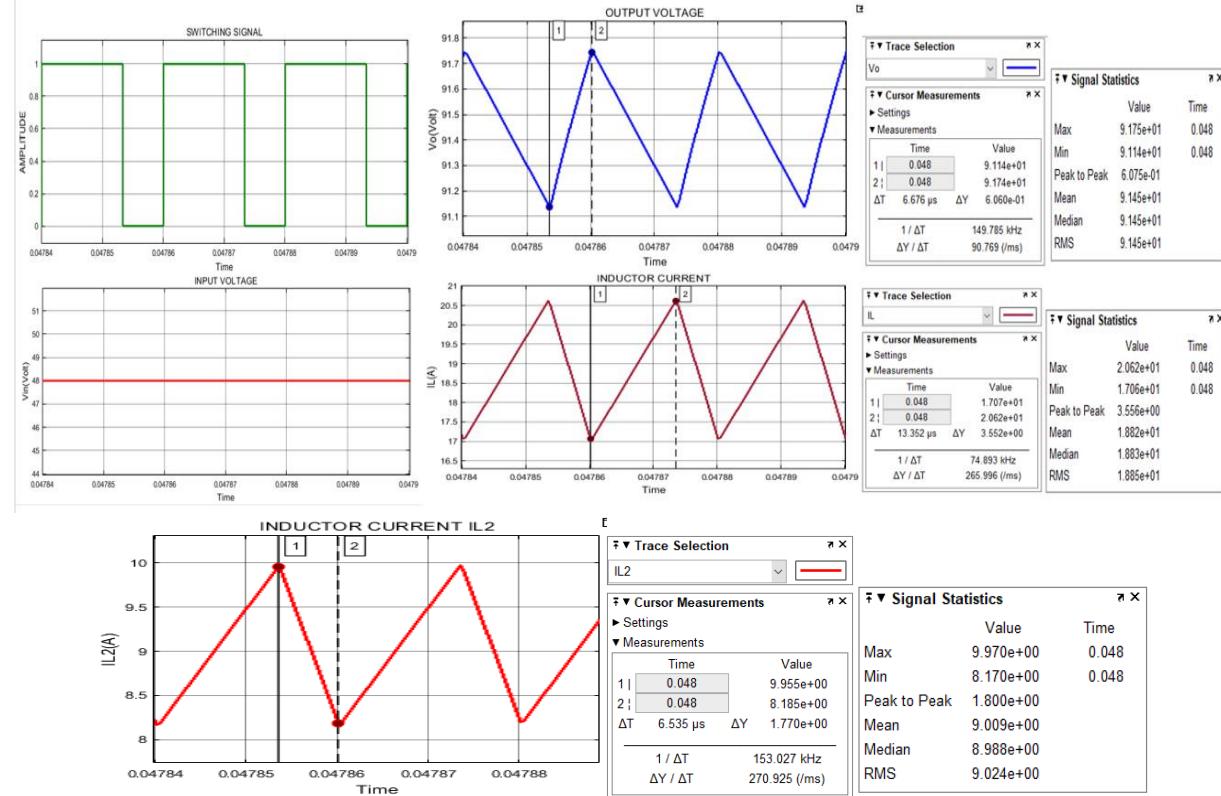


Fig 3: simulated circuit diagram

12. WAVEFORMS OBTAINED:



13. SIMULATION RESULT:

Parameter	Theoretical value	Practical value	Ripple value
Output voltage	96 V	91.45 V	.607 V
Inductor average current I_{L1}	19.2 A	18.82 A	3.55 A
Inductor average current I_{L2}	9.6 A	9.009 A	1.8 A

Submitted by – Shivraj Vishwakarma,
M. Tech. 1st Year
Roll No. - 224102112
EEED, Power Engg.

EXPERIMENT No. 3

CONTINUOUS AND DISCONTINUOUS CONDUCTION MODES OF BASIC DC - DC CONVERTERS USING MATLAB/SIMULINK

Objective:

To study the Continuous and Discontinuous Conduction modes of basic DC - DC converters viz. buck, boost, buck-boost converters using MATLAB/SIMULINK

1. Buck Converter

Parameters:

Input voltage: **48V**; Switching Frequency: **50 kHz**; Duty Ratio: **0.5**; Load Resistance: **5 Ω**;

Capacitor: **200 μF**, Inductor: Case-1, **L=10*L_c**, Case-2, **L=L_c**, Case-3, **L=L_c / 10**

Theory: -

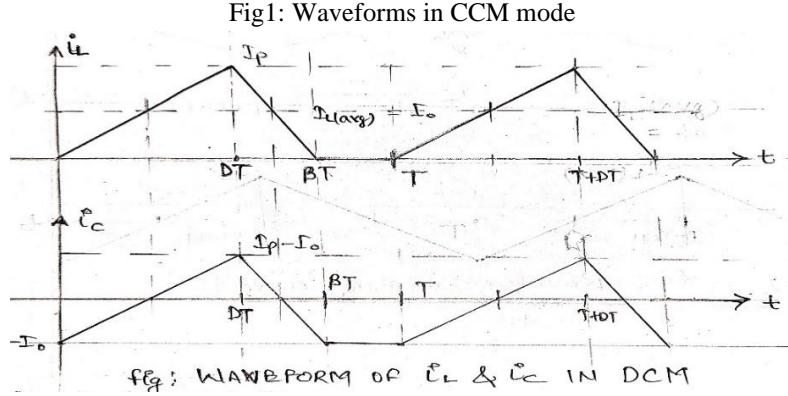
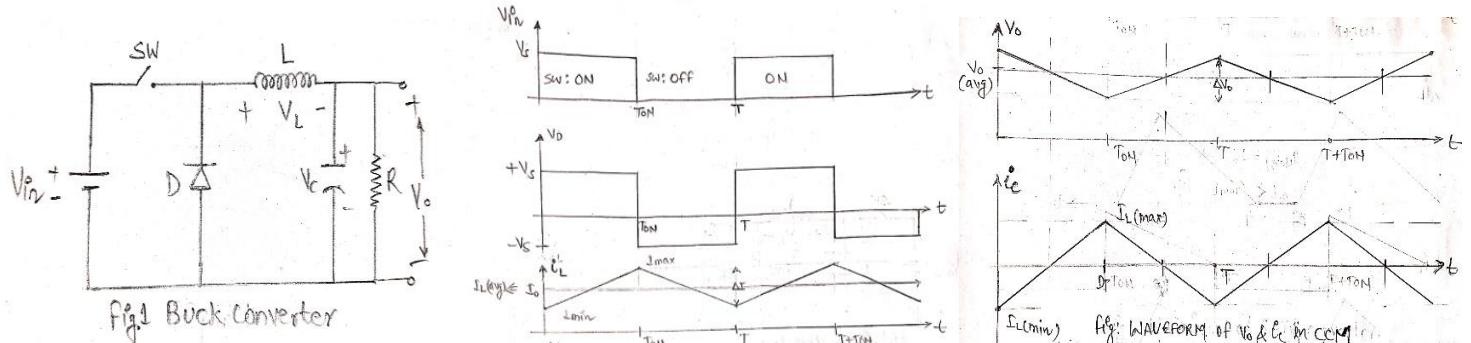
A buck converter is a step-down DC to DC converter. The switching action can be implemented by a BJT, a MOSFET, or an IGBT. Figure 1 shows a simplified block diagram of a buck converter that accepts a DC input and uses pulse-width modulation (PWM) of switching frequency to control the switch. An external diode, together with external inductor and output capacitor, produces the regulated dc output. Buck, or step-down converters produce an average output voltage lower than the input source voltage.

The output voltage can be represented as, $V_o = D * V_{in}$ where D is duty ratio defined as-

CCM and DCM:

The buck converter can operate in two different modes; continuous conduction mode (CCM) and discontinuous conduction mode (DCM). A buck converter operates in continuous mode if the current through the inductor never falls to zero during the commutation cycle. In DCM, the current through the inductor falls to zero during part of the period.

CIRCUIT DIAGRAM & WAVEFORMS: -



DESIGN PARAMETER CALCULATIONS: -

INDUCTOR:

For critical Inductance, $I_{L(\text{avg.})} = \Delta I_L \Rightarrow L_c = \frac{(1-D)*R}{2f} \Rightarrow L_c = 25 \mu\text{H}$

CASE 1: When $L = 10 \times 25 \mu\text{H} \Rightarrow L = 250 \mu\text{H}$ (CCM)

Output voltage: $V_o = D \cdot V_{in} \Rightarrow V_o = 24 \text{ V}$

$$\text{Ripple in Inductor Current } \Delta I_L = \frac{(1-D) \cdot V_o}{fL} \Rightarrow \Delta I_L = 0.96 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = \frac{\Delta I_L}{8fC} \Rightarrow \Delta V_o = 12 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

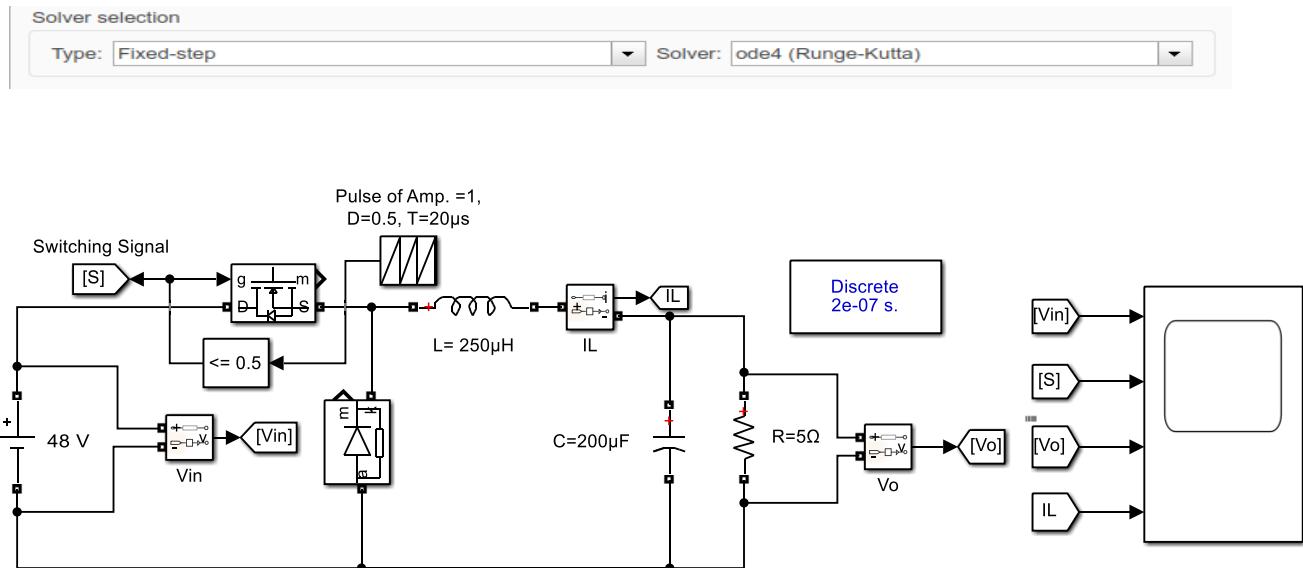
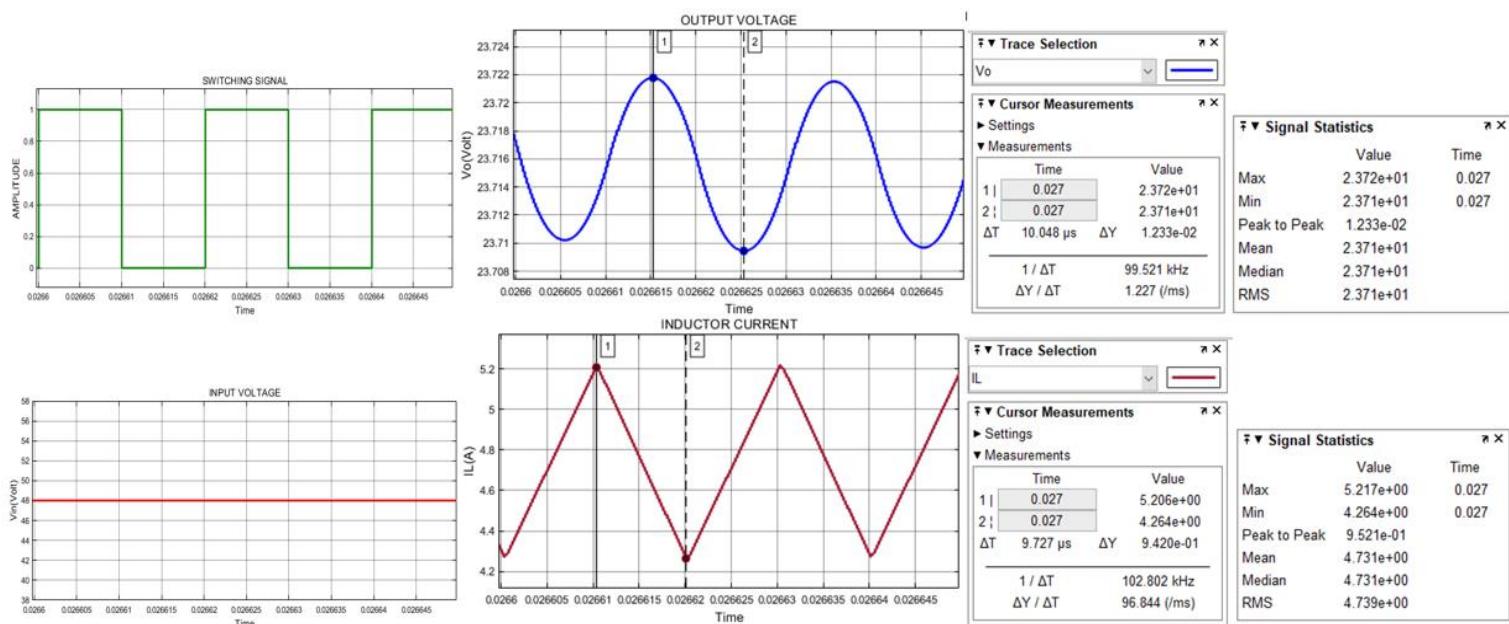


Fig3: Simulation Model of Buck Converter

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

$$\text{Ripple in Inductor Current } \Delta I_L = 0.952 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = 12.33 \text{ mV}$$

CASE 2: When $L = L_c \Rightarrow L = 25 \mu\text{H}$ (Boundary)

Output voltage: $V_o = D \cdot V_{in} \Rightarrow V_o = 24 \text{ V}$

$$\text{Ripple in Inductor Current } \Delta I_L = \frac{(1-D) \cdot V_o}{2fL} \Rightarrow \Delta I_L = 9.6 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = \frac{\Delta I_L}{8fC} \Rightarrow \Delta V_o = 120 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

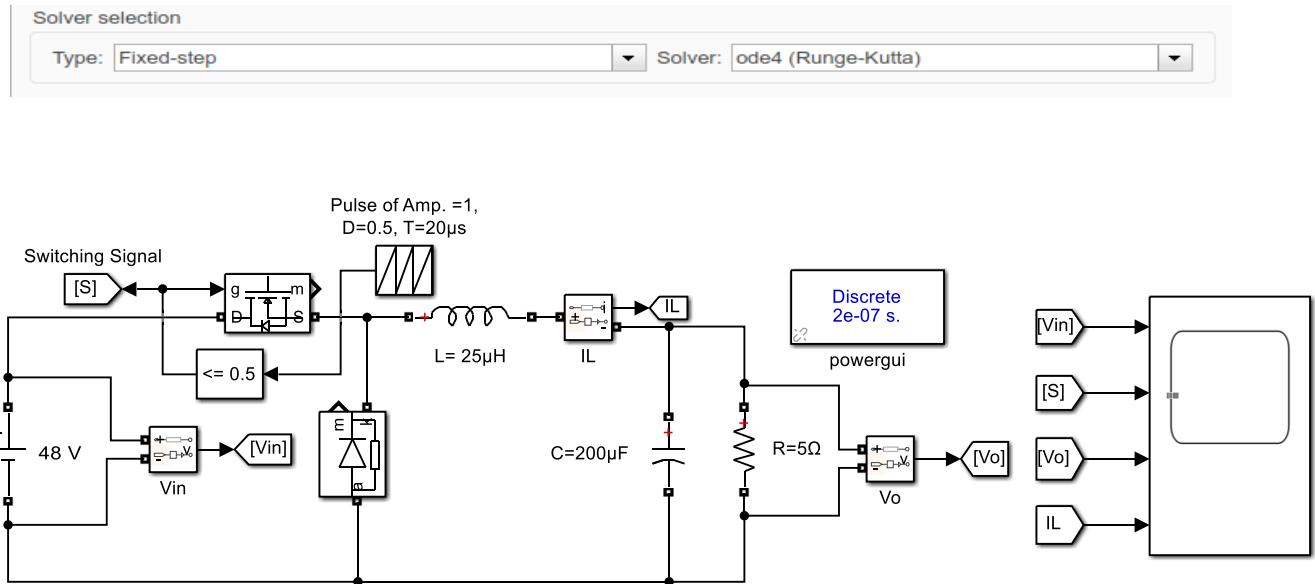
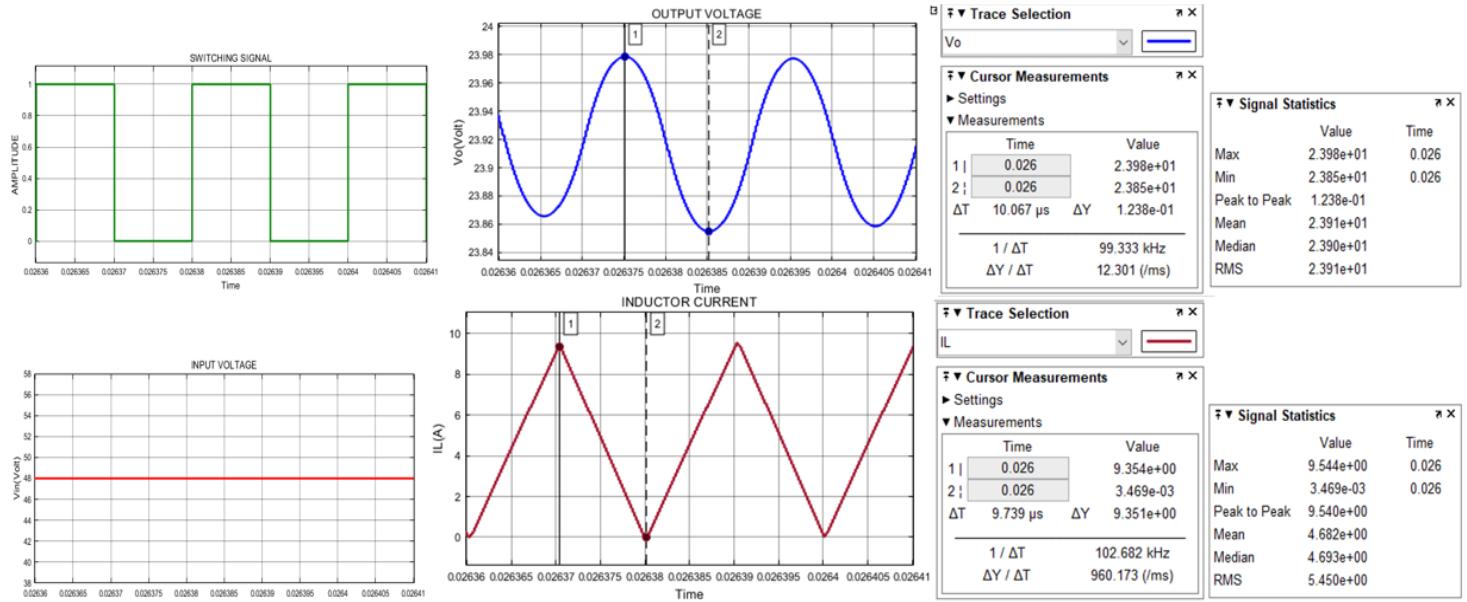


Fig3: Simulation Model of Buck Converter at Boundary of CCM & DCM

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 9.54 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 123.8 \text{ mV}$

CASE 3: When $L = \frac{L_c}{10} \Rightarrow L = 2.5 \mu\text{H (DCM)}$

Output voltage: In DCM Mode for constant D,

$$\frac{V_o}{V_{in}} = \frac{2}{1 + \sqrt{1 + \frac{4K}{D^2}}} \text{ where } K = \frac{2L}{RT_s} \Rightarrow V_o = 40.997 \text{ V}$$

Since in DCM,

$$V_o = \frac{D}{\beta} * V_{in} \Rightarrow \beta = 0.5854,$$

Ripple in inductor current = Peak value of inductor current,

$$\Delta I_L = I_p = \frac{(\beta - D)V_o}{fL} = 28.012 \text{ A}$$

Ripple in Output Voltage

$$\Delta V_o = \frac{I_o}{4fC} (2 - \beta)^2 \Rightarrow \Delta V_o = 410.2 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

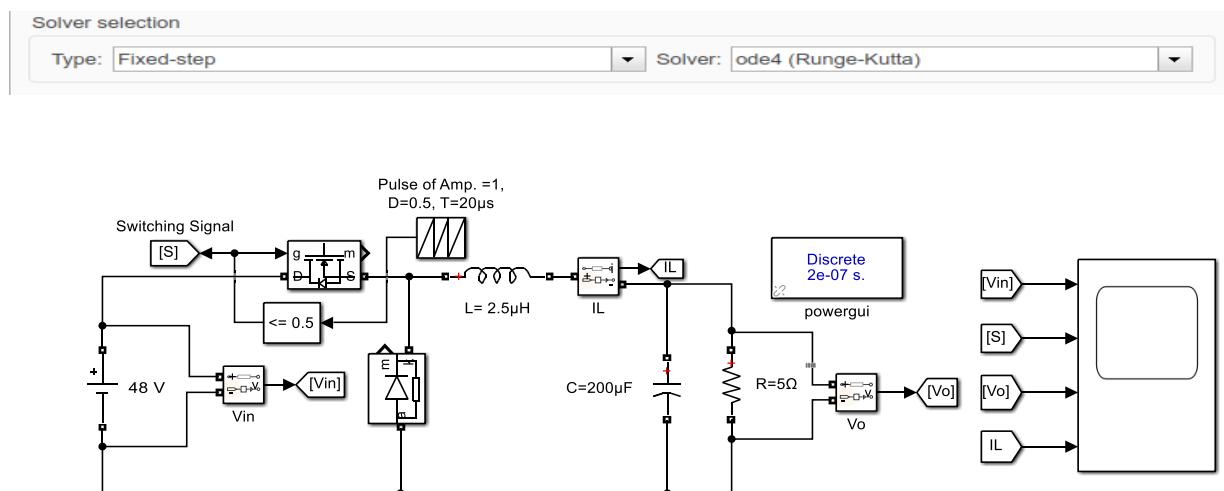
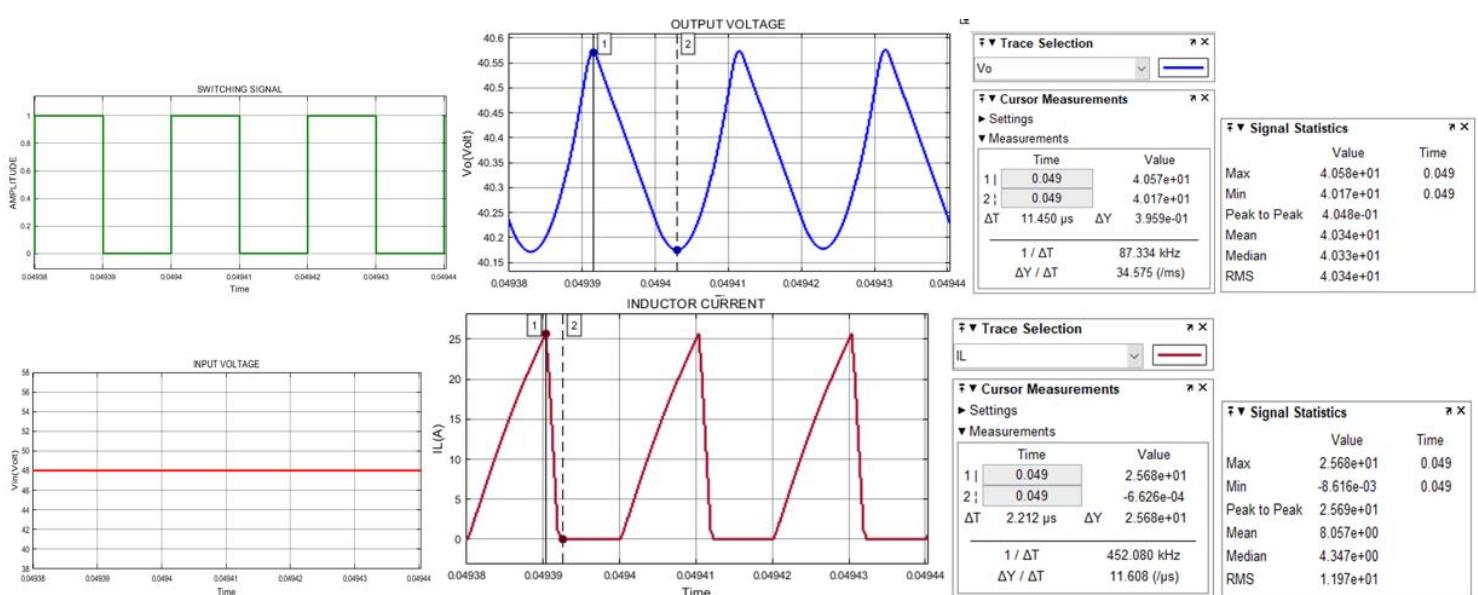


Fig3: Simulation Model of Buck Converter in DCM

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 25.69 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 404.8 \text{ Mv}$

2. Boost Converter

Parameters:

Input voltage: **24V**; Switching Frequency: **50 kHz**; Duty Ratio: **0.5**; Load Resistance: **10 Ω**;

Capacitor: **200 μF**, Inductor: Case-1, **L=10*L_c**, Case-2, **L=L_c**, Case-3, **L= L_c / 10**

Theory: -

A boost converter's output voltage is always higher than the input voltage. Figure 1 shows the schematics of a boost converter. The diode provides a path for the inductor current when the switch is opened and is reverse biased when the switch is closed.

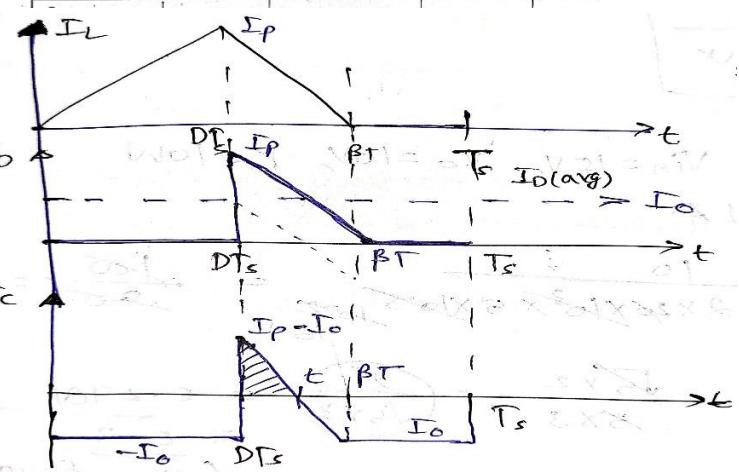
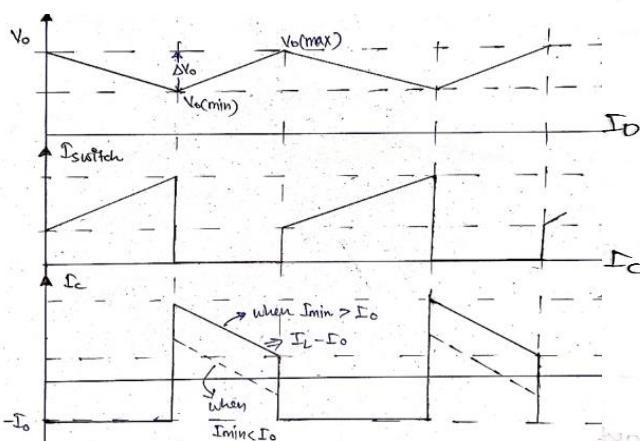
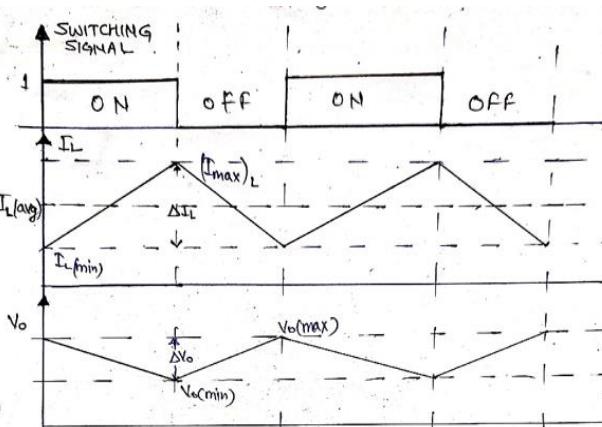
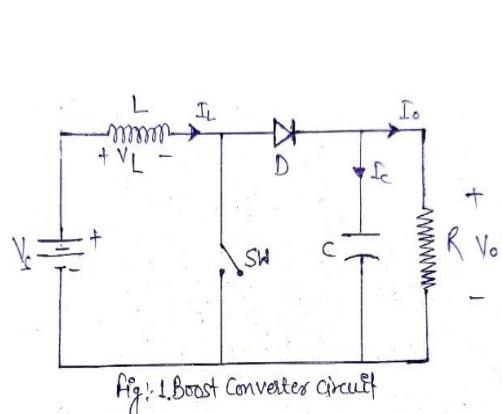
$$V_o = \frac{V_S}{1-D}, \quad \text{where } D = \frac{T_{on}}{T}$$

CCM, DCM & CRITICAL INDUCTANCE:

- If the inductor current is continuous, the boost converter is operating in continuous conduction mode.
- If the inductor current is reduced to zero, the boost converter is operating in discontinuous mode.
- The minimum inductance to keep the boost converter in continuous conduction mode is the critical inductance. Value of the critical inductance is-

$$L_C = \frac{D(1-D)^2 R}{2f}$$

CIRCUIT DIAGRAM & WAVEFORMS: -



DESIGN PARAMETER CALCULATIONS: -

INDUCTOR: For critical Inductance,

$$I_{L \text{ (avg.)}} = \Delta I_L \Rightarrow L_c = \frac{D(1-D)^2 * R}{2f} \Rightarrow L_c = 12.5 \mu\text{H}$$

CASE 1: When $L = 10 * 12.5 \mu\text{H} \Rightarrow L = 125 \mu\text{H (CCM)}$

$$\text{Output voltage: } V_o = \frac{V_s}{1-D} \Rightarrow V_o = 48 \text{ V, } I_{L \text{ (avg.)}} = \frac{I_o}{1-D} = 9.6 \text{ A}$$

$$\text{Ripple in Inductor Current } \Delta I_L = \frac{D * V_s}{fL} \Rightarrow \Delta I_L = 1.92 \text{ A,}$$

$$\text{Since } I_{L \text{ (min)}} = 9.6 - \frac{1.92}{2} = 8.64 \text{ A} \Rightarrow I_{L \text{ (min)}} > I_o$$

$$\text{Therefore, Ripple in Output Voltage } \Delta V_o = \frac{D I_o}{f C} \Rightarrow \Delta V_o = 240 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

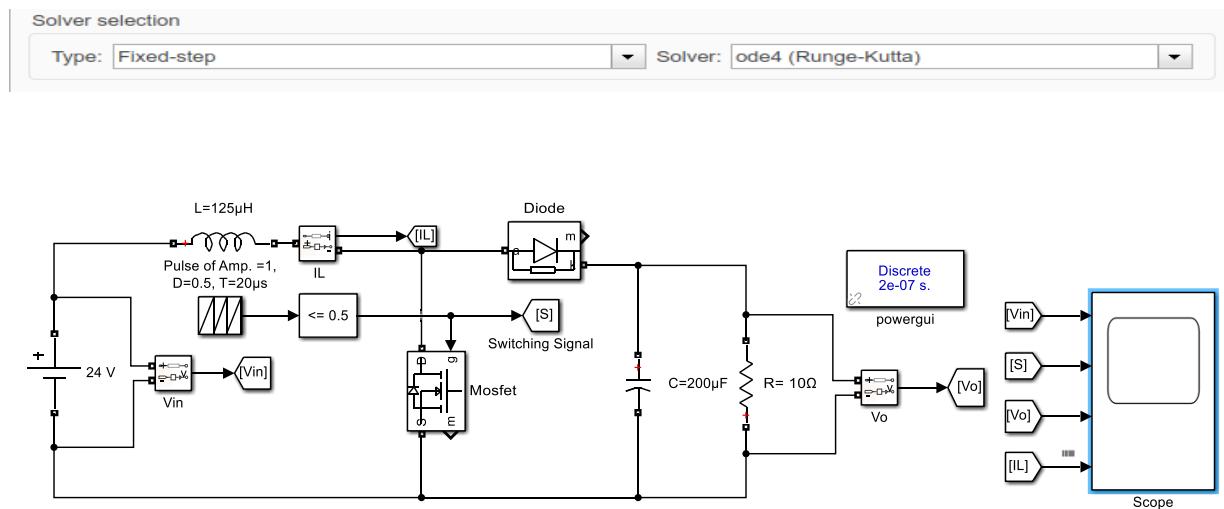
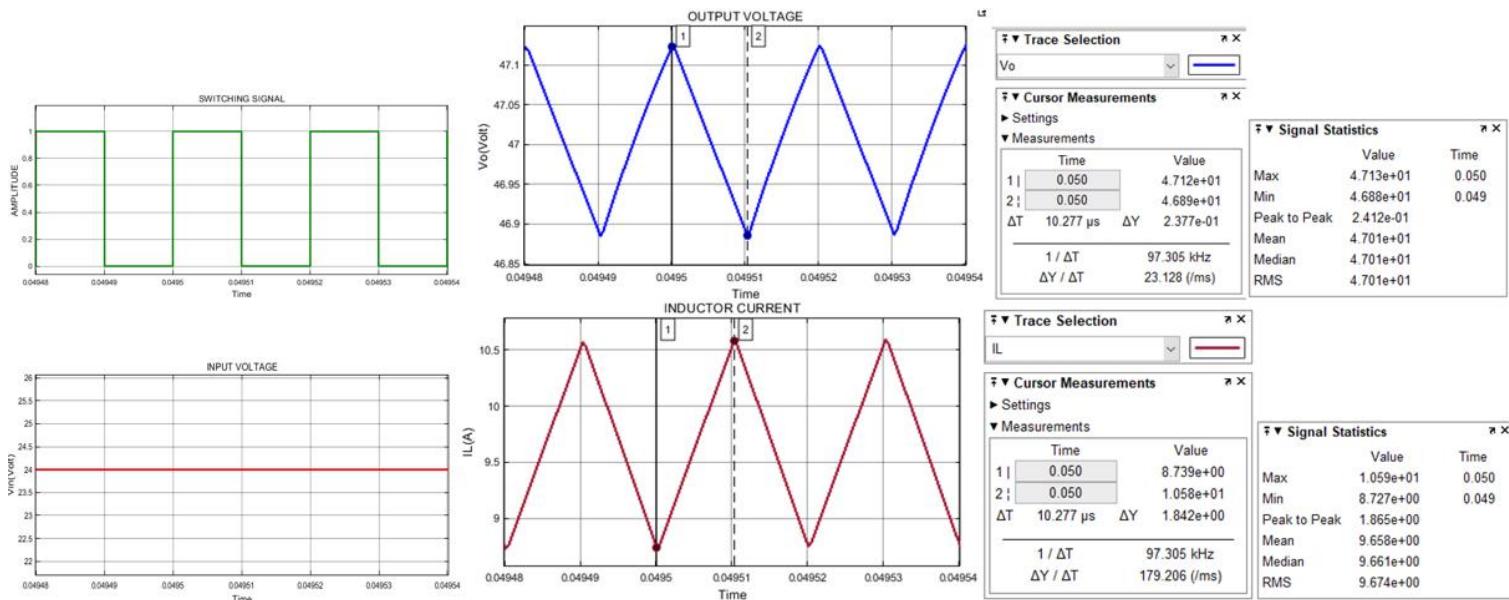


Fig3: Simulation Model of Boost Converter

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 1.865 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 241.2 \text{ mV}$

CASE 2: When $L = L_c \Rightarrow L = 12.5 \mu\text{H}$ (*Boundary*)

Output voltage: $V_o = D \cdot V_{in} \Rightarrow V_o = 24 \text{ V}$, & $I_{L(\text{avg})} = \frac{\Delta I_L}{2}$, $I_{L(\text{avg})} = \frac{I_o}{1-D} = 9.6 \text{ A}$

Ripple in Inductor Current $\Delta I_L = \frac{DV_S}{fL} \Rightarrow \Delta I_L = 19.2 \text{ A}$,

Ripple in Output Voltage $\Delta V_o = \frac{I_o}{4fC} (1 + D)^2 \Rightarrow \Delta V_o = 270 \text{ mV}$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

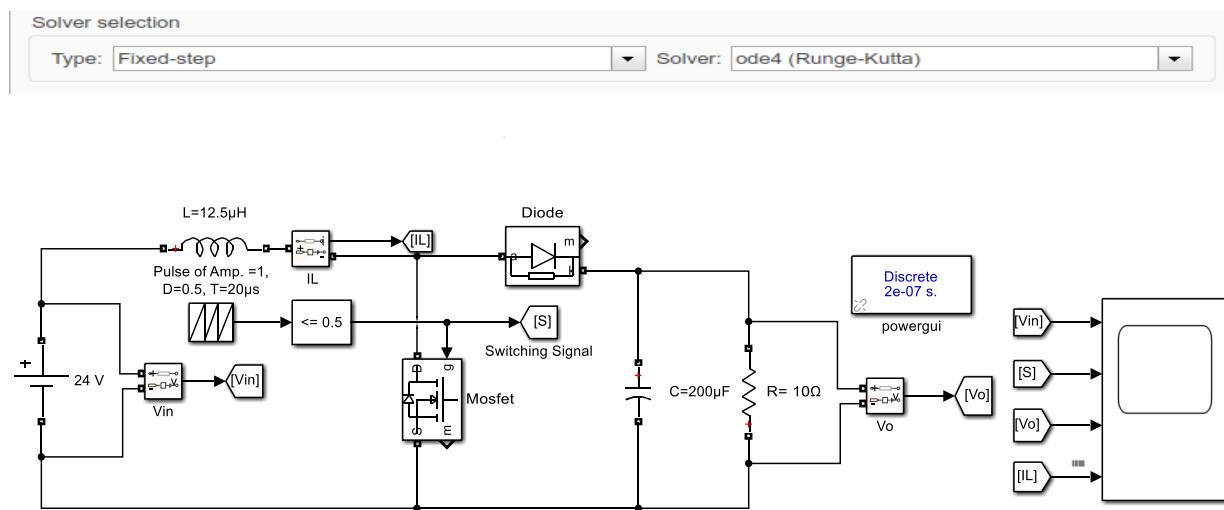
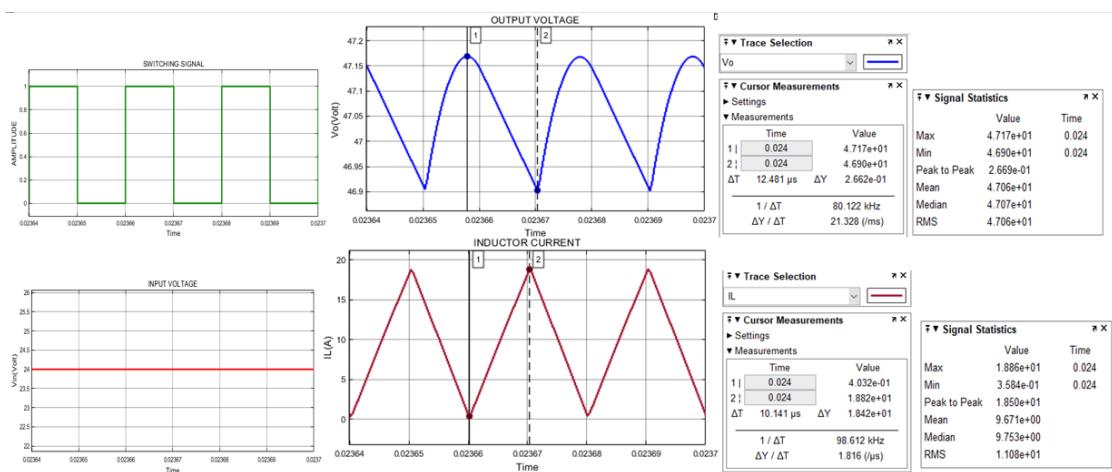


Fig3: Simulation Model of Boost Converter at Boundary of CCM & DCM

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 18.5 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 266.9 \text{ mV}$

CASE 3: When $L = \frac{L_c}{10} \Rightarrow L = 1.25 \mu\text{H (DCM)}$

Output voltage: In DCM mode for constant D,

$$\frac{V_o}{V_{in}} = \frac{1 + \sqrt{1 + \frac{4D^2}{K}}}{2}, \text{ where } K = \frac{2L}{RT_S} \Rightarrow V_o = 120 \text{ V} \text{ & } I_o = \frac{V_o}{R} = 12 \text{ A}$$

Since in DCM,

$$V_o = \frac{V_{in}}{1 - \frac{D}{\beta}} \Rightarrow \beta = \frac{D V_o}{V_o - V_{in}} \Rightarrow \beta = 0.625$$

Ripple in inductor current = Peak value of inductor current,

$$\Delta I_L = I_p = \frac{DV_s}{fL} = 192 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = \frac{I_o}{4fC} \{2 - (\beta - D)\}^2 \Rightarrow \Delta V_o = 1054.7 \text{ mV}$$

SIMULATION CIRCUIT: Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

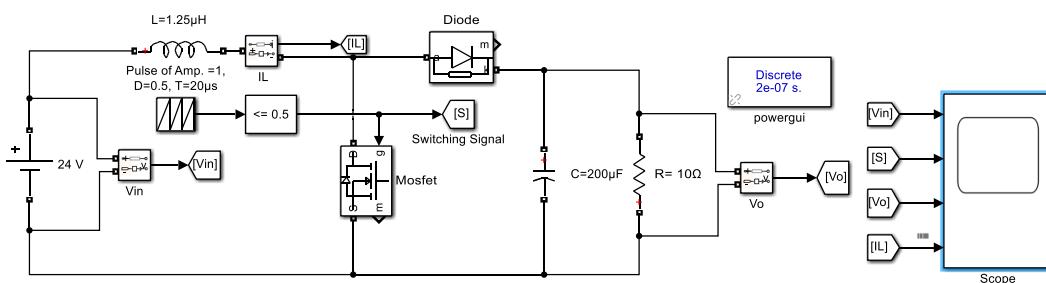
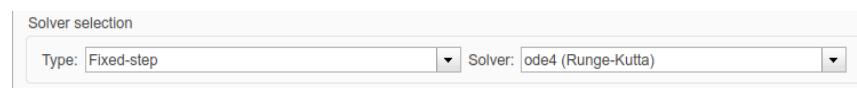
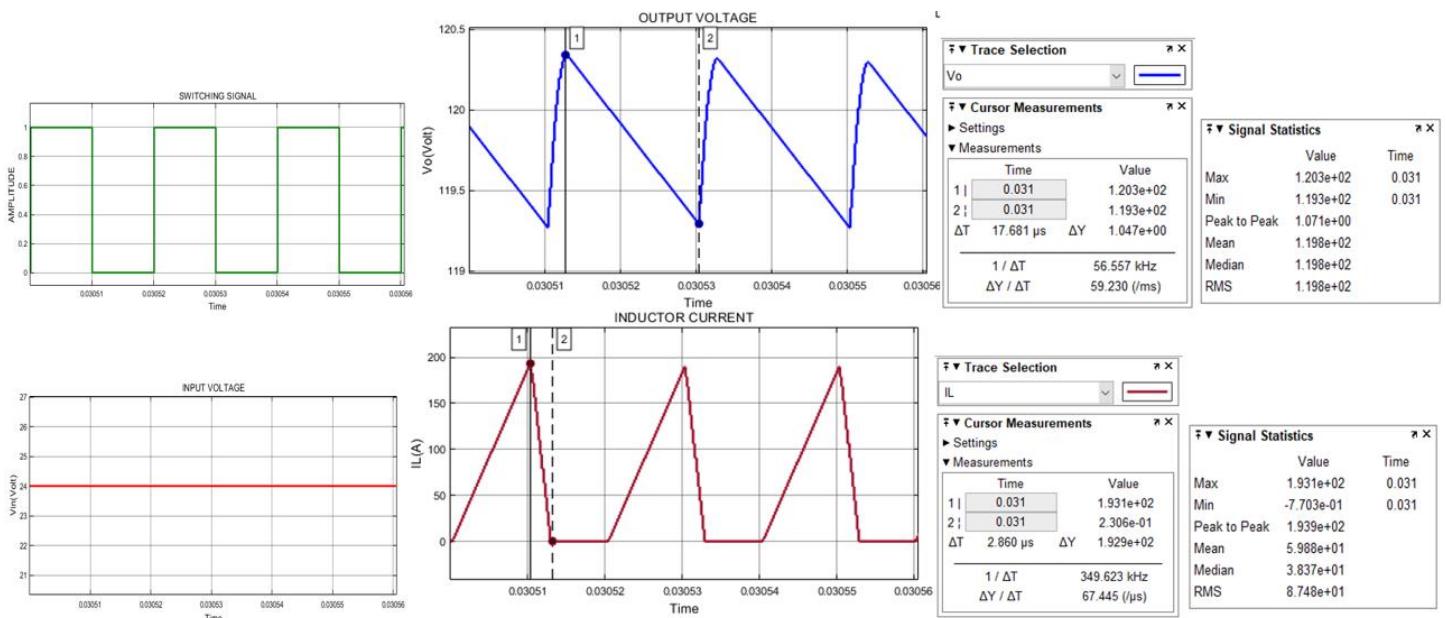


Fig3: Simulation Model of Boost Converter

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 193.9 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 1071 \text{ mV}$

3. Buck - Boost Converter

Parameters:

Input voltage: **100V**; Switching Frequency: **50 kHz**; Duty Ratio: $D = \frac{1}{3}$; Load Resistance: **25 Ω**;

Capacitor: **200 μF**, Inductor: Case-1, $L=10*L_c$, Case-2, $L=L_c$, Case-3, $L=L_c/10$

Theory: -

The output voltage of a buck-boost converter can be either higher or lower than the input voltage. Ratio of the output voltage over the input voltage is shown below.

$$\frac{V_o}{V_{in}} = \frac{D}{1-D}$$

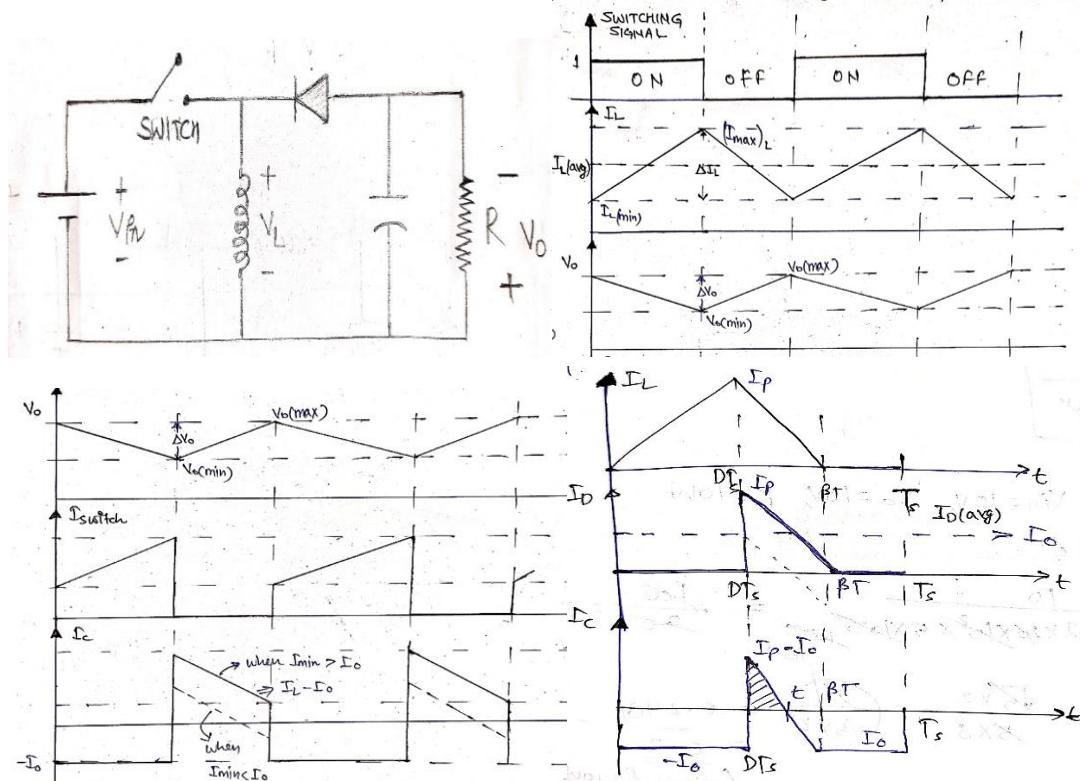
If $D > 0.5$, the output voltage is greater than the input voltage. If $D < 0.5$, the output voltage is less than the input voltage. (1)

CCM, DCM & CRITICAL INDUCTANCE:

- If the inductor current is continuous, the boost converter is operating in continuous conduction mode.
- If the inductor current is reduced to zero, the boost converter is operating in discontinuous mode.
- The minimum inductance to keep the boost converter in continuous conduction mode is the critical inductance. Value of the critical inductance is-

$$L_c = \frac{(1-D)^2 R}{2f}$$

CIRCUIT DIAGRAM & WAVEFORMS: -



DESIGN PARAMETER CALCULATIONS: -

INDUCTOR: For critical inductance,

$$I_{L(\text{avg.})} = \Delta I_L \Rightarrow L_c = \frac{(1-D)^2 * R}{2f} \Rightarrow L_c = 111.11 \mu\text{H}$$

CASE 1: When $L = 10 * 111.11 \mu\text{H} \Rightarrow L = 1111.1 \mu\text{H (CCM)}$

Output voltage: $V_o = \frac{D}{1-D} V_{in} \Rightarrow V_o = 50 \text{ V}$, $I_{L(\text{avg.})} = \frac{I_o}{1-D} = 3 \text{ A}$ & $I_o = \frac{V_o}{R} = 2 \text{ A}$

Ripple in Inductor Current $\Delta I_L = \frac{DV_S}{fL} \Rightarrow \Delta I_L = 0.6 \text{ A}$,

$$\text{Since } I_{L(\min)} = 3 - \frac{0.6}{2} = 2.7 \text{ A} \Rightarrow I_{L(\min)} > I_o$$

$$\text{Therefore, Ripple in Output Voltage } \Delta V_o = \frac{D I_o}{f C} \Rightarrow \Delta V_o = 66.66 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

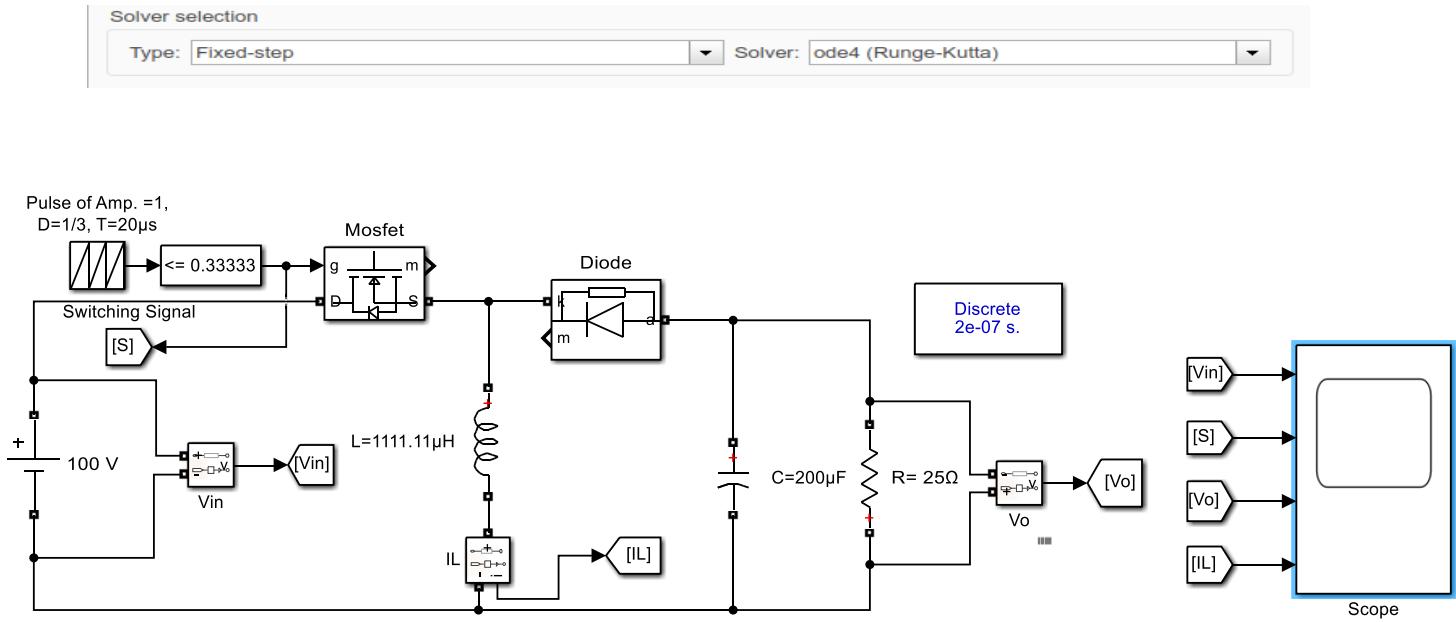
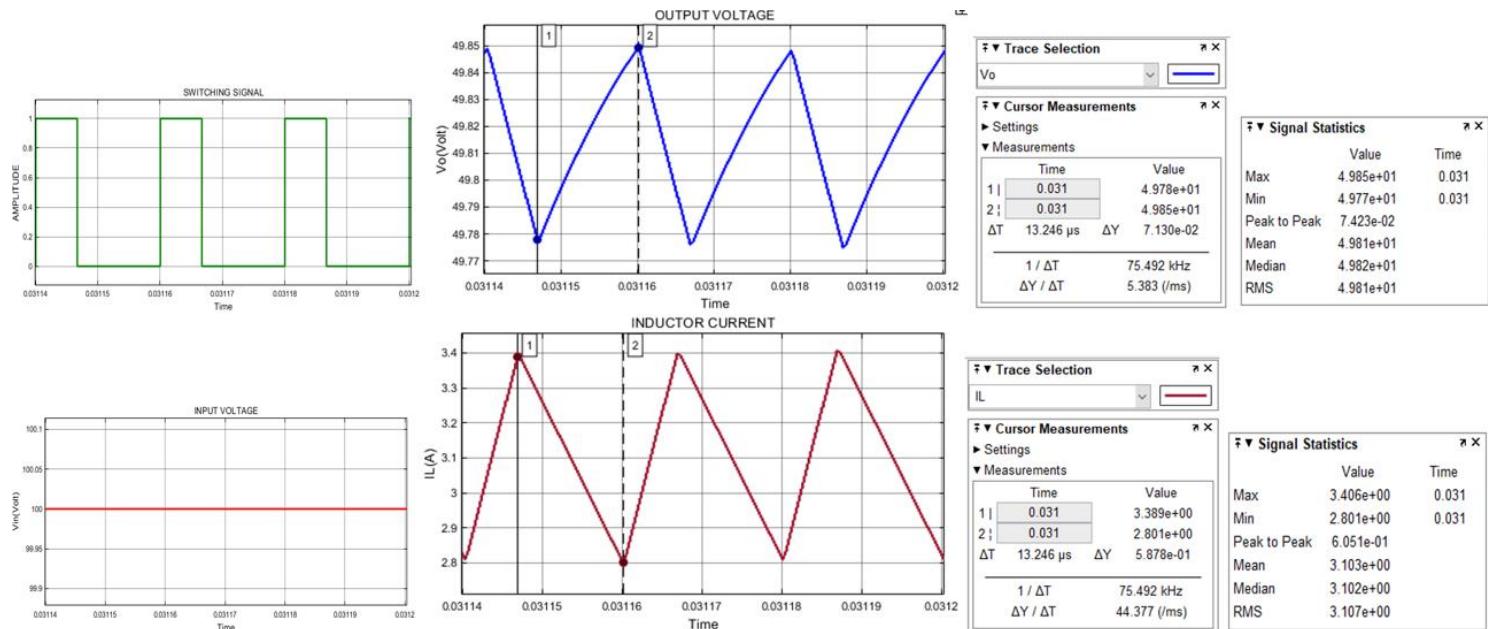


Fig3: Simulation Model of Buck-Boost Converter

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

$$\text{Ripple in Inductor Current } \Delta I_L = 0.6051 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = 71.3 \text{ mV}$$

CASE 2: When $L = L_c \Rightarrow L = 111.11 \mu\text{H}$ (Boundary)

Output voltage: $V_o = \frac{D}{1-D} V_{in} \Rightarrow V_o = 50 \text{ V}$

$$\text{Ripple in Inductor Current } \Delta I_L = \frac{(1-D)*V_o}{2fL} \Rightarrow \Delta I_L = 6 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = \frac{I_o}{4fC} (1 + D)^2 \Rightarrow \Delta V_o = 88.8 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

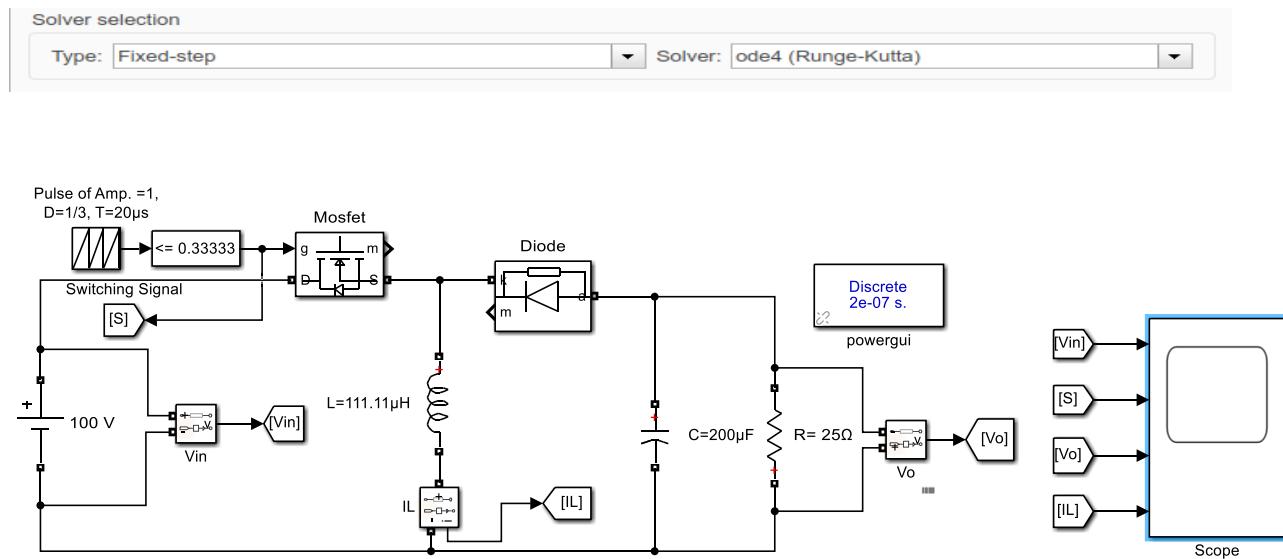
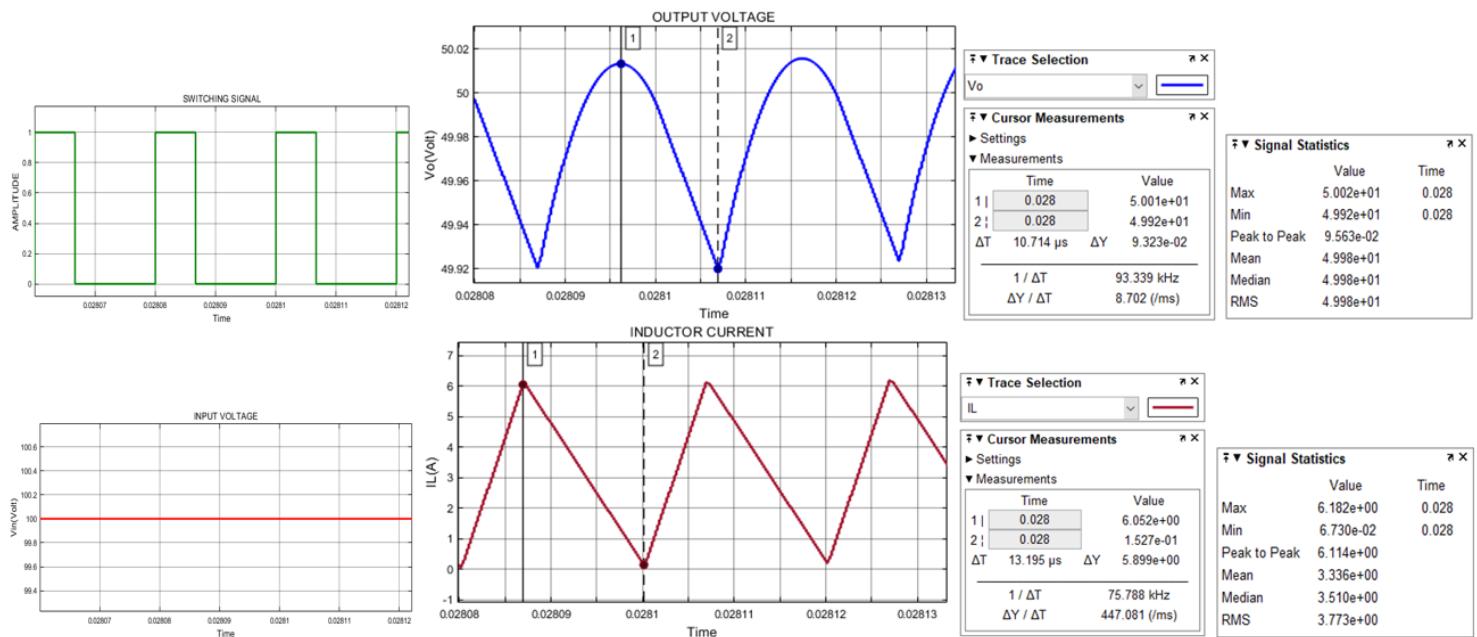


Fig3: Simulation Model of Buck-Boost Converter at Boundary of CCM & DCM

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

$$\text{Ripple in Inductor Current } \Delta I_L = 6.11 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = 93.23 \text{ mV}$$

CASE 3: When $L = \frac{L_c}{10} \Rightarrow L = 11.111 \mu\text{H (DCM)}$

Output voltage: In DCM mode for constant D,

$$\frac{V_o}{V_{in}} = \frac{D}{\sqrt{K}}, \text{ where } K = \frac{2L}{RT_s} \Rightarrow V_o = 158.12 \text{ V} \text{ & } I_o = \frac{V_o}{R} = 6.32 \text{ A}$$

Since in DCM,

$$V_o = \frac{D}{\beta - D} V_{in} \Rightarrow \beta = \frac{D(V_{in} + V_o)}{V_o} \Rightarrow \beta = 0.544$$

Ripple in inductor current = Peak value of inductor current,

$$\Delta I_L = I_p = \frac{DV_s}{fL} = 60 \text{ A}$$

$$\text{Ripple in Output Voltage } \Delta V_o = \frac{I_o}{4fC} \{2 - (\beta - D)\}^2 \Rightarrow \Delta V_o = 505.9 \text{ mV}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

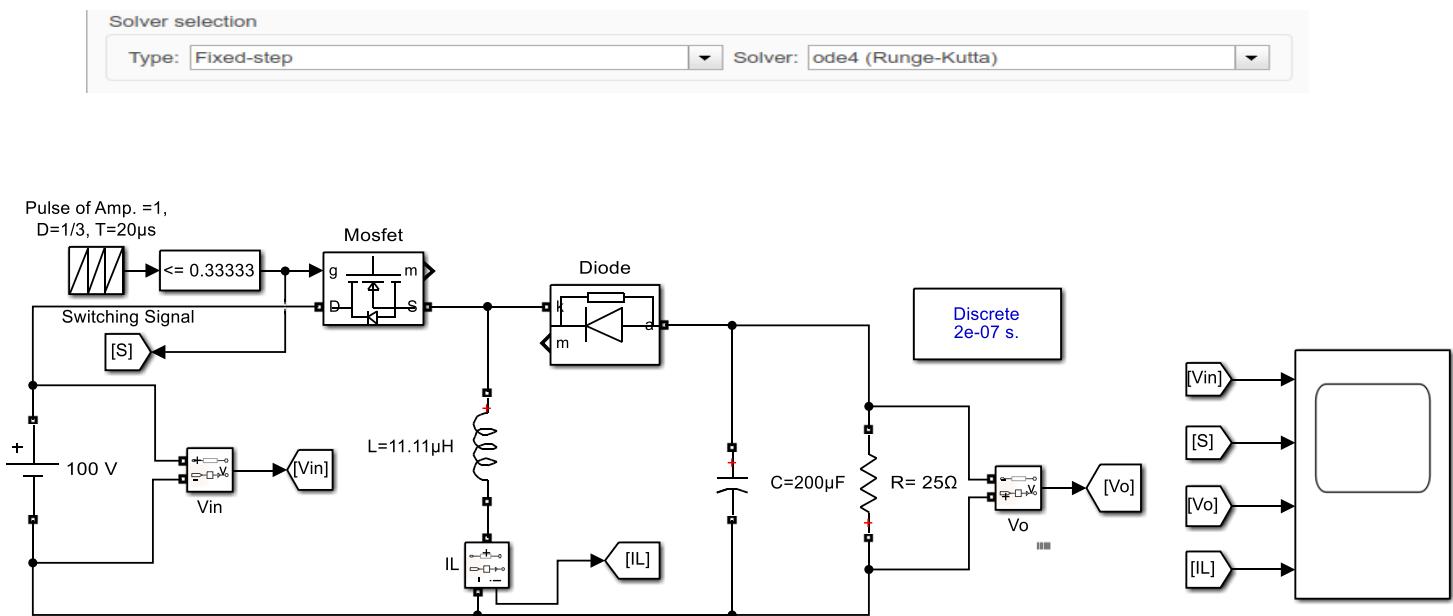
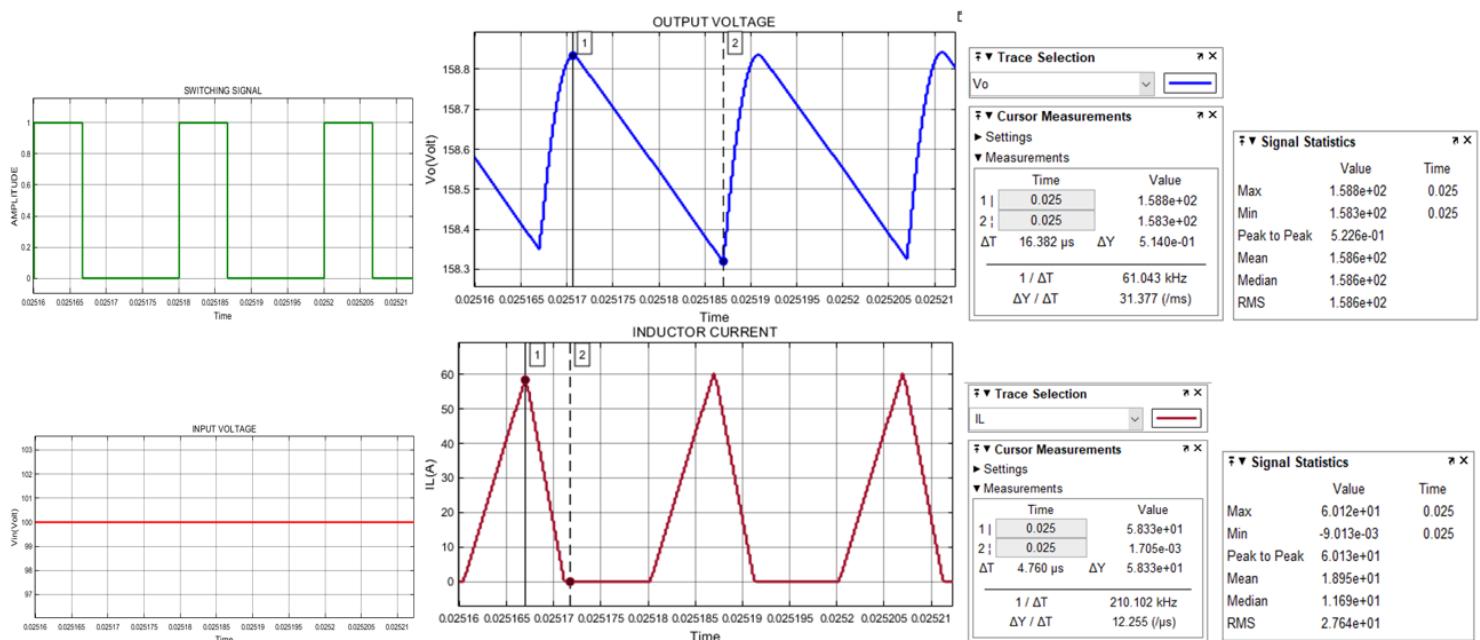


Fig3: Simulation Model of Buck- Boost Converter

WAVEFORMS OBTAINED:



RESULTS AFTER SIMULATION: -

Ripple in Inductor Current $\Delta I_L = 60.13 \text{ A}$

Ripple in Output Voltage $\Delta V_o = 514 \text{ mV}$

*Submitted by – Shivraj Vishwakarma,
M. Tech. 1st Year,
Power Engg.
Roll No. - 224102112*

EXPERIMENT No. 2

SIMULATION OF BOOST CONVERTER USING MATLAB/SIMULINK

Objective: To Design & Simulate a Boost Converter circuit using MATLAB/SIMULINK.

Parameters of the Buck Converter: Input voltage: **24V**; Output Voltage: **48V**; Output Power: **100W**; Switching Frequency: **100kHz**; Ripple in Inductor Current: **25%**; Ripple in Output Voltage: **0.1%**;

Theory: -

A boost converter's output voltage is always higher than the input voltage. Figure 1 shows the schematics of a boost converter. It has a dc input voltage, a transistor working as a switch, an inductor and a capacitor forming a low pass filter to smooth out the output voltage, and a load resistor. The diode provides a path for the inductor current when the switch is opened and is reverse biased when the switch is closed.

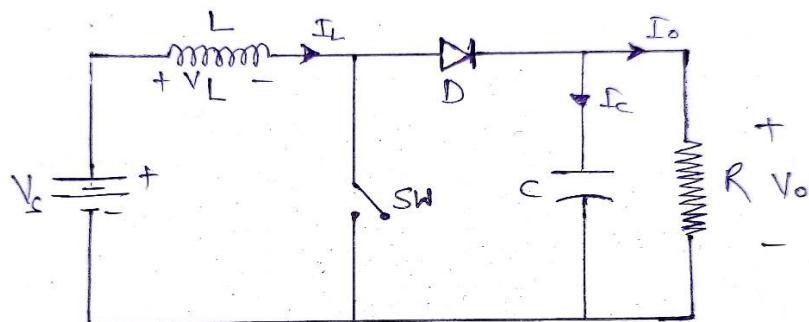
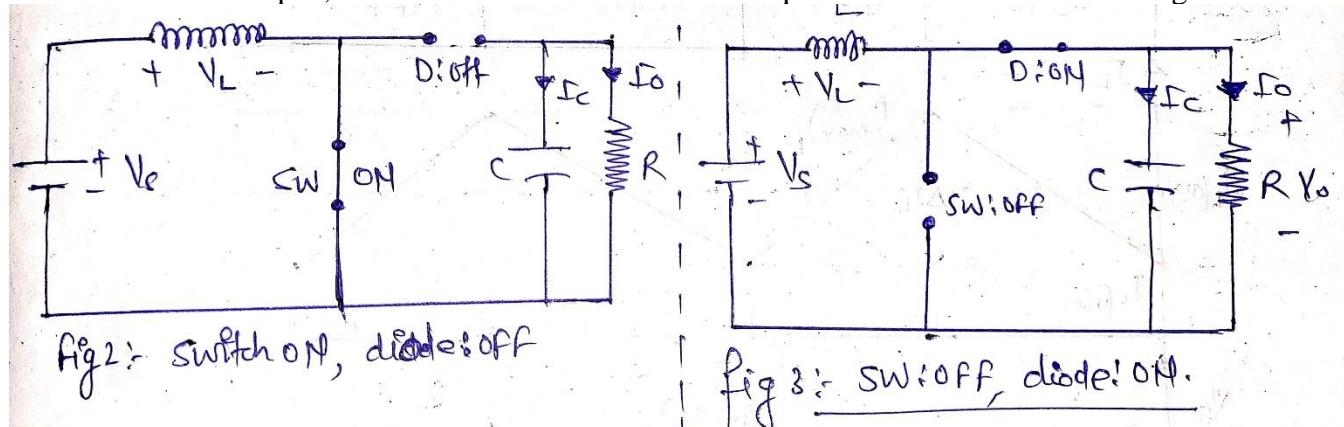


Fig: 1. Boost Converter circuit

When the switch is closed, the diode is reverse biased. The equivalent circuit is shown in Figure 2. When the switch is open, the diode is forward biased. The equivalent circuit is shown in Figure 3.



Output voltage of a boost converter is controlled by a pulse width modulated (PWM) signal shown in figure 4. The duty cycle is the ratio between the on time and the switching period shown in (2). By adjusting the duty cycle, we can obtain desired output voltage, which is shown in (1). When D increases, the output voltage increases. When D decreases, the output voltage decreases.

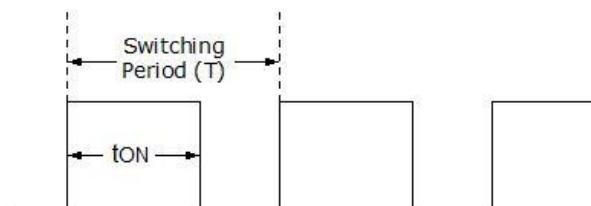


Figure 4. Pulse width modulated (PWM) Signal

$$V_o = \frac{V_s}{1-D}, \quad \dots(1)$$

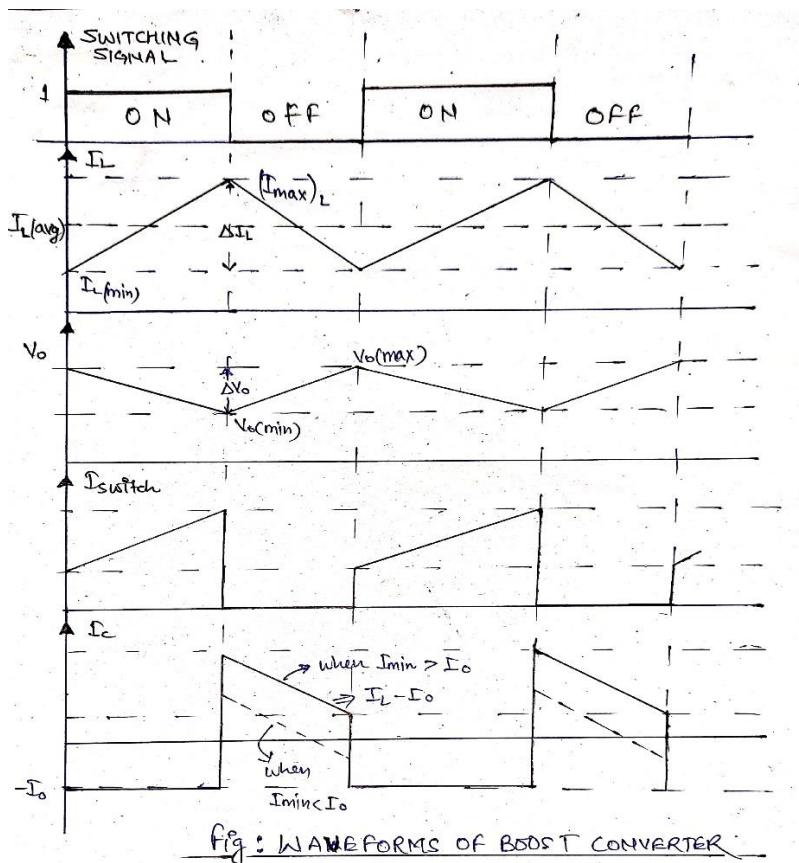
$$\text{where } D = \frac{T_{on}}{T} \quad \dots(2)$$

CRITICAL INDUCTANCE:

If the inductor current is continuous, the boost converter is operating in continuous conduction mode. If the inductor current is reduced to zero, the boost converter is operating in discontinuous mode. We would like to keep the boost converter operating in continuous conduction mode. The minimum inductance to keep the boost converter in continuous conduction mode is the critical inductance. Value of the critical inductance is shown in (3).

$$L_c = \frac{D(1-D)^2 R}{2f}$$

WAVEFORMS: -



Designing Parameters for Buck Converter:

The peak-to-peak inductor current is often a design criterion in the design of a boost converter. It is also called the current ripple. The current ripple is inversely proportional to the inductance and the switching frequency. To reduce the current ripple, we can increase the inductance or the switching frequency.

CALCULATIONS: -

Using given values as $V_o = 48 \text{ V}$, $f_{sh} = 100 \text{ kHz}$, $P_o = 100 \text{ W}$, $\Delta I_L = 25\% \text{ of } I_{L(\text{avg})}$, $\Delta V_o = 1\% \text{ of } V_o$

$$1. \text{ Duty Ratio } D = \frac{V_o - V_{in}}{V_o} = \frac{24}{48} = 0.5,$$

$$2. \text{ Average output current } I_o = \frac{P_o}{V_o} = \frac{100}{48} = 2.083 \text{ A},$$

$$3. \text{ Average inductor current } I_L = \frac{I_o}{1-D} = \frac{2.083}{1-0.5} = 4.166 \text{ A,}$$

$$4. \text{ Ripple in inductor current } \Delta I_L = \frac{1}{4} * 4.166 = 1.0416 \text{ A,}$$

$$5. \text{ Now } L = \frac{DV_s}{f\Delta I_L} = \frac{\frac{1}{2} * 24}{100 * 10^3 * 1.0416} = 115.207 \mu\text{H}$$

$$6. \Delta V_o = 0.1\% \text{ of } 48 \text{ V} = .048 \text{ V, } C = \frac{DI_o}{f\Delta V_o} = \frac{0.5 * 2.083}{100 * 10^3 * 0.048} = 217.01 \mu\text{F}$$

$$7. \text{ Load Resistance, } R = \frac{V_o^2}{P_o} = \frac{2304}{100} = 23.04 \Omega$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8 \text{ V}$, & solver configuration as-

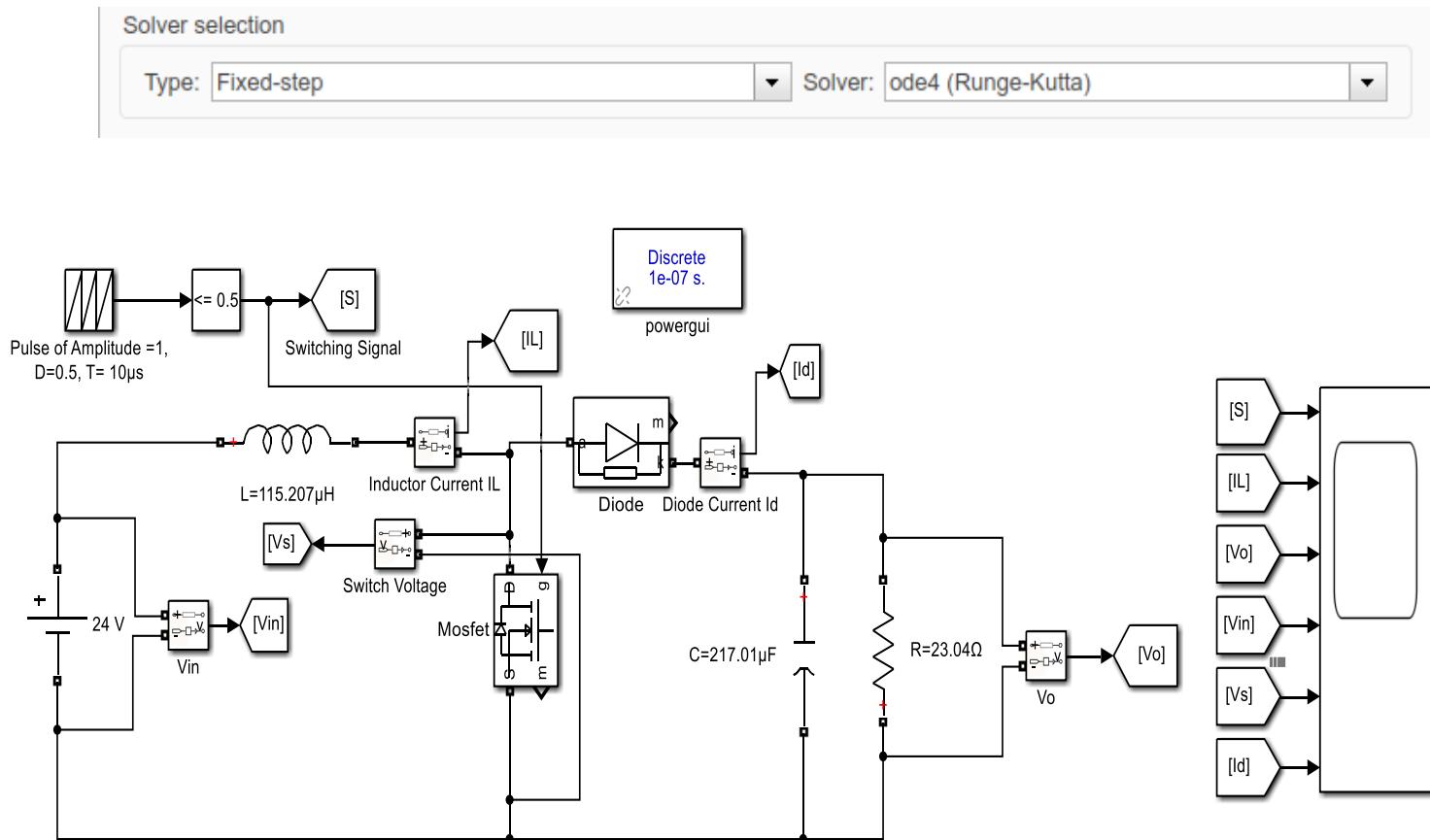
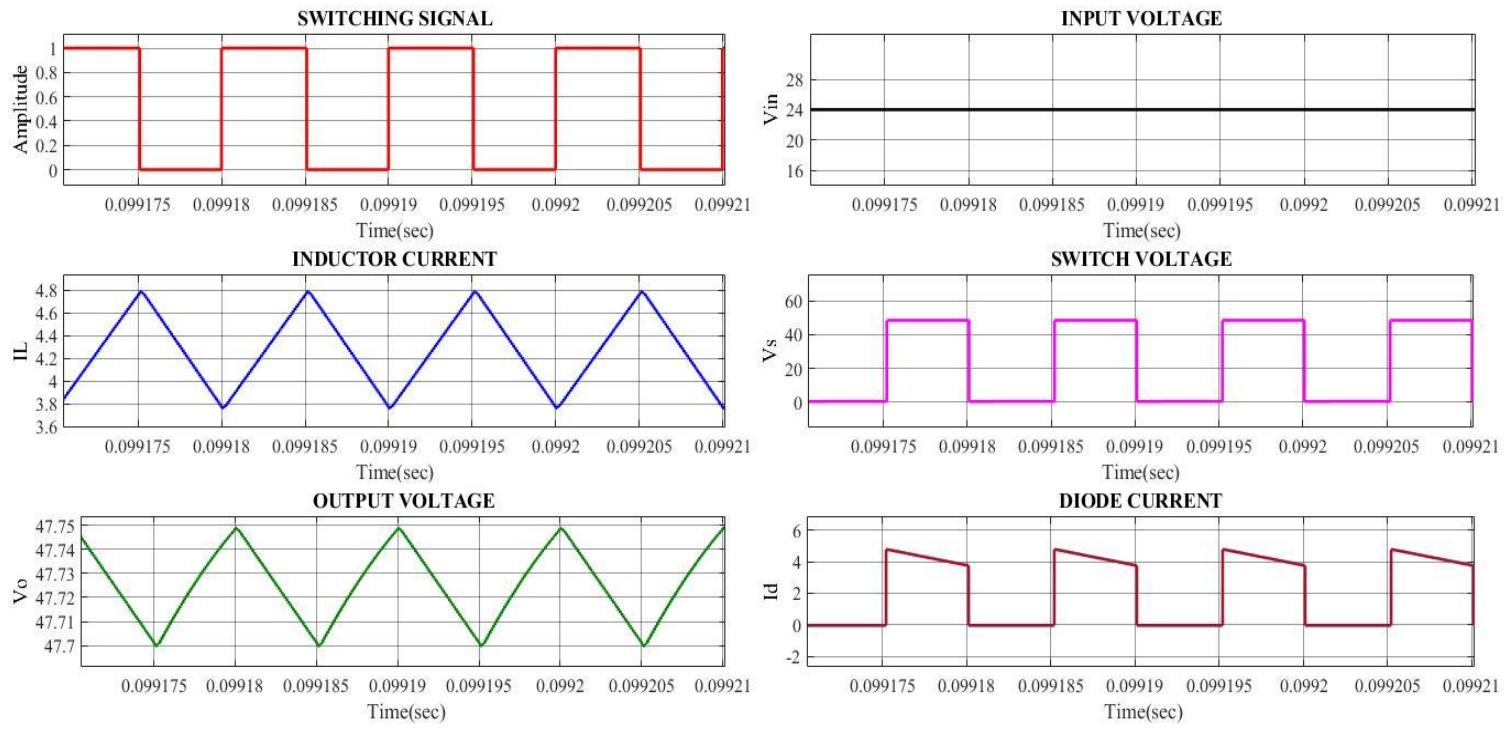


Fig3: Simulation Model of Boost Converter

RESULTS OBTAINED:

Steady State Waveform:



CALCULATIONS AFTER SIMULATION: -

Current through Inductor:

$$I_L(\text{max}) = 4.8 \text{ A}, I_L(\text{min}) = 3.78 \text{ A},$$

$$\text{Avg. Current through Inductor } I_L = (4.80 + 3.78)/2 = 4.29 \text{ A}$$

Ripple in Inductor Current,

$$\Delta I_L(\text{PEAK - PEAK}) = 4.8 - 3.78 = \mathbf{1.02 \text{ A (23.77\% of } I_L)}$$

Output voltage:

$$V_{o(\text{max})} = 47.749 \text{ V}, \quad V_{o(\text{min})} = 47.7 \text{ V}$$

$$\text{Avg. Output Voltage} = (47.7 + 47.749)/2 = \mathbf{47.7245 \text{ V}}$$

Ripple in Output Voltage

$$\Delta V_{o(\text{peak-peak})} = \mathbf{0.049 \text{ V (0.107\% of } V_o)}$$

CONCLUSION:

1. Ripple in inductor Current – 23.77% of I_L
2. Ripple in output voltage – 0.107% of V_o

Submitted by – **Shivraj Vishwakarma,**
M. Tech. 1st Year,
Power Engg.
Roll No. 224102112

EXPERIMENT No. 1

SIMULATION OF BUCK CONVERTER USING MATLAB/SIMULINK

Objective: To Design & Simulate a Buck Converter circuit using MATLAB/SIMULINK.

Parameters of the Buck Converter: Input voltage: **24V**; Output Voltage: **12V**; Output Power: **100W**; Switching Frequency: **100kHz**; Ripple in Inductor Current: **25%**; Ripple in Output Voltage: **0.1%**;

Theory: -

A buck converter is a step-down DC to DC converter. For a DC-DC converter, input and output voltages are both DC. It uses a power semiconductor device as a switch to turn on and off the DC supply to the load. The switching action can be implemented by a BJT, a MOSFET, or an IGBT. Figure 1 shows a simplified block diagram of a buck converter that accepts a DC input and uses pulse-width modulation (PWM) of switching frequency to control the switch. An external diode, together with external inductor and output capacitor, produces the regulated dc output. Buck, or step-down converters produce an average output voltage lower than the input source voltage.

The output voltage can be represented as, $V_o = D * V_{in}$

Where D is duty ratio defined as

$$D = \frac{V_o}{V_{in}} = \frac{T_{on}}{(T_{on} + T_{off})}$$

CCM and DCM:

The buck converter can operate in two different modes; continuous conduction mode (CCM) and discontinuous conduction mode (DCM). The difference between the two is that in CCM the current in the inductor does not fall to zero. A buck converter operates in continuous mode if the current through the inductor never falls to zero during the commutation cycle. In DCM, the current through the inductor falls to zero during part of the period. Practically, converter can operate in either operation modes. Figure 2 shows CCM and DCM mode.

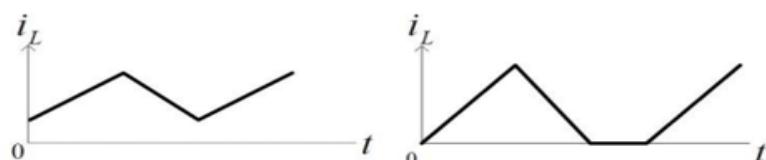
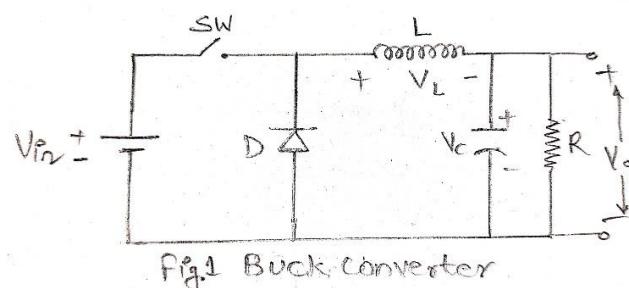


Figure: CCM & DCM Mode

CIRCUIT DIAGRAM & WAVEFORMS: -



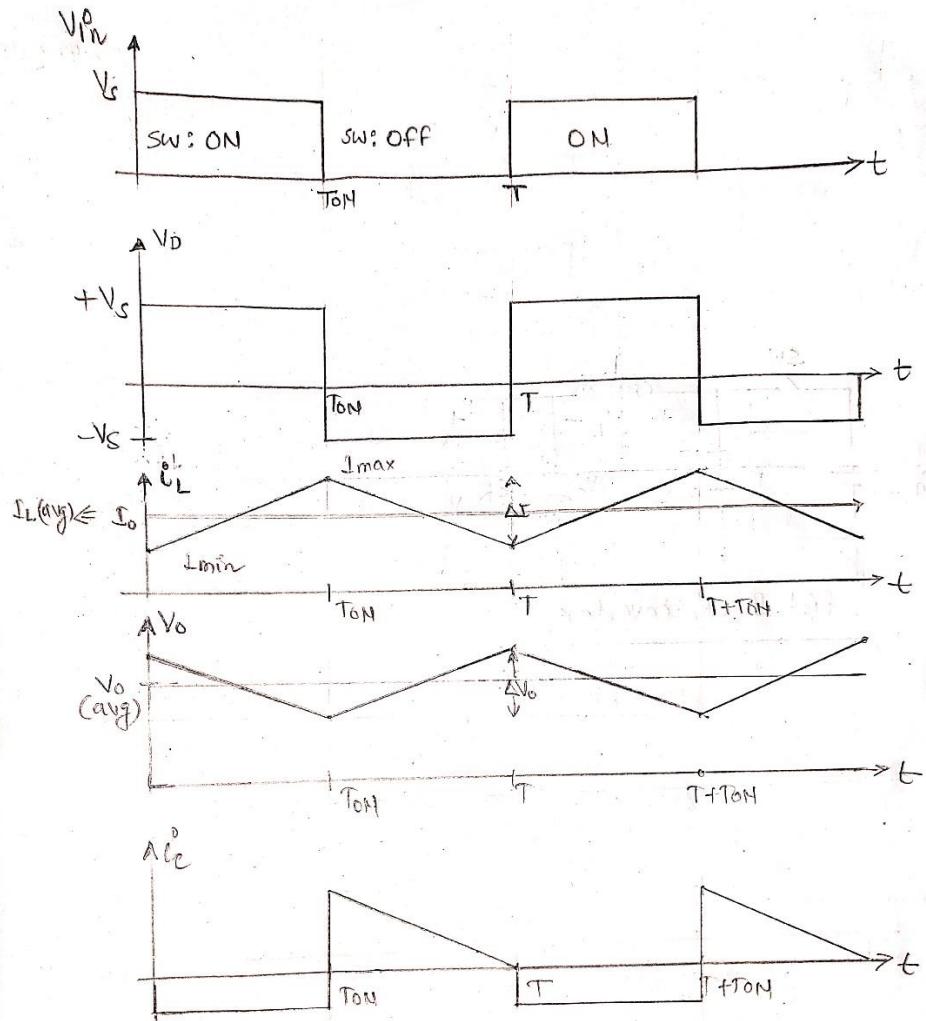
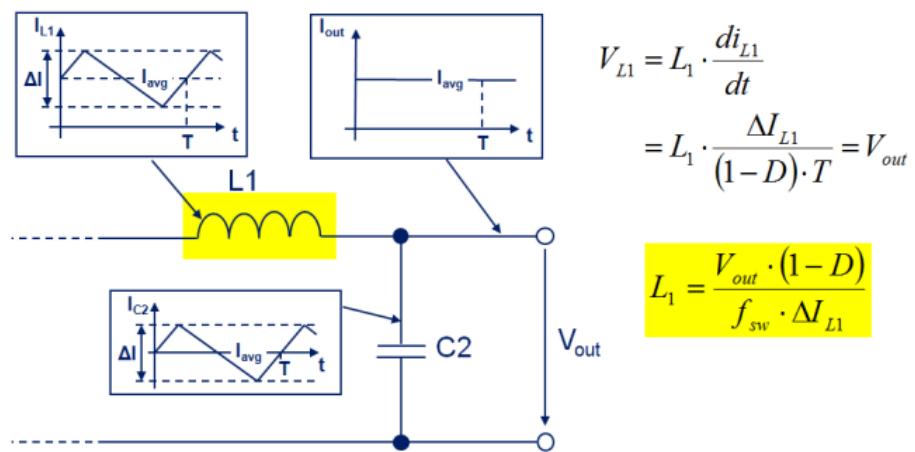


fig:- Waveforms of Buck converter.

Designing Parameters for Buck Converter:

Since in OFF state of switch Voltage across inductor = output voltage, so using this concept we can calculate the inductance of Inductor required.



Similarly, we can calculate value of capacitance also, so finally these formulas can be used to calculate the value of Inductance & Capacitance required.

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{\Delta I_L \times f_s \times V_{in}}$$

Where

V_{in} = typical input voltage

V_{out} = desired output voltage

f_s = minimum switching frequency of the converter

ΔI_L = estimated inductor ripple current

$$C = \frac{\Delta I_L}{8 \times f_s \times \Delta V_{out}}$$

Where

ΔI_L = estimated inductor ripple current

f_s = minimum switching frequency of the converter

ΔV_{out} = desired output voltage ripple

CALCULATIONS: -

Using given values as $V_o = 12$ V, $f_{sh} = 100$ kHz, $P_o = 100$ W, $\Delta I_o = 25\%$, $\Delta V_o = .1\%$

1. Duty Ratio

$$D = 0.5$$

2. INDUCTANCE-

Ripple in Inductor Current

$$\Delta I_L = 25/12$$

$$= 2.08 \text{ A} \text{ & hence,}$$

$$L = 28.8 \mu\text{H}$$

3. CAPACITANCE

Ripple in Output Voltage

$$\Delta V_o = 1\% \text{ of } 12 \text{ V} = 0.012 \text{ V} \text{ & hence } C = 217 \mu\text{F}$$

4. Load Resistance,

$$R_L \text{ (Fixed)} = V_o/I_{DC} = 1.44 \Omega$$

5. Avg output Current,

$$I_{DC} = P/V_o$$

$$I_{DC} = 8.33 \text{ Amp.}$$

SIMULATION CIRCUIT:

Considering diode $V_f = 0.8$ V, & solver configuration as-



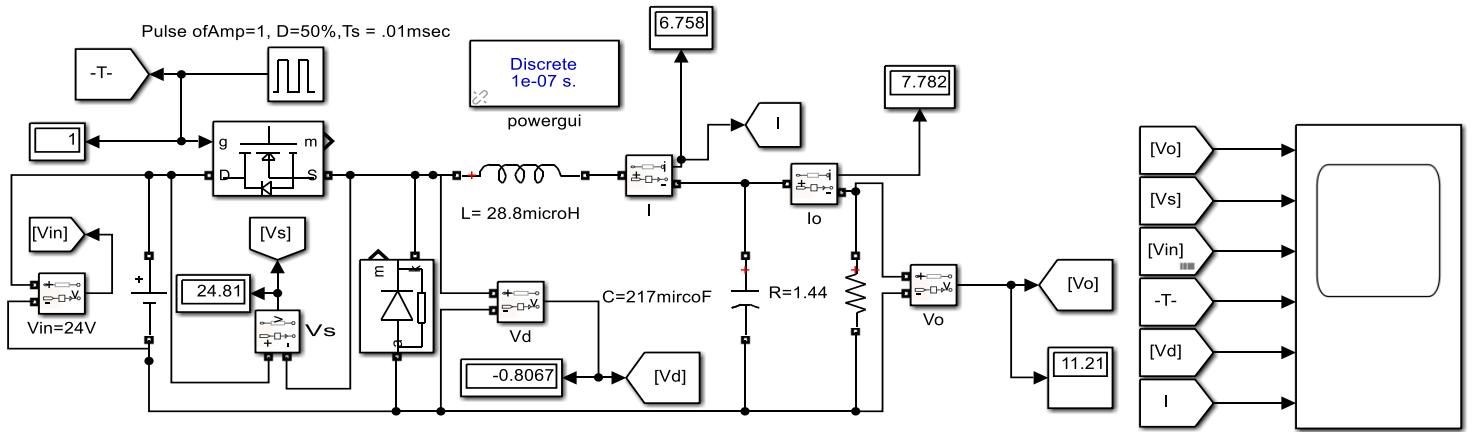
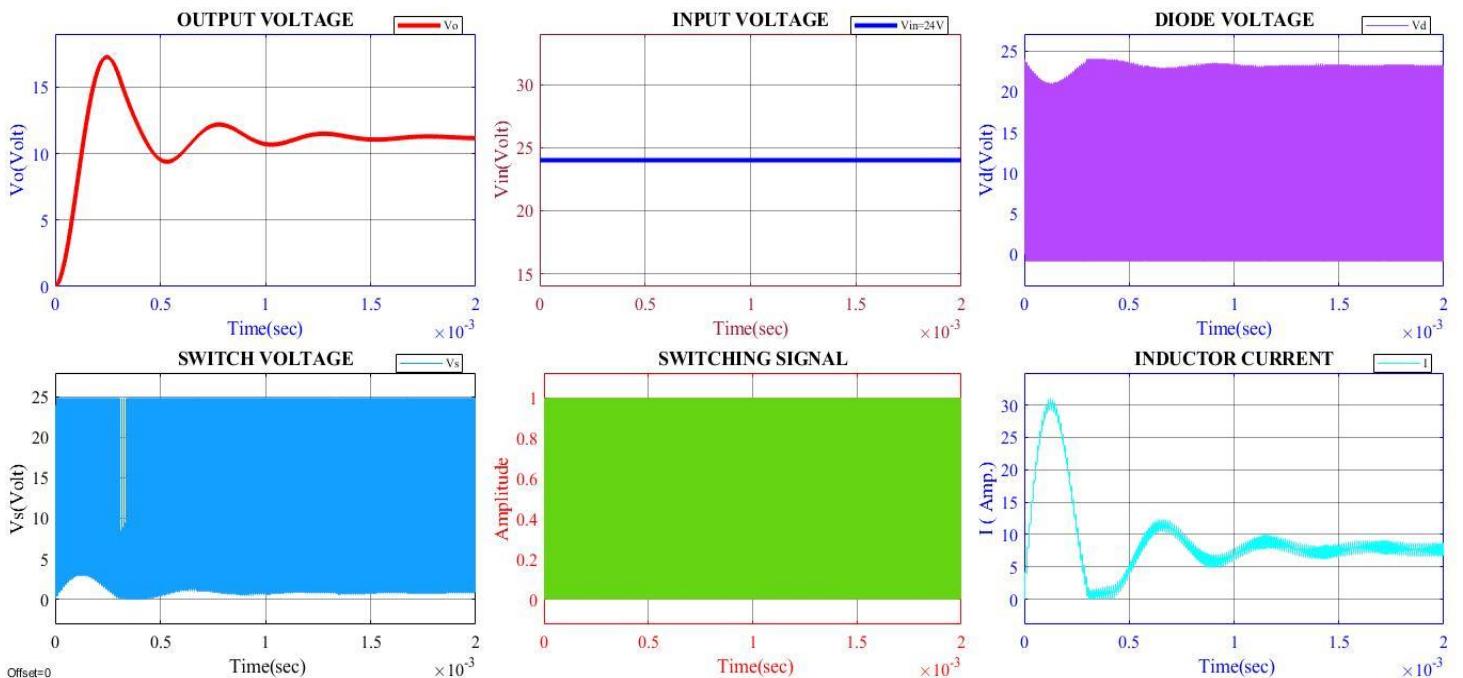


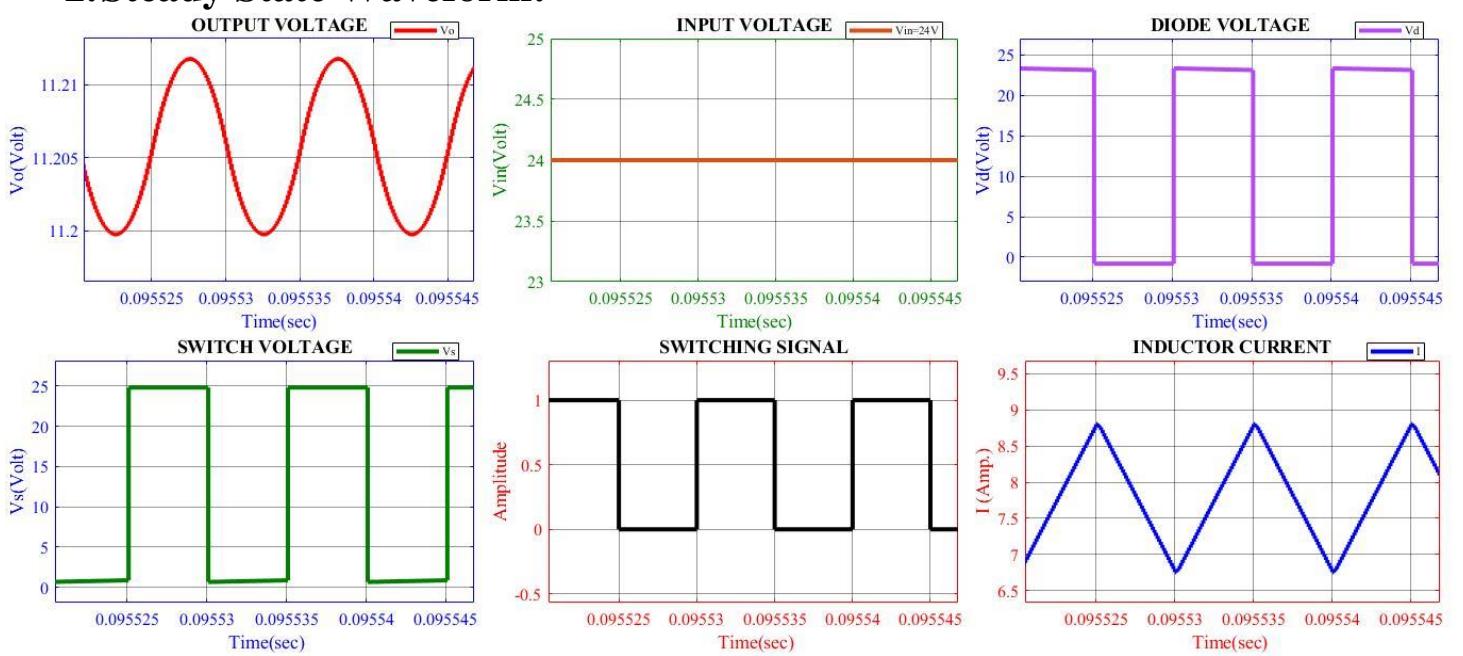
Fig3: Simulation Model of Buck Converter

RESULTS OBTAINED:

1. Transient State Waveform:



2. Steady State Waveform:



CALCULATIONS AFTER SIMULATION: -

Current through Inductor:

Avg Current through Inductor

$$I_{DC} = 6.758 \text{ Amp.}$$

Ripple in Inductor Current,

$$I_{L(max)} = 8.80 \text{ A,}$$

$$I_{L(min)} = 6.75 \text{ A,}$$

$$\Delta I_{L(PEAK-PEAK)} = 8.8 - 6.75$$

$$= 2.05 \text{ A (26.34% of } I_o)$$

Output voltage:

Avg. Output Voltage = **11.21 V**

Ripple in Output Voltage

$$V_{o(max)} = 11.2116 \text{ V,}$$

$$V_{o(min)} = 11.1996 \text{ V}$$

$$\Delta V_{o(peak-peak)} = 0.012 \text{ V (0.107% of } V_o)$$

6. **Average Output Current $I_o = 7.782 \text{ A}$**

7. **Average Output Power $P_o = 87.24 \text{ W}$**

CONCLUSION:

1. Ripple in inductor Current – **26.4% of I_o**
2. Ripple in output voltage – **0.107% of V_o**

*Submitted by – Shivraj Vishwakarma,
M. Tech. 1st Year,
Power Engg.
Roll No. 224102112*
