

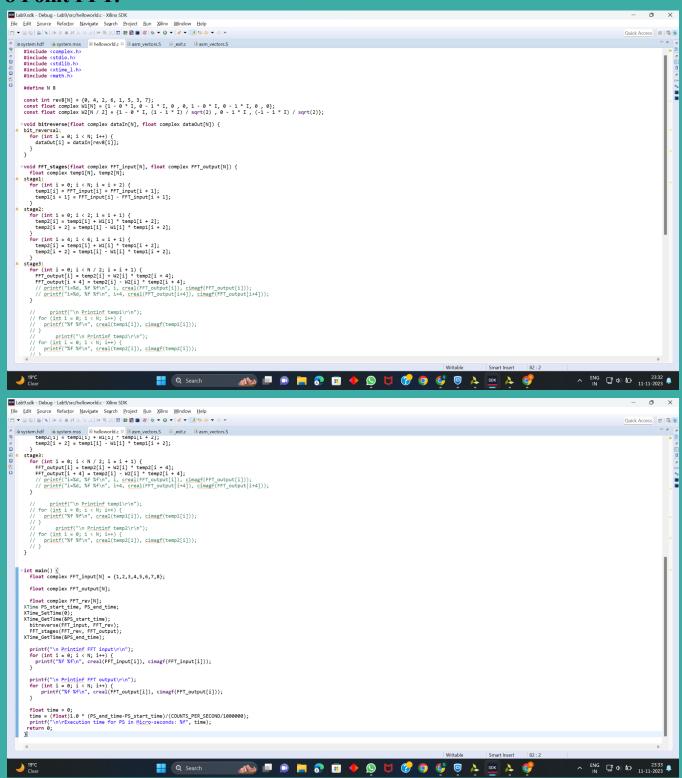
INDRAPRASTHA INSTITUTE *of*INFORMATION TECHNOLOGY DELHI

Department of Electronics & Communication Engineering

Embedded Logic Design Lab 9 Submission

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Source Code



```
■ Lab9.sdk - Debug - Lab9/src/helloworld.c - Xilimx SDK

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       | Bystem.hdf | By
            void bitreverse(float complex dataIn[N], float complex dataOut[N]) {
bit_reversal:
                   for (int i = 0; i < N; i++) {
   dataOut[i] = dataIn[rev16[i]];</pre>
             void FFT_stages(float complex FFT_input[N], float complex FFT_output[N]) {
  float complex temp1[N], temp2[N], temp3[N];
            } stage2: for (int i = 0; i < 2; i = i + 1) {            temp2[i] = temp1[i] + W1[i] * temp1[i + 2];            temp2[i + 2] = temp1[i] - W1[i] * temp1[i + 2];
                 }
for (int i = 4; i < 6; i = i + 1) {
    temp2[i] = temp1[i] + W1[i] * temp1[i + 2];
    temp2[i + 2] = temp1[i] - W1[i] * temp1[i + 2];
}
                 for (int i = 8; i < 10; i = i + 1) {
    temp2[i] = temp1[i] + W1[i] * temp1[i + 2];
    temp2[i + 2] = temp1[i] - W1[i] * temp1[i + 2];
                 }
for (int i = 12; i < 14; i = i + 1) {
    temp2[i] = temp1[i] + W1[i] * temp1[i + 2];
    temp2[i + 2] = temp1[i] - W1[i] * temp1[i + 2];
             }
stage3:
for (int i = 0: i < N / 4: i = i + 1) {
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 Lab9.sdk - Debug - Lab9/src/helloworld.c - Xilinx SDK
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     stem.hdf 🔊 system.mss 🔞 *helloworld.c 🐯 🔊 asm_vectors.S 🗟 _exit.c 🕒 asm_vectors.S
             stage3:
for (int i = 0; i < N / 4; i = i + 1) {
   temp3[i] = temp2[i] + W2[i] * temp2[i + 4];
   temp3[i + 4] = temp2[i] - W2[i] * temp2[i + 4];</pre>
                 }
for (int i = 8; i < 12; i = i + 1) {
    temp3[i] = temp2[i] + W2[i] * temp2[i + 4];
    temp3[i + 4] = temp2[i] - W2[i] * temp2[i + 4];
            } stage4: for (int i = 0; i < N / 2; i = i + 1) { FFT_output[i] = temp3[i] + N3[i] * temp3[i + 8]; FFT_output[i + 8] = temp3[i] - N3[i] * temp3[i + 8]; }
                // printf("\n Printinf templ\n\n");
// for (int i = 0; i < N; i++) {
// printf("% %\n", creal(templ[i]), cimagf(templ[i]));
// printf("\n Printinf temp2\n");
// for (int i = 0; i < N; i++) {
// printf("%f %f\n", creal(temp2[i]), cimagf(temp2[i]));
// printf("%f %f\n", creal(temp2[i]), cimagf(temp2[i]));
                 eint main() {
                 float complex FFT_input[N] = {1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16};
                  float complex FFT_output[N];
                 float complex FFI_rev[N];
XTime PS_start_time, PS_end_time;
XTime_SetTime(0);
XTime_GetTime(0);
Strime_GetTime(0);
bitreverse(FFI_input, FFI_rev);
FFI_stages(FFI_rev, FFI_output);
XTime_GetTime(&PS_end_time);
                  printf("\n Printinf
for (int i = 0; i < N; i++) {
    printf("%f %f\n", creal(FFT_input[i]), cimagf(FFT_input[i]));</pre>
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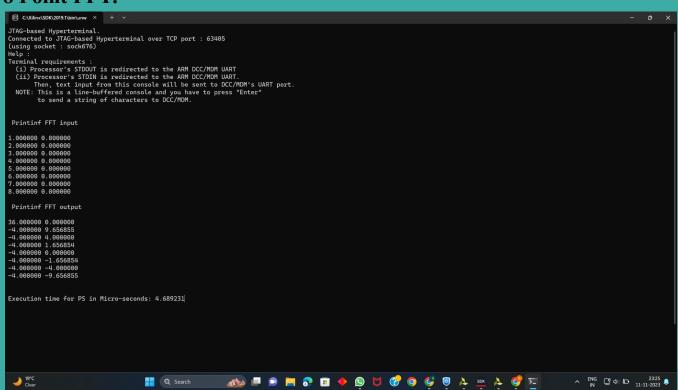
```
Lab9.sdk - Debug - Lab9/src/helloworld.c - Xilinx SDK
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   in system.hdf in system.mss a "helloworld." (in sm. vectors.) (in system.hdf in system.mss a "helloworld." (in sm. vectors.) (in stage4: (in sm. vectors.) (in sm. vectors.)
                ge4:
or (int i = 0; i < N / 2; i = i + 1) {
FFT_output[i] = temp3[i] + W3[i] * temp3[i + 8];
FFT_output[i + 8] = temp3[i] - W3[i] * temp3[i + 8];</pre>
        // printf("\n Printinf templ\n\n");

// for (int i = 0; i < 0; i++) {
    // printf("M fivn', creal(templ[i]), cimagf(templ[i]));

// printf("N fivn', creal(templ[i]), cimagf(templ[i]));

// for (int i = 0; i < 0; i++) {
    // printf("M fivn', creal(templ[i]), cimagf(templ[i]));
    // printf("M fivn', creal(templ[i]), cimagf(templ[i]));
}
            oint main() {
            float complex FFT_input[N] = {1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16};
             float complex FFT_output[N];
             float complex FFT_rev[N];
XTime PS_start_time, PS_end_time;
XTime_SetTime(0);
XTime_GetTime(8PS_start_time);
bitreverse(FFT_input, FFT_rev);
FFT_stages(FFT_rev, FFT_output);
XTime_GetTime(&PS_end_time);
             printf("\n Printinf FFT input\r\n");
for (int i = 0; i < N; i++) {
    printf("%f %f\n", creal(FFT_input[i]), cimagf(FFT_input[i]));</pre>
             printf("\n Printinf FFT output\r\n");
for (int i = 0; i < N; i++) {
    printf("%f %f\n", creal(FFT_output[i]), cimagf(FFT_output[i]));</pre>
                float time = 0;
time = (float)1.0 * (PS_end_time-PS_start_time)/(COUNTS_PER_SECOND/1000000);
printf("\n\rExecution time for PS in Micro-seconds: %f", time);
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jtag terminal output



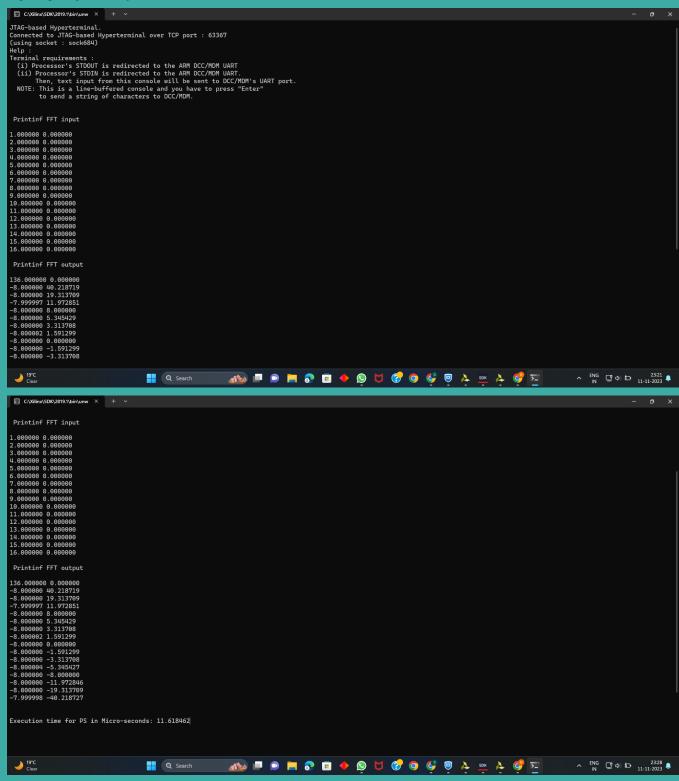
(ZOOMED OUTPUT)

```
C:\Xilinx\SDK\2019.1\bin\unw X
JTAG-based Hyperterminal.
Connected to JTAG-based Hyperterminal over TCP port : 63405
(using socket : sock676)
Help:
Terminal requirements:
  (i) Processor's STDOUT is redirected to the ARM DCC/MDM UART
  (ii) Processor's STDIN is redirected to the ARM DCC/MDM UART.
       Then, text input from this console will be sent to DCC/MDM's UART port.
  NOTE: This is a line-buffered console and you have to press "Enter"
        to send a string of characters to DCC/MDM.
 Printinf FFT input
1.000000 0.000000
2.000000 0.000000
3.000000 0.000000
4.000000 0.000000
5.000000 0.000000
6.000000 0.000000
7.000000 0.000000
8.000000 0.000000
 Printinf FFT output
36.000000 0.000000
-4.000000 9.656855
-4.000000 4.000000
-4.000000 1.656854
-4.000000 0.000000
-4.000000 -1.656854
-4.000000 -4.000000
-4.000000 -9.656855
Execution time for PS in Micro-seconds: 4.689231
```

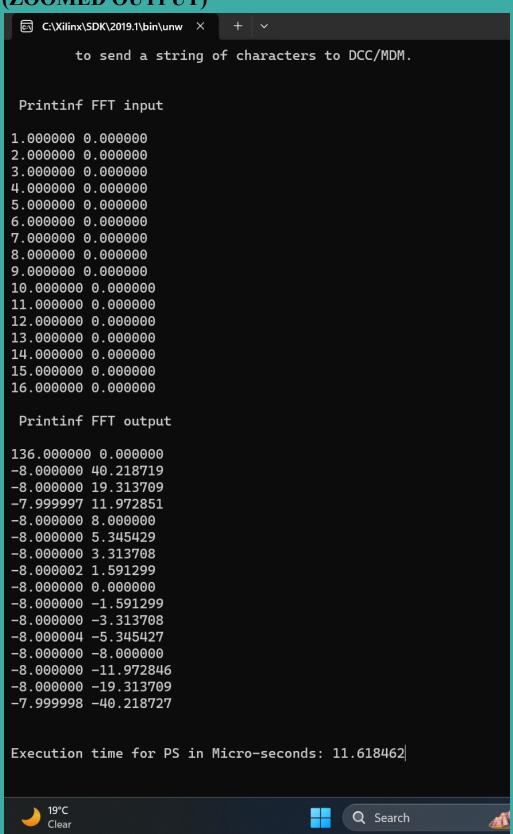
Expected output from Matlab

```
>> x = [1,2,3,4,5,6,7,8];
>> fft(x)
ans =
36.0000 + 0.0000i -4.0000 + 9.6569i -4.0000 + 4.0000i -4.0000 + 1.6569i -4.0000 + 0.0000i -4.0000 - 1.6569i -4.0000 - 4.0000i -4.0000 - 9.6569i
```

jtag terminal output



(ZOOMED OUTPUT)



Expected output from Matlab

```
>> x = [1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16];
>> fft(x)

ans =

1.0e+02 *

Columns 1 through 9

1.3600 + 0.0800 i -0.0800 + 0.4022i -0.0800 + 0.1931i -0.0800 + 0.1197i -0.0800 + 0.0800i -0.0800 + 0.0331i -0.0800 + 0.0159i -0.0800 + 0.0000i

Columns 10 through 16

-0.0800 - 0.0159i -0.0800 - 0.0331i -0.0800 - 0.0535i -0.0800 - 0.0800i -0.0800 - 0.1197i -0.0800 - 0.1931i -0.0800 - 0.4022i
```

Thank You