Dr. Mayank Srivastava

Designation: **Assistant Professor**
Official Address: **Department of Electronics and Communication Engineering**
National Institute of Technology, Jamshedpur
Telephone: **+91-9990289528**
E-mail: **mayank.ece@nitjsr.ac.in**
mayank2780@gmail.com
Residential Address: Flat No. 310, Phase-II, Platina Dream City, Near NIT Hostel, Adityapur, Jamshedpur.

PROFESSIONAL QUALIFICATION

- **Ph.D.-**Electronics & Communication Engineering (Analog Integrated Circuits and Signal Processing)
- **M. Tech-** Engineering Systems (Electronics)
- **B.E. -** Electronics & Communication Engineering

WORK EXPERIENCE

- Currently working as an **Assistant Professor** with Department of Electronics and Communication Engineering, National Institute of Technology, Jamshedpur (Jharkhand) from 11th June 2018 till today

- Worked as a **Professor** with Department of Electronics and Communication Engineering, Krishna Institute of Engineering and Technology (KIET), Ghaziabad (U.P.) from 18th Jul-2016 to 6th June 2018.

- Worked as **Associate Professor** with Department of Electronics and Communication Engineering, The Northcap University (Formerly ITM University), Gurgaon (Haryana) from 15th Jan-2016 to 16-July-2016.

- Worked as **Assistant Professor-II** with Department of Electronics and Communication Engineering, Amity School of Engineering and Technology, Amity University, Noida (U.P.) from 2nd July-2012 to 13th Jan-2016.

- Worked as **Assistant Professor** with Department of Electronics and Communication Engineering, Raj Kumar Goel Institute of Technology (RKGIT), Ghaziabad (U.P.) from 14th August 2008 to 29th June 2012.

PHD GUIDANCE

- Kapil Bharadwaj (2019-ongoing)
- Dheeraj Kalra (2019-ongoing)
- Tapish Verma (2018-ongoing)

RESEARCH INTERESTS

- Modern analog integrated circuits and analog signal generation/processing circuits
- Advance CMOS active building blocks and circuits
- Fractional order circuits
- Memristors and Memristor based circuits
- Optical integrated circuits and Digital logic implementations in all-optical domain

RESEARCH PUBLICATIONS :

International Journal Publications

[1]. Kapil Bharadawaj, **Mayank Srivastava, "**New Electronically adjustable Memelement Emulator for realizing the behaviour of Fully-floating Meminductor and Memristor" *Microelectronics Journal* , vol. 114, 2021. **(SCI Indexed)**

[2]. Dheeraj Kalra, Vishal Goyal , **Mayank Srivastava**, "Design and performance analysis of low power LNA with variable gain current reuse technique" *Analog Integrated Circuits and Signal Processing*, **(SCI Indexed)** (Accepted for Publication, 2021)

[3]. Kapil Bharadawaj, **Mayank Srivastava,** "Floating memristor and inverse memristor emulators with Electronic tuning" *Journal of Circuits, Systems, and Computers* **(SCI Indexed)** (Accepted for Publication, 2021)

[4]. Kapil Bharadawaj, **Mayank Srivastava,** "Mathematical Framework for Three cross-over memristor and its Realization Employing OTAs*" Circuit World* **(SCI Indexed)** (Accepted for Publication, 2021)

[5]. Kapil Bharadawaj, **Mayank Srivastava, "**Emulation of Three Pinch-off Memristor emulator based on highly non-linear charge-flux characteristics" *Radioengineering Journal* vol. 30, no.1, pp. 164-171, 2021. **(SCI Indexed)**

[6]. Kapil Bharadawaj, **Mayank Srivastava,** "Development of mathematical model and circuit emulators for four lobe memristive behavior" *COMPEL-The International Journal For*

Computation And Mathematics In Electrical And Electronic Engineering, vol. 40, no. 1, pp. 51-61, 2020 **(SCI Indexed)**

[7]. Kapil Bharadawaj, **Mayank Srivastava, "**Mathematical Formulation & OTA based Emulator for Three Crossover Memristor" *International Journal of Electronics*, Taylor and Francis **(SCI Indexed)** (Accepted for Publication, 2020)

[8]. Ajay Kumar, Basedeba Behera, Manish Kumar, Sumit Kumar Jindal and **Mayank Srivastava**. "Implementation of All-Optical Ripple Down Counter using the Micro-Ring Resonator Structures" *Applied Physics B*, Springer, vol.127, no.14, 2021 **(SCI Indexed)**

[9]. Kapil Bharadawaj, **Mayank Srivastava**, "Floating Memristor and Inverse Memristor Emulation configurations with Electronic/Resistance controllability,' *IET Circuits Devices and Systems*, vol. 14, no. 7, pp-1065-1076, 2020 (**SCI Indexed)**

[10]. **Mayank Srivastava**, "New grounded series/parallel lossy inductor simulators with electronic control" *Journal of Engineering Research*, vol. 6, no.1, pp.118-135, 2018. **(SCI Indexed)**

[11]. Dinesh Prasad, Javed Ahmad and **Mayank Srivastava**, "A Novel Grounded To Floating Admittance Converter With Electronic Control" *Indian Journal of Physics*, vol. 92, no. 1, pp. 49-56, 2017 Springer (**SCI Indexed**)

[12]. Dinesh Prasad, D. R. Bhaskar**, Mayank Srivastava,** "Universal voltage-mode biquad filter using voltage differencing transconductance amplifier" *Indian Journal of Pure & Applied Physics*, vol. 51, no. 12, pp. 864-868, 2013. (**SCI Indexed)**

[13]. **Mayank Srivastava** and Kapil Bharadwaj, "Compact lossy inductance simulator with electronic control" *Iranian Journal of Electrical and Electronics Engineering*, vol. 15, no. 3, pp-343-351, 2019 **(Scopus Indexed).**

[14]. **Mayank Srivastava**, "New Synthetic Grounded FDNR with Electronic Controllability Employing Cascaded VDCCs and Grounded Passive Elements" *Journal of Telecommunication, Electronics and Computer Engineering*, vol. 9, no. 4, pp. 97-102, 2017. **(Scopus Indexed)**

[15]. **Mayank Srivastava** and Dinesh Prasad, "Minimum Component Electronically Tunable Simulator for Grounded Inductor with Low Parasitic Effects" *Journal of Telecommunication, Electronics and Computer Engineering*, vol.9, no. 2, pp. 41-46, 2017. **(Scopus Indexed)**

[16]. **Mayank Srivastava** and Dinesh Prasad "VDCC based dual-mode quadrature sinusoidal oscillator with current/voltage outputs at appropriate impedance level" *Advances in Electrical and Electronics Engineering*, vol. 14, no. 2, pp. 168-177, 2016. **(Scopus Indexed)**

[17]. **Mayank Srivastava**, Dinesh Prasad and D. R. Bhaskar, "New electronically tunable grounded inductor simulator employing single VDTA and one grounded capacitor" *Journal of Engineering Science and Technology*, vol. 12, no. 1, pp. 113-126, 2017 **(Scopus Indexed)**

[18]. **Mayank Srivastava** and Dinesh Prasad "VDTA Based Electronically Tunable Purely Active Simulator Circuit for Realizing Floating Resistance" *Journal of Engineering Science and Technology Review*, vol. 8, no.3, pp. 112-116, 2015. **(Scopus Indexed)**

[19]. **Mayank Srivastava**, Dinesh Prasad "A Novel Purely Active Electronically Controllable Configuration for Simulating Resistance in Floating Form" *Electronics Journal* vol. 20, no.2, pp. 90-94, 2016 **(Scopus Indexed)**

[20]. **Mayank Srivastava**, Dinesh Prasad and D. R. Bhaskar, "Voltage mode quadrature oscillator employing single VDTA and grounded capacitors" *Contemporary Engineering Sciences*, vol. 27, no. 7, pp. 1501-1507, 2014. **(Scopus Indexed)**

[21]. Dinesh Prasad, D. R. Bhaskar and **Mayank Srivastava, "**New single VDCC-based explicit current-mode SRCO employing all grounded passive components" *Electronics Journal (Banja luka)* vol.18, no.2, pp. 81-88, 2014 **(Scopus Indexed)**

[22]. Ghamshyam Singh, Dinesh Prasad, D. R. Bhaskar and **Mayank Srivastava "**A VDVTA-Based novel configuration for realizing grounded inductance" *Advances in Intelligent Systems and Computing – Springer*, vol.479, pp. 243-250, 2016. **(Scopus Indexed)**

[23]. Dinesh Prasad, Kuldeep Panwar, D. R. Bhaskar and **Mayank Srivastava**, "CDDITA-based voltage mode first order all-pass configuration" *Circuits and Systems (USA)*, vol. 6, no.11, pp. 252-256, 2015.

[24]. Dinesh Prasad, **Mayank Srivastava** and D. R. Bhaskar, "Transadmittance-type universal current-mode biquad filter using VDTAs," *ISRN* (*Hindawi*, USA), vol. 2014, Article id. 762845, 4 pages, 2014.

[25]. Dinesh Prasad, **Mayank Srivastava**, D. R. Bhaskar 'Electronically controllable fully uncoupled explicit current mode Quadrature oscillator using VDTAs and grounded capacitors' *Circuits and Systems (USA)*, vol. 04, no. 02, pp. 169-172, 2013. **(ISI-Thomson Reuters web of Knowledge)**

[26]. Dinesh Prasad, D. R. Bhaskar and **Mayank Srivastava**, 'Universal current mode biquad filter using a VDTA' *Circuits and Systems (USA)*, vol. 04, no. 01, pp. 29-33, 2013. **(ISI Thomson Reuters web of Knowledge)**

International Conference Publications

[1]. Priyanka Joshi, Kapil Bhardwaj and **Mayank Srivastava**, "Noval single CDDITA based resistively tunable all-pass filter configuration with grounded passive elements" Springer International conference on Distributed Computing and Optimizing Techniques 2021 (ICDCOT-2021), 26-27, June 2021 (Accepted and Presented) **(Scopus Indexed)**

[2]. Rampriya Kumar, Anand Kumar, Kapil Bhardwaj, **Mayank Srivastava**, "Active realization of series inductor and shunt capacitor and their applications in electronically tunable wave active filters" *5th IEEE International Conference on Computing Methodologies and Communication (ICCMC)*, pp.683-688, April 8-10, 2021. **(Scopus Indexed)**

[3]. Hari Prakash Reddy Allipur, Naga Pavan Surya Sai Charan Randhi, **Mayank Srivastava**, New Active-Only Impedance Multiplier using VDBAs, 2020, Springer Electric Power and Renewable Energy Conference-2020 (EPRC-2020), NIT Jamshedpur, May 29-30, 2020 (Published in Springer Lecture Notes in Electrical Engineering) **(Scopus Indexed)**

[4]. Venkata Mohit Sai Chandra Kukunoori, Vinay Reddy Godhala, **Mayank Srivastava**, Grounded FDNR Simulation Circuit using VDDIBAs, 2020, Springer Electric Power and Renewable Energy Conference-2020 (EPRC-2020), NIT Jamshedpur, May 29-30, 2020 (Accepted and Presented) **(Scopus Indexed)**

[5]. Venkata Sai Krishna Dheeraj Kotla, Reddaiah Reddy Alluru and **Mayank Srivastava**, A New Electronically Controllable Active R-L Network Simulator, 2020, Springer Electric Power and Renewable Energy Conference-2020 (EPRC-2020), NIT Jamshedpur, May 29-30, 2020 (Accepted and Presented. **(Scopus Indexed)**

[6]. Dheeraj Kalra, **Mayank Srivastava**, Vishal Goyal, Manish Kumar "Realization of Low Noise Amplifier for 1-5 GHz Using Noise Cancellation Technique" IEEE 2020 International Conference on Power Electronics & IoT Applications in Renewable Energy and its Control (PARC), pp. 472-474, Feb 28-29, 2020 **(Scopus Indexed)**

[7]. Kapil Bhardwaj, **Mayank Srivastava**, New resistorless FDNR simulation configuration employing CDDITAs, 2019, Springer 2nd International conference on VLSI, Communication and Signal Processing, pp. 561-570, 21-23 0ct 2019 **(Scopus Indexed)**

[8]. Kapil Bhardwaj, **Mayank Srivastava**, Novel CDDITA based grounded inductance simulation circuits, 2019, Springer 2nd International conference on VLSI, Communication and Signal Processing, pp. 571-582, 21-23 0ct 2019 **(Scopus Indexed)**

[9]. Kapil Bhardwaj, **Mayank Srivastava**, New FDNR and FDNC Simulation Configurations using Inverted VDDIBAs 2019, Springer 2nd International conference on VLSI, Communication and Signal Processing, pp. 583-594, 21-23 0ct 2019. **(Scopus Indexed)**

[10]. P.A. Khanam, **Mayank Srivastava**, Minimum Component Grounded Inductor Simulator employing CDDITA, 2019, 2019 3rd IEEE International Conference on Recent Developments in Control Automation & Power Engineering(RDCAPE), pp. 580-584, 10-11 Oct 2019 (**Scopus Indexed)**

[11]. Prashant Shekhar, Ajay Kumar, Amir Ahmad, **Mayank Srivastava**, All Optical OR/NOR logic gate using the Micro-ring Resonator based Switching Activity, 2019 IEEE International Conference on Electrical, Electronics and Computer Engineering (UPCON), pp. 1-4, 8-10 Nov 2019 (**Scopus Indexed)**

[12]. **Mayank Srivastava** and Kapil Bharadwaj, Ajay Roy and Anant Singh, "An Active-C Realization for Simulating Electronically Controllable Lossy Grounded Inductance" Sixth IEEE International Conference on Signal Processing & Integrated Networks" (IEEE-SPIN 2019), pp. 137-140, Noida, 2019. (**Scopus Indexed)**

[13]. Kuldeep Panwar, Kapil Bhardwaj, **Mayank Srivastava** and Dinesh Prasad, "Grounded Parallel R-L impedance Simulator using CDDITA" 2nd IEEE International Conference EII-2019, Noida. (**Scopus Indexed)**

[14]. Kapil Bhardwaj Dinesh Prasad, **Mayank Srivastava**, Ajay Roy and Kuldeep Panwar, "Grounded Series R-L impedance Simulator using CDDITA" IEEE 5th International conference on signal processing and communication, JIIT, Noida, 07-09 March 2019, pp. 254-257, 2019 (**Scopus Indexed)**

[15]. Kogara Naveen Kumar, Pappala Chandra Mouli, Suraj Das K, and **Mayank Srivastav**a, "New Electronic/Resistance Adjustable Capacitance Multiplier Circuit with VDCCs and Grounded Resistances"), pp. 1112- Sixth IEEE International Conference on Signal Processing & Integrated Networks"-2019 (IEEE-SPIN 20191115, Noida, 2019. (**Scopus Indexed)**

[16]. Suraj Das K, Kogara Naveen Kumar, Pappala Chandra Mouli and **Mayank Srivastava**, "New Generalized grounded impedance scaling configuration with Electronic/resister tunability " Sixth IEEE International Conference on Signal Processing & Integrated Networks" (IEEE-SPIN 2019), pp. 811-815, Noida, 2019. (**Scopus Indexed)**

[17]. Pranjal Gupta, **Mayank Srivastava**, Aishwarya Verma, Ayushi Singh, Devyansi, Arshi Ali, "'A VDCC based Grounded Passive Element Simulator/Scaling Configuration with Electronic Control" Springer International Conference ICSC-2018, Noida, pp. 429-441, 2019. (**Scopus Indexed)**

[18]. **Mayank Srivastava**, Pranjal Gupta, Ajay Roy, Manish Kumar Verma and Dinesh Prasad, "New Electronically Tunable Floating FDNR Configuration employing Grounded Capacitances" Fifth IEEE International Conference on Signal Processing & Integrated Networks" (IEEE-SPIN 2018), pp. 754-759, 2018. (**Scopus Indexed)**

[19]. Pranjal Gupta, **Mayank Srivastava**, Ajay Roy, Manish Kumar Verma and Dinesh Prasad, "A Generalized Grounded Impedance Simulator/Grounded Impedance Scaling Circuit with Electronic Tuning" Fifth IEEE International Conference on Signal Processing & Integrated Networks" (IEEE-SPIN 2018), pp. 771-776, 2018. (**Scopus Indexed)**

[20]. Pranjal Gupta and **Mayank Srivastava** "New Frequency Dependence Negative Conductance Simulator employing VDTAs and Grounded Capacitances" Fifth IEEE International Conference on Signal Processing & Integrated Networks" (IEEE- SPIN 2018), pp. 275-279, 2018. (**Scopus Indexed)**

[21]. Dinesh Prasad, **Mayank Srivastava** and Javed Ahmad, "New CM/VM 3rd-Order Quadrature Oscillator using VDCCs' *IEEE International Conference on Applied System Innovation*, **Sapporo (Japan)**, pp. 1328-1332, May 2017. (**Scopus Indexed)**

[22]. Dinesh Prasad, Shalu Singh Anupma Kumari, and **Mayank Srivastava,** "A Novel digitally programmable quadratur oscillator using VDGA" *IEEE International Conference on Applied System Innovation*, **Sapporo (Japan)**, pp. 1391-1394, May 2017. (**Scopus Indexed)**

[23]. Dinesh Prasad, **Mayank Srivastva**, Laxya, Farah Jabin, Ghania Fatima, Sif Ali Khan, Safa Tanzeem, "'Novel Active PID Controller Employing VDTA" *2016 IEEE International Symposium on Signal Processing and Information Technology (ISSPIT),* pp. 1-4, 2016, **Limassol** (**Cyprus)**. (**Scopus Indexed)**

[24]. Dinesh Prasad and **Mayank Srivastva**, "Novel Ms^2-Type FDNR Simulation Configuration with Electronic Control and Grounded Capacitances" *24th IEEE International Conference on Electronics Circuits and Systems-2017 (ICECS-2017)*pp. 1-5, 2018. (**Scopus Indexed)**

[25]. **Mayank Srivastava**, Ajay Roy and Ramendra Singh, "New VDCC based Electronically Tunable Grounded Frequency Dependent Negative Resistance Simulator employing grounded passive elements"*IEEE-RDCAPE-2017*, Noida, India (**Scopus Indexed)**

[26]. **Mayank Srivastava**, Ajay Roy and Ramendra Singh, "New Grounded FDNR Simulation Circuit with Electronic Control*" IEEE-TELNET-2017*, Noida, India (**Scopus Indexed)**

[27]. **Mayank Srivastava,** Dinesh Prasad and Ajay Roy, "A Purely Active Circuit Simulator for Realizing Electronically Tunable Floating Resistance" *Springer International Conference ICICCD-2017,* AISC book series, volume 624, pp. Dehradun, India (**Scopus Indexed)**

[28]. **Mayank Srivastava**, Dinesh Prasad, Laxya and Avadesh Kumar Shukla, "Novel active circuit for realizing variable grounded passive elements" *IEEE International conference on Microelectronics and Telecommunication Engineering-2016*, pp. 559-563, 22-23 Sept 2016, Ghaziabad, India. (**Scopus Indexed)**

[29]. **Mayank Srivastava**, Dinesh Prasad, Laxya and Ghanshyam Singh, "A new simulator for realizing floating Resistance/ capacitance with electronic control" *IEEE International conference on Microelectronics and Telecommunication Engineering-2016,* pp. 663-666, 22-23 Sept 2016, Ghaziabad, India. (**Scopus Indexed)**

[30]. **Mayank Srivastava,** P. Bhanja and S. F. Mir, "A new configuration for simulating passive elements in floating state employing VDCCs and grounded passive elements" *IEEE-International Conference on Power Electronics, Intelligent Control and Energy Systems (ICPEICES- 2016),* , pp. 13-18, Delhi, India, 2016. (**Scopus Indexed)**

[31]. **Mayank Srivastava,** D. Prasad, D. R. Bhaskar, "New Parallel R-L impedance using single VDTA & its high pass filter applications" *IEEE-International Conference on Signal Processing and Integrated Networks-SPIN-2014*, pp. 535-537, Noida, India, 2014. (**Scopus Indexed)**

[32]. Dinesh Prasad, **Mayank Srivastav**a Anwar Ahmad, Apritam Mukhopadhyay, Bharat Bhushan Sharma "Novel VDCC Based Low-Pass and High-Pass Ladder Filters" *IEEE-INDICON-2015*, pp.1-4, 2015, New Delhi, India. (**Scopus Indexed)**

[33]. **Mayank Srivastava,** D. Prasad, Gaurav Chitranshi, Prateek Sengar, Mamta "Novel Electronically tunable current-mode integrator employing VDTA" *IEEE-INDICON-2015*, pp.1-4, 2015, New Delhi, India. (**Scopus Indexed)**

[34]. **Mayank Srivastava,** Shubhi Srivastava, Kavya Goel, Swati Joshi and D. Prasad,"New active only scaling configuration for grounded passive elements with electronic scaling facility" pp. 502-506, *IEEE-SPIN-2016*, Noida, India. (**Scopus Indexed)**

[35]. **Mayank Srivastava**, Swati Joshi, Shubhi Srivastava, Kavya Goel and Dinesh Prasad, Jai Kumar, Girish Parmar "New electronically controllable grounded to floating admittance convertor with no requirement of passive elements" *IEEE International Conference on Computing, Communication and Automation (ICCCA)-2016*, pp.955-959, 2016, Gr. Noida, India. (**Scopus Indexed)**

[36]. Dinesh Prasad, Javed Ahmad, **Mayank Srivastava,** Laxya "New VDVTA Based Electronically Tunable Floating Inductor Simulator" *IEEE International Conference on Power Electronic, Intelligent Control and Energy Systems*, pp. 1-5, 2016, Delhi, India. (**Scopus Indexed)**

[37]. Deepak Garg, Vijay kumar Tayal, **Mayank Srivastava**, "Digital Simulation of Limit Cycle Parameters in Analog Filters" *International Conference on Future Trends in Information and Communication Technologies-2011,* Ghaziabad, India, pp. 186-191, 2011.

[38]. Nitin Kumar, Sandeep Bhatia, **Mayank Srivastava**, "Study and Design of Microstrip Reflectarray Antenna fof Direct to Home(DTH)" *International Conference on Future Trends in Information and Communication Technologies-2011,* Ghaziabad, India, pp. 144-147, 2011.

[39]. Shekhar Pundir, **Mayank Srivastava**, Harish Parthasarthy, M.P. Tripathi, "An Optimized Numerical Technique for Current Distribution of Dipole Antenna: Matlab Approach" *International Conference on Future Trends in Information and Communication Technologies*-2011, Ghaziabad, pp. 262-267, 2011, Ghaziabad, India.

[40]. Vaibhav Sharma, Sharmila Verma , **Mayank Srivastava**, "Recent Activities and Future Trends of Microwave Wireless Power Transmission" *Symposium Cum Exposition on Vacuum Electron Devices and Applications-2011* (Organized by VEDA society IT-BHU, Varanasi), pp. 61-66, 2011.

[41]. Ramendra Singh, **Mayank Srivastava**, Vaibhav Sharma, Riju Jindal, Sharmila Verma, 'Effect of Defected Ground Structures on Microwave Circuits' *Symposium Cum Exposition on Vacuum Electron Devices and Applications-2011* (Organized by VEDA society IT-BHU, Varanasi), pp. 80-81, 2011.

CITATION EARNED

Citations: 435 (Google scholar) **H Index: 10**
i10 Index: 12

REVIEWER (INTERNATIONAL JOURNALS)

- IEEE Sensor Letters
- Wireless Personal Communication, Springer (SCI Indexed)
- Optik, Elsevier (SCI Indexed)
- Sadhana Journal, Springer (SCI Indexed)
- IET Electronics Letters (SCI Indexed)
- AEU-International Journal of Electronics and Communication, Elsevier, (SCI Indexed)
- Physical Communication, Elsevier, (SCI Indexed)
- MAPAN Journal of Metrology Society, Springer (SCI Indexed)
- Chaos, Solitons & Fractals, Elsevier (SCI Indexed)
- Engineering Science and Technology, Elsevier (SCI Indexed)
- Indian Journal of Pure & Applied Physics (SCI Indexed)
- Journal of Electrical and Computer Engineering, Hindawi, USA (Scopus Indexed)
- Journal of Optical Communication, De-Gruyter, Germany (Scopus Indexed)
- Advances in Electrical and Electronics Engineering, Slovakia (Scopus Indexed)
- Circuits and Systems, Scientific Research, USA

EDITOR (INTERNATIONAL JOURNALS)

- International Journal of Telecommunications and Emerging Technologies
- International Journal of Digital Communication and Analog Signals

REVIEWER / MEMBER TECHNICAL PROGRAMME COMMITTEE (INTERNATIONAL CONFERENCES)

- International Conference on Artificial Intelligence:Theory and Applications (AITA2021), 23-24 December 2021, NIT Patna
- 2021 4th IEEE International Conference on Recent Developments in Control Automation & Power Engineering(RDCAPE), 07-08 Oct 2021, Amity University, Noida (U.P.)
- Springer International Conference on Smart Energy and Advancement in Power Technologies 2021 (ICSEAPT-2021), 06-08 September 2021, NIT Jamshedpur.
- Springer Electric Power and Renewable Energy Conference-2021 (EPRC-2021), May 28-30, 2021, NIT Jamshedpur
- Springer Emerging Trends and Technologies on Intelligent Systems Conference 2021 (ETTIS-2021) , 04-05 March 2021, CDAC Noida
- Springer First International Conference on Advances in Recent Advances in Information, Computation and Energy Systems 2021 (ICICES 2021), 08 - 09, January 2021, Ongole, Andhra Pradesh, India.
- Springer Electric Power and Renewable Energy Conference-2020 (EPRC-2020), May 29-30, 2021, NIT Jamshedpur.
- IEE International Conference On Contemporary Computing And Applications-ICA3 2020 05-07 Feb 2020, AKTU Lucknow
- 2019 3rd IEEE International Conference on Recent Developments in Control Automation & Power Engineering(RDCAPE), 10-11 Oct 2019, Noida (U.P.)
- Sixth IEEE International Conference on Signal Processing & Integrated Networks"-2019 (IEEE-SPIN 2019), 07-08 March 2019, Noida (U.P.)
- IEEE International Conference on Computing, Communication, and Intelligent Systems (ICCCIS-2019) Oct 18-19, 2019, Greater Noida (U.P.)
- 6th IEEE Uttar Pradesh Section International Conference On Electrical, Electronics And Computer Engineering (upcon-2019), 08-10 Nov 2019, AMU Aligarh (U.P)
- IEEE International Conference on Communication, Management and Information Technology-2017 (“ICCMIT’17”), 3- 4 April 2017, Warsaw, Poland

- International Conference on Computational Science and Mathematics in Engineering 2017 (CSME 2017) , Beijing, China

- International Conference on Circuits and Systems (CIRSY 2016), 27-28 Aug 2016, Dubai (UAE).

- IEEE International Conference on Communication, Management and Information Technology-2018 (ICCMIT'18) 02-04 April 2018, Madrid (Spain)

- International Conference on advances and soft computing applications in design and manufacturing (ASCADM-2018), going to be held on 4^{th} -6^{th} June 2018, NIT Patna, India

- IEEE International Conference on Advances in Computing, Communication Control and Networking (ICACCCN2018), Gr. Noida, India

- 12^{th} IEEE International Conference-INDIACOM-2018 going to be held on 14^{th} -16^{th} March 2018, New Delhi India.

- IEEE- International Conference on Signal Processing and Integrated Networks (SPIN 2018), 11-12 Feb 2018, Noida, (U.P.)

- IEEE-International conference on Recent Developments in Control Automation and Power Engineering-2017 (RDCAPE-2017), 26-27 Oct-2017, Amity University, Noida

- SPINGER- International Conference on Intelligent Communication Control and Devices (ICICCD-2017), 15-16 April 2017, Dehradun (U.K.)

- IEEE-International Conference on Computing Communication and Automation (ICCCA2017), 05-06 May, 2017, Greater Noida (U.P.)

- IEEE International Conference on Advances in Computing, Communication and Automation (ICACCA-2016) held on 30 Sept-01 Oct 2016, Bareilly (U.P.)

- SPINGER- International Conference on Intelligent Communication Control and Devices (ICICCD-2016), 2-3 April 2016, Dehradun (U.K.)

- IEEE-International Conference on Computing Communication and Automation (ICCCA2016), 29-30 April 2016, Greater Noida (U.P.)

- IEEE- International Conference on Signal Processing and Integrated Networks (SPIN 2016), 11-12 Feb 2016, Noida, (U.P.)

- IEEE International Conference on Medical Imaging, m-Health and Emerging Communication Systems (MEDCOM-2014), Greater Noida (U.P.)

- IEEE International Conference on Machine Intelligence Research and Advancement (ICMIRA-2014), Jammu (J&K), 29 Nov-01Dec, 2014

- International Conference on Circuits and Systems (CIRSY 2014),Bengluru.

- IEEE International Conference on Machine Intelligence Research and Advancement (ICMIRA-2013), Jammu (J&K), 21-23 Dec 2013

SESSION CHAIRED IN INTERNATIONAL CONFERENCES

- **Session Chaired** in 4th National Conference on Recent Trends in Electronics and Electrical Engineering (NCRTEEE - 2020), Ghaziabad (U.P.) 25th - 26th June, 2020

- **Session Co-Chaired** in National Conference on Electronic, Communication and Computation , 05-06 Sept 2020, NIT jamshedpur

- **Session Co-Chaired** Springer Electric Power and Renewable Energy Conference-2020 (EPRC-2020), NIT Jamshedpur

- **Session Chaired** in 2019 3rd IEEE International Conference on Recent Developments in Control, Automation & Power Engineering (RDCAPE), Noida (U.P.) 10-11 Oct 2019

- **Session Chaired** in IEEE- International Conference on Signal Processing and Integrated Networks (SPIN 2016), Amity University, Noida, (U.P.) on 12 Feb 2016.

- **Session Chaired** in IEEE-International Conference on Computing Communication and Automation (ICCCA2016), 29-30 April 2016, Greater Noida (U.P.)

- **Session Chaired** in Spinger International Conference On Smart Trends For Information Technology and Computer Communication-2016, 06-07 Aug-2016, 2016, Jaipur, India

- **Session Chaired** in Elsevier- International Conference On Advance Material Technologies" 27-28 December 2016, Vishakhapatnam (A.P).

- **Session Chaired** in Second International Conference On Information and Communication Technology for Competitive Strategies (ICTCS-2016) 4-5 March-2016, 2016, Udaipur, India.

- **Session Chaired** in IEEE-International conference on Recent Developments in Control Automation and Power Engineering-2017 (RDCAPE-2017), 26-27 Oct-2017, Amity University, Noida, India

- **Session Chaired** in SPINGER International Conference On Information and Communication Technology for Intelligent Systems, (ICTIS 2017), 25-26 March,

Ahmadabad, 2017, India

- **Session Chaired** in IEEE-International Conference on Innovative Applications of Computational Intelligence on Power, Energy and Control with Their Impact on Humanity -2016 (CIPECH-16), 18th -19th November 2016, Ghaziabad (U.P.)

- **Expert Talk** on topic "Modern active elements and their applications in non-conventional analog signal processing" at NIT Uttrakhand, Srinagar, Garwal on 23-01-2021

- **Keynote Speaker** AKTU TEQUIP-III sponsored Faculty development program in Vishveshwarya Group of Institution at topic "Latest trends in analog signal processing circuits" on 08-06-2018.

SPECIAL SESSION ORGANIZED

- **Organized Special Technical Session** on topic "Nano Scale electronic devices and Modern active devices" in IEEE- International Conference on Signal Processing and Integrated Networks-2016 (SPIN 2016), Noida, India with **Prof. Jean-Pierre Leburton, University of Illinois, (USA).**

- **Organized Special Technical Session** on topic "Recent Trends in mixed mode signal processing using new generation active building blocks" SPINGER International Conference On Smart Trends For Information Technology and Computer Communication-2016, 6-7 Aug-2016, 2016, Jaipur, India.

- **Organized Special Technical Session** on topic "Latest Advancements in Analog Integrated Circuit Blocks and Applications" Elsevier- International Conference On Advance Material Technologies" 27-28 December 2016, Vishakhapatnam (A.P).