Dr. Amit Prakash
Associate Professor, Department of Electronics & Communication Engg.
National Institute of Technology Jamshedpur, Jharkhand, India.
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Academic

- Ph. D — Department of Electronics & Communication Engg., N I T Jamshedpur, Jharkhand, India.

- M. Sc. Engg. (Power Electronics) — Department of Electrical Engg., N I T Jamshedpur, (Formerly R I T Jamshedpur), Jharkhand, India. — 7.75

- B. E (Electronics & Communication Engg.) — College of Engineering & Technology, Bijapur, Karnataka, India. — 63.94

Research Area

Parallel Processing; Network Reliability; Wireless Communication; VLSI Design

Under Graduate courses

VLSI Design, Mobile Communication, Microprocessor & its application, Computer Communication & Networking, Digital Signal Processing, Digital Communication, Introduction to Computer & Internet Technology, Programming & Data Structure

Post Graduate courses

Computer Communication & Networking, VLSI Design, VLSI CAD, VLSI & Embedded System Design

Departmental & Institutes Administrative Assignment

1. Convenor/Member of various departmental academic activities

2. Convenor/Member of various administrative assignments: Electrical Works Committee, Engineering Services, Admission Committee, Civil Works Committee, Convocations, Computer Centre *etc.*

3. Prof-in-charge of various institute administrative assignments: Vehicle Section, Institute Telephone Section, Electrical Maintenance & Repair, Cultural Activities *etc.*

4. Warden/Asstt. Warden of boy's hostels

5. Associate Dean (Planning & Development): Electrical Works & Engineering Services (2015-20)

6. Chairman, Electrical Maintenance & Services: 2020

Organization of Seminar/Conference/Workshop

1. Co-coordinator, National Workshop organized on "VLSI & Embedded System Based Designs" *VESD-2009*, 6-8th March 2009 at NIT Jamshedpur.

Professional Affiliation

1. Reviewer of Communication System, Tata McGraw Hill
2. Reviewer of GPS Satellite Communication, Tata McGraw Hill
3. Reviewer of Wireless Communication, Tata McGraw Hill
4. Reviewer of Wireless Communication Networks, Tata McGraw Hill

Conferences/Research Journals

1. Shashikant, Amit Prakash, "Noise Cancellation by Linear Adaptive Filter based on efficient RLS Lattice Algorithm" *IJSER*, Volume 4, Issue 5, 2013.

2. Piyush Chauhan, Amit Prakash, "Comparison and overview of SCR Based ESD Protection Devices for CMOS ICs" *IJERT*, Vol. 2, ISSN: 2278-0181.

3. Amit Prakash, Paper presentation at National Conference *NCAET-2013*.

4. Amit Prakash, Dilip Kumar Yadav, Arvind Choubey, "A survey of Multistage Interconnection Networks", *Recent Advances in Electrical & Electronic Engineering, Bentham Science*, Vol. 13, No. 2, pp. 165 –183, 2020.

5. Amit Prakash, Dilip Kumar Yadav, "Design and reliability analysis of fault-tolerant shuffle exchange gamma logical neighborhood interconnection network," *The Journal of Supercomputing*, Vol. 75, No. 12, pp. 7934 –7951, 2019.

6. Amit Prakash, D K Yadav, A Choubey, "Terminal reliability analysis of multistage interconnection networks", *International Journal of System Assurance Engg. Management*, Vol. 11, No. 1, pp. 110–125, 2020.

7. Amit Prakash, D K Yadav, "Multiprocessor Systems Design: Reliability Analysis of Multistage Interconnection Network," *In Inventive Computation Technologies, Springer International Publishing*, Vol. 98, pp. 858–864, 2020.

8. Amit Prakash, Dilip Kumar Yadav, "2-Terminal Reliability Analysis of Single Fault-tolerant Multistage Interconnection Networks", *2nd International Conference on Communication & Computational Technologies*, 2019. ISBN: 978-081-15-5077-5, Springer, doi: 10.1007/978-981-15-5077-5.