CS220 Assignment 7 Documentation

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1   Milestone 1 - Registers and Usage Protocol

2   Milestone 2 - Instruction and Data Memory

3   Milestone 3 - Instruction Encoding

4   Milestone 4 - Implementation of Instruction Fetch

5   Milestone 5 - Instruction Decode

6   Milestone 6 - Airthmetic Logic Unit (ALU)

7   Milestone 7 - Branching Operations

8   Milestone 8 - FSM Design and Control Processor

9   Milestone 9 - MIPS Code for Bubble Sort

10   Milestone 10 - Implementation in CSE-BUBBLE