


1. General information.	
Name	ASHOK.K
Date of Birth	13/02/1988
Gender	Male
Marital Status	Single
Languages Known	English, Tamil, Malayalam and Hindi
Correspondence Address	No.2/442,Lakshmi Krupa,Basthi ,II nd Cross Avalapalli Dam Road Hosur - 635109
Phone No.	Mobile No: 9442727895
Email	ashok_ace01@yahoo.co.in



2. Educational Qualification.						
S.No.	Qualification	University	Year	Subject(s)/Topic(s)	% Achieved	Distinctions Etc.
1	Ph.D.,	Anna University	2017	Image Processing	Commended	
2	M.E.,	Anna University	2011	Communication Systems	85%	First Class With Distinction
3	B.E.,	Anna University	2009	Electronics and Communication Engineering	71%	First Class
4	Diploma.,	DoTE	2006	Electronics and Communication Engineering	78%	First Class With Honors

3.Research Experience / Teaching Experience					
S.No.	Post	Organization/ University	Duration		Experience (In years and months)
			From (Date)	To (Date)	
1	Full Time Researcher	Anna University, Coimbatore	07/07/2011	April -2017	5 Years 9 Months
2.	Assistant Professor	Bangalore College of Engineering and Technology	02/05/2017	Till Date	1year 7 Months

4. Scholarly achievements.

	Details
Peer reviewer for	KSII Transactions on Internet and Information Systems
	COMPEL-The Int. Journal for Computation and Mathematics in Electrical and Electronic Engineering

5. Publications.

Category	Conferences	Journals
National	2	-
International	1	2
Total	5	

International Journals

1. **Ashok K** & Vijaykumar V R 2016, 'Adaptive impulse detection based selective window median filter for removal of random-valued impulse noise in digital images COMPEL-The Int. Journal for Computation and Mathematics in Electrical and Electronic Engineering, vol. 35, no. 5, pp.1604-1616 ISSN : 0332-1649 (**Impact factor – 0.5**)
2. **Ashok K** & Vijaykumar V R 2016, 'Adaptive window based impulse noise detection for removal of random-valued impulse noise in digital images', Asian Journal of Information Technology, vol. 15, no. 3, pp. 635-638. ISSN : 1993-5994 (Scopus Indexed)
3. **Ashok K** & Vijaykumar V R 'Efficient Eigenvalue Based Unsymmetric Trimmed Mean Filter for Removing Gaussian Noise', Optik (Under Review)

International Conferences

4. Vijaykumar, V. R., **Ashok.K.**, and. Dhivya.K. (2012) 'Efficient image transmission over MIMO channel using compressive sensing' Third International Conference on. IEEE Computing Communication & Networking Technologies (ICCCNT'12).

National Conferences

5. **K. Ashok** (2011) 'Implementation and analysis of space time block codes for MIMO systems' Second National Conference on Advanced Telecommunication Technologies (NCATT'11).
6. **K. Ashok** (2009) 'Implementation and analysis of space time block codes with different modulation schemes' National Conference on. Convergent Communication Technologies (NCCCT'09).

6. Participation in National/ International forums in the area of academic and professional expertise.

Category	Conferences	Workshops	FDP / SDP / STTP
Organized			
Attended	1	17	6

1. Participated in a **AICTE Sponsored Two Week** Faculty Development programme on **“Adapation of computational Intelligence in Wireless Communication Systems and Network Security”** from 4th to 17th December 2017 organized by Department of ECE Adhiyamaan College of Engineering, Hosur.
2. Participated in a **Seven Days** Faculty Development programme on **“EC 6402 – Communication Theory”** from 14th to 20th December 2016 organized by Centre For Faculty development Anna University Chennai.
3. Participated in the two day workshop on **“VLSI DESIGN USING OPEN SOURCE EDA”**, on 8th and 9th September 2016 organized by the Department of ECE at Anna University Regional Campus Coimbatore.
4. Participated in the one day workshop on **“Integration of Keil and Proteus for Microcontroller Based Applications”**, on 30th August 2016 organized by the Department of ECE at Anna University Regional Campus Coimbatore.
5. Participated in the one day Hands on Training Programme on **“Integration of MATLAB and XILINX for image processing Applications”** on 29th July 2016 organized by the Department of ECE at Anna University Regional Campus Coimbatore.
6. Participated in the two day workshop on **“Antenna Design and Analysis using ESI’S CEM solution Tool”**, on 3rd and 4th December 2015 organized by the Department of ECE at Anna University Regional Campus Coimbatore.
7. Participated in the one day workshop on **“SciLAB – An open source tool equivalent to MATLAB”**, on 16th October 2015 organized by the Department of ECE at Anna University Regional Office Coimbatore.
8. Participated in the one day workshop on **“ARDUINO Microcontroller”**, on 12th October 2015 organized by the Department of ECE at Anna University Regional Office Coimbatore.
9. Participated in the two day seminar on **“Advanced Digital Image Processing”**, on 17th and 18th April 2015 organized by the Department of ECE at Anna University Regional Centre Coimbatore.
10. Participated in the two Day Faculty Development programme on **“Solid State Device Modelling and Simulation”** on 17th and 18th October 2014 organized by the Department of ECE at Anna University Regional Centre Coimbatore.

11. Participated in the two day training programme on “**MSP430 Ultra Low Power Microcontroller**”, on 25th and 26th September 2014 organized by the Department of ECE at Anna University Regional Centre Coimbatore.
12. Participated in a **Two-Week** ISTE workshop on “**Signal and Systems**” from 2nd to 12th January 2014 Conducted by **Indian Institute of Technology, Kharagpur**.
13. Attended the fourth National Conference on Computer Vision, Pattern Recognition, Image Processing and Graphics NCVPRIPG 2013 Organized by **IIT Jodhpur**
14. Participated in a **Seven Days** Faculty Development programme on “**EC 2301 – Digital Communication**” from 10th to 16th June 2013 organized by Centre For Faculty development Anna University Chennai.
15. Attended a two day workshop on “**Analog System Design – Educators Workshop**” on 27th and 28th January 2013 Conducted by Texas Instruments India University Programme.
16. Attended a Short Course on “**MIMO / OFDM Based Advanced 4G Cellular Networks**” from 11th to 13th January 2013 Conducted by **Indian Institute of Technology Kanpur**.
17. Attended a One day workshop on “**Cognitive Radio Networks**” on 28th August 2012 organized by the Department of ECE Coimbatore Institute of Technology Coimbatore.
18. Attended the two day Workshop on “**CMOS Layout Design Using Microwind**”, on 23rd and 24th August 2012 organized by the Department of ECE at Anna University Regional Centre Coimbatore in association with ni2 Designs Pune.
19. Attended a three day “**Train the Trainer Programme on Analog System Design Using ASLKv2010 Starter Kit**” on 23rd to 25th July 2012 Conducted by Texas Instruments India University Programme in association with Cranes Software International Ltd Bangalore.
20. Attended the two day Faculty Development programme on “**Analog System Design Using - ASLK**” on 5th and 6th July 2016 organized by the Department of ECE at Anna University Regional Centre Coimbatore in association with Texas Instruments India University Programme.
21. Attended in a four day national workshop on “**Sparse Image and Signal Processing SISP**” from 25th to 26th December 2011 organized by the Centre for Excellence in Computational Engineering and Networking, Amritha Vishwa Vidyapeetham, Coimbatore.
22. Participated in the one day workshop on “**Digital Video Processing**”, on 30th September 2011 organized by the Department of ECE at Anna University Coimbatore.
23. Attended the Workshop on “**MIMO Wireless Communications**”, from 24th to 27th August 2011 organized by the Department of ECE Thiagarajar College of Engineering Madurai.
24. Participated in a **Seven Days** Faculty Development programme on “**Wavelet and Multiresolution processing**” from 14th to 20th December 2011 organized by Centre For Faculty development Anna University Coimbatore.

7.Skill Set.				
Circuit Simulators	Microwind	Programming Language		
HDL	Verilog HDL	C, MATLAB		
FPGA	Xilinx Spartan 3E Xilinx Systemgenerator Altera Dsp Builder	Design tool FEKO-Antenna design tool		
Platforms	Windows Linux	ASIC TOOL Cadence Virtuoso RTL Design Compiler Encounter layout.		

10.Details of Referee.				
S.No.	Name of the Referee	Post Held by Referee	E-mail	Mobile
1	Dr.S.Chandrakumar Mangalam	Professor and Head, Department of Management Studies Anna University Regional campus , Coimbatore -641046	ckmaucbe@gmail.com	9865616158
2	Dr.V.R.Vijaykumar	Associate Professor and Head, Dept of ECE, Anna University Regional campus , Coimbatore -641046	vr_vijay@yahoo.com	9442014139

DECLARATION

I **ASHOK.K** hereby declare that all the above information and particulars provided by me are true to best of my knowledge.

Place : Hosur

Date : 03/11/2018.

Signature of the Applicant