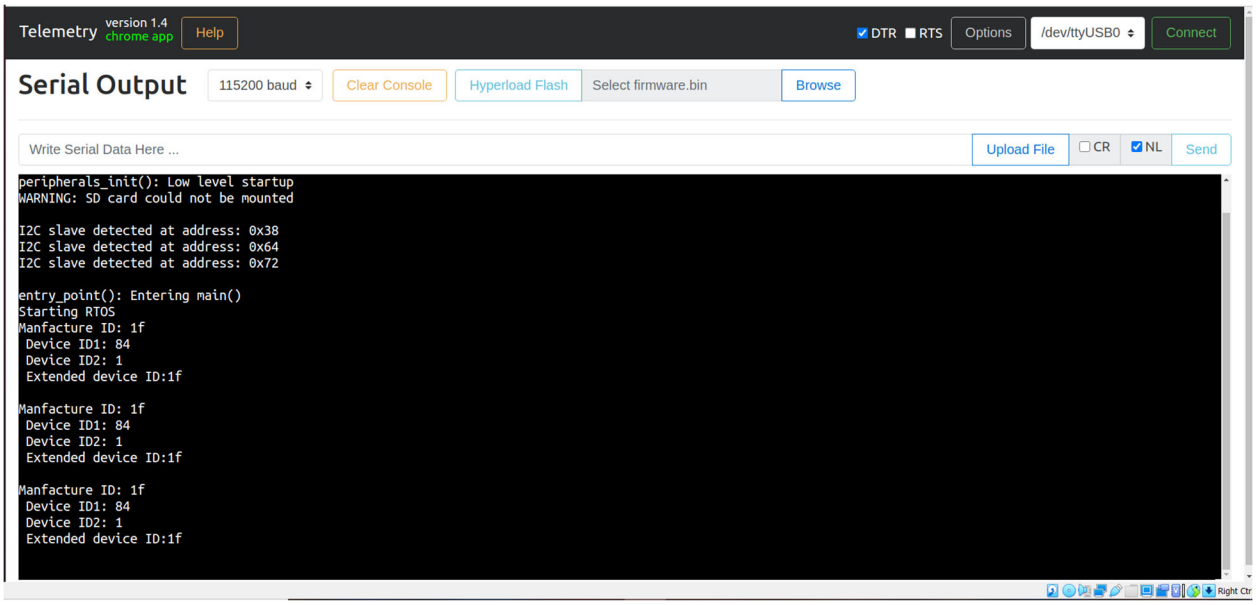
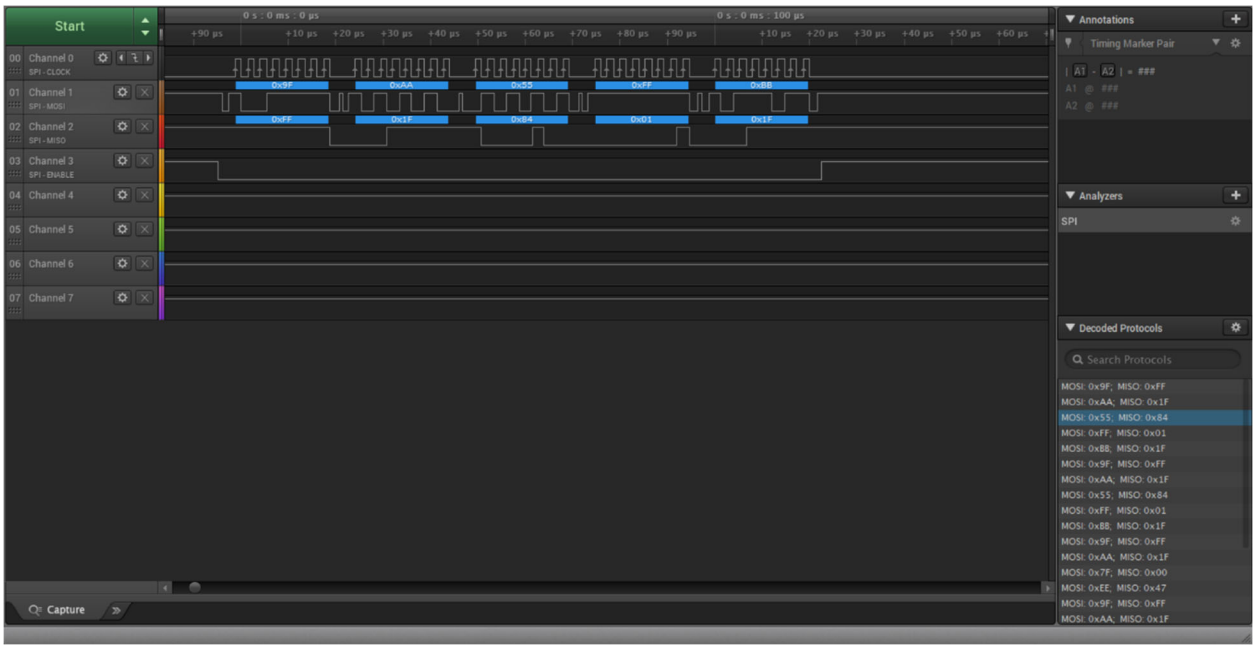
LAB 6: SPI

PART 1:

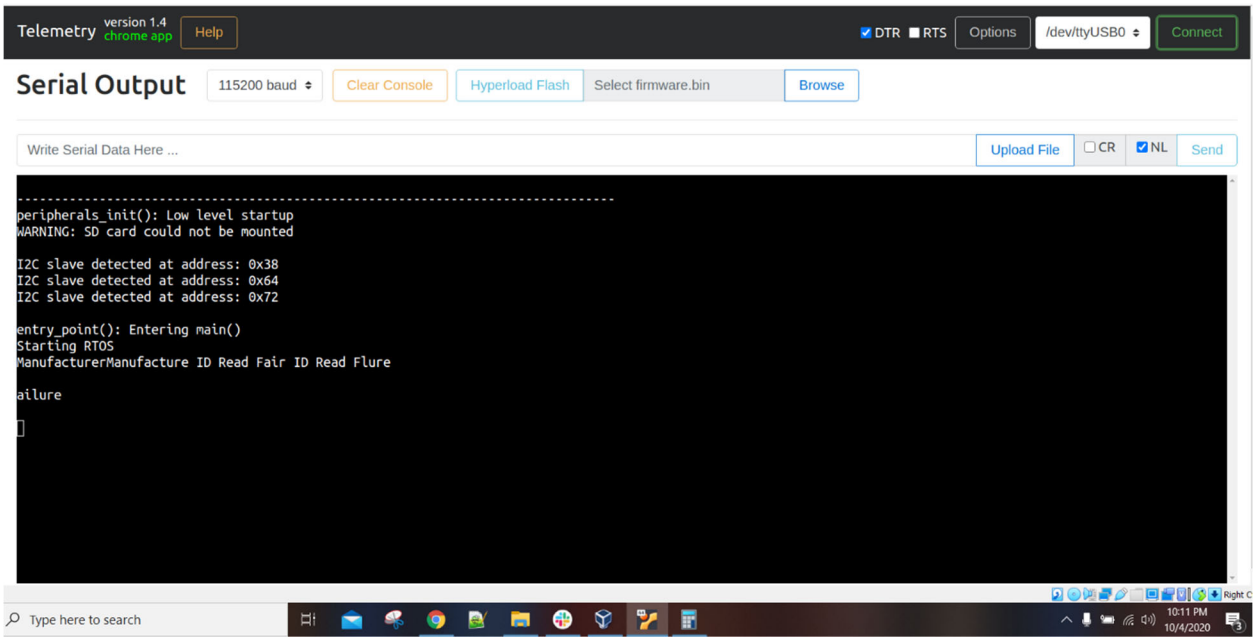


Output from Logic Analyzer:

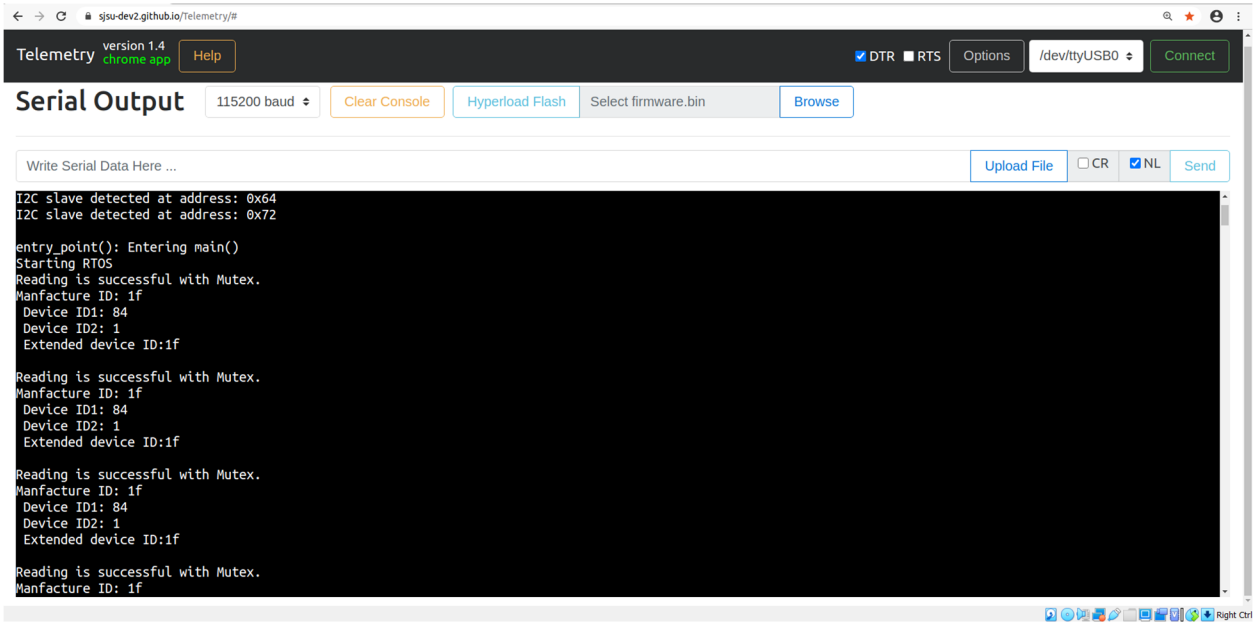


PART2:

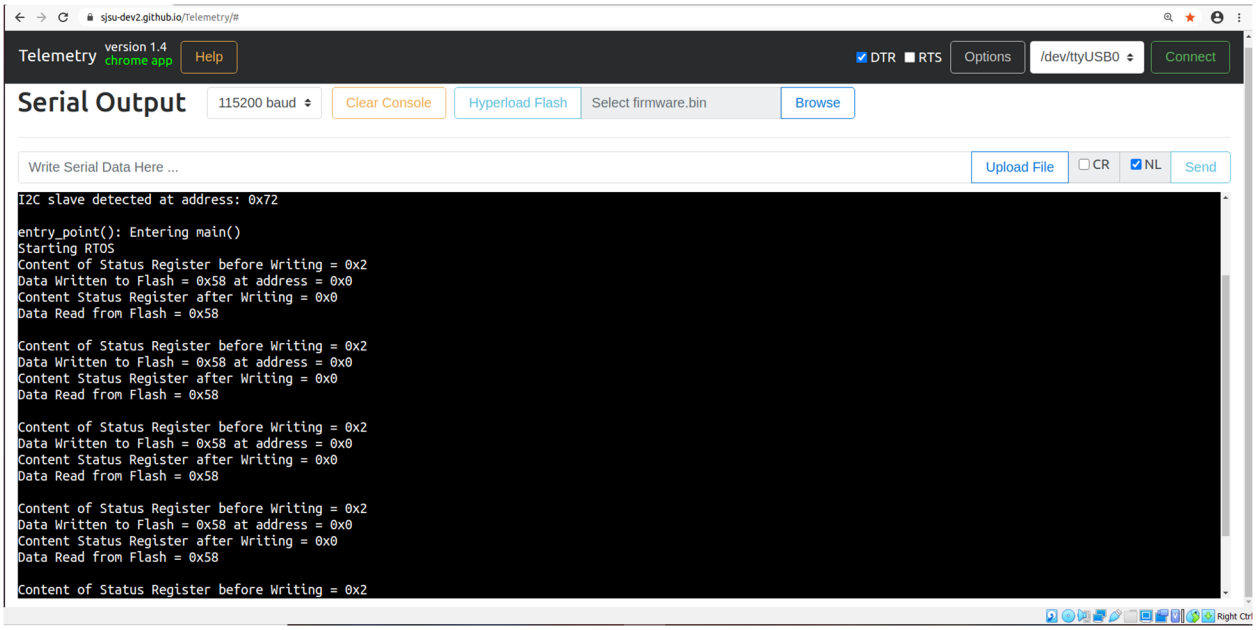
Without Mutex



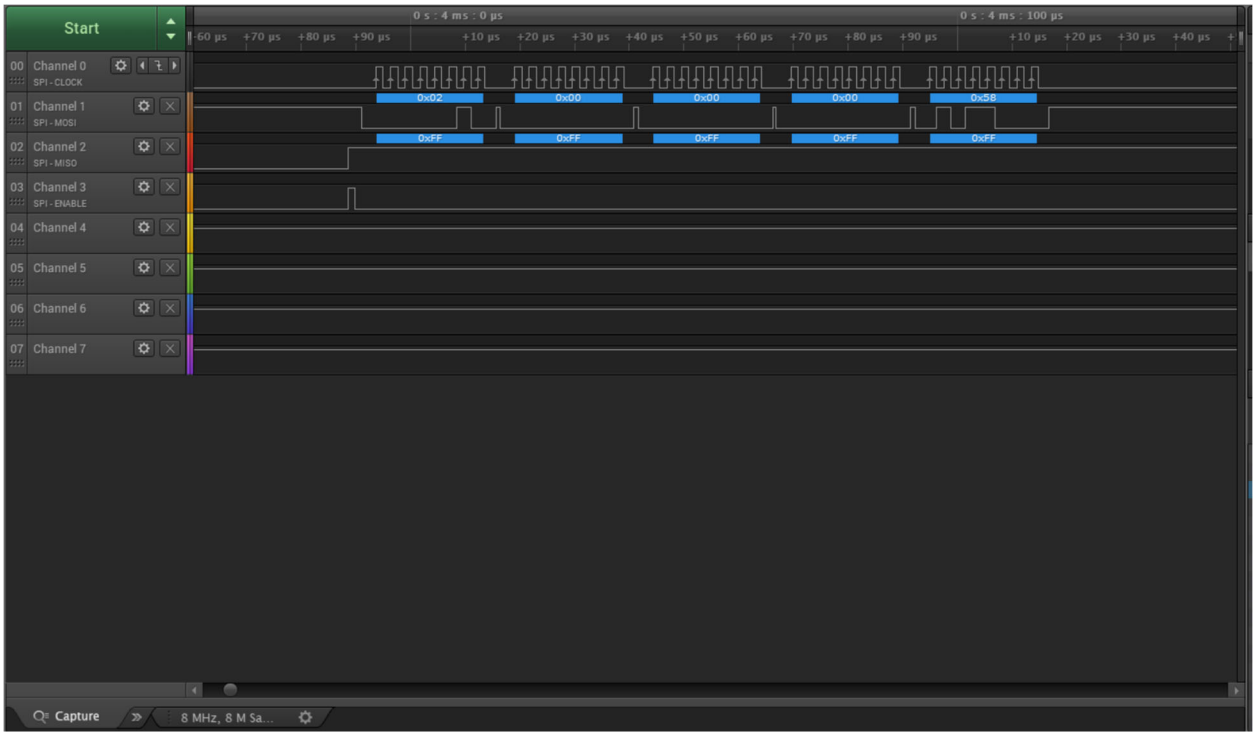
With Mutex:



EXTRA CREDIT:



WRITING DATA:



READING DATA:

Right hand side of the screenshot below shows the sequence of bytes sent and received by SPI while reading and writing data. The waveform in screenshot displays only the bytes read from SPI.