

MICROPROCESSORS MODEL LAB EXAM

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CSE-C

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1. Write an ALP using 8051 to find number of ones and zeros in a 16 bit number.

Aim: To find the number of ones and zeros in a 16 bit number using 8051.

Algorithm:

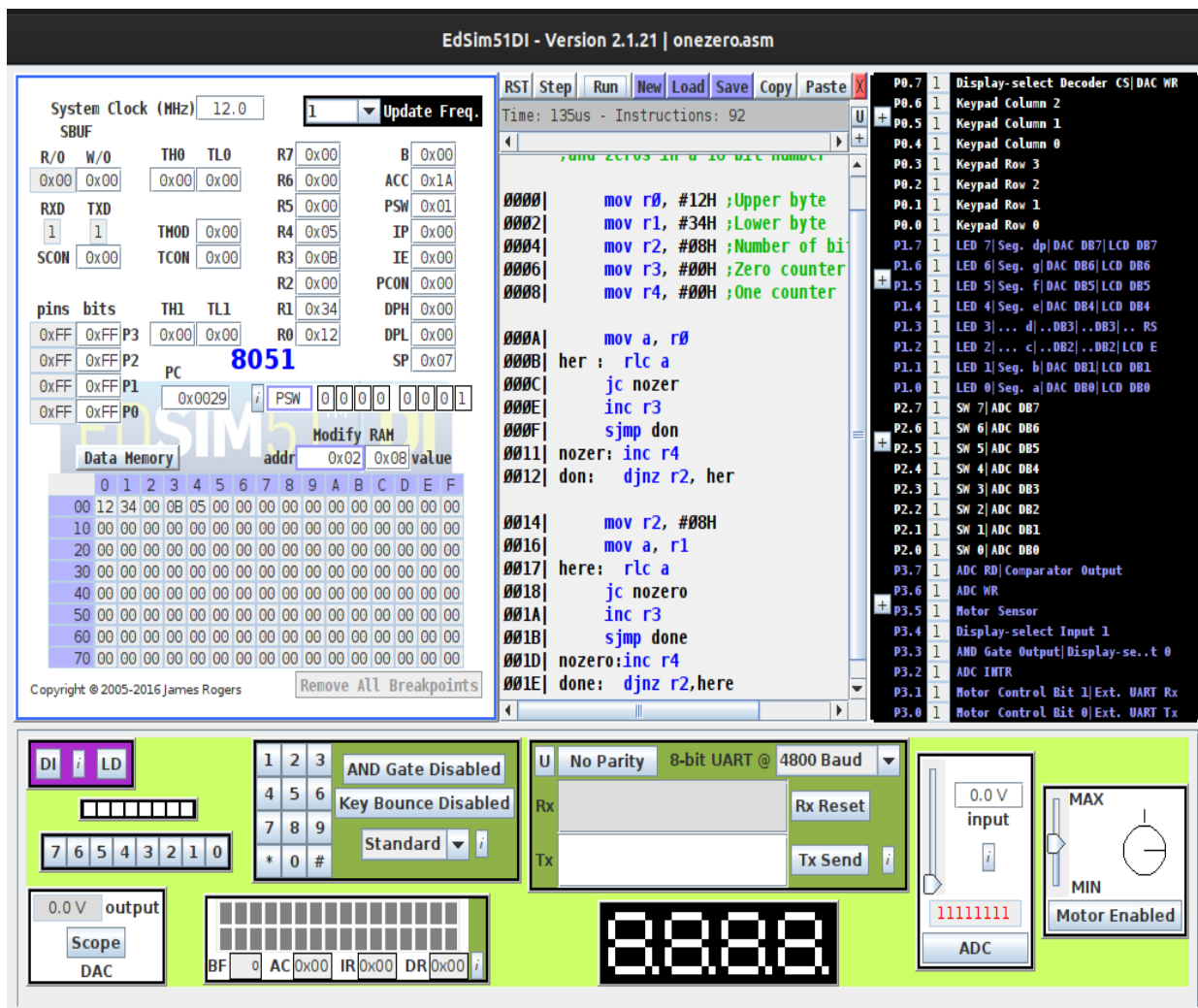
- Assign the upper byte of number to be counted to register 0, lower byte to register 1 and 08H(number of bits in each byte) to register 2.
- Move values 00H to registers 3 and 4 to act as zero and one counters respectively.
- Move contents of register 0 to A.
- Under label HER, left rotate A using RLC A.
- Jump to label NOZER if CF = 1.
- Increment register 3 and jump to label DON.
- Under label NOZER, increment register 4.
- Under label DON, decrement register 2 and loop back to HER if it is not 0.
- Move value 08H to register 2, and contents of register 1 to A.
- Under label HERE, left rotate A using RLC A.
- Jump to label NOZERO if CF = 1.
- Increment register 3 and jump to label DONE.
- Under label NOZERO, increment register 4.
- Under label DONE, decrement register 2 and loop back to HERE if it is not 0.

Program:

;Program to find number of ones
;and zeros in a 16 bit number

```
        mov r0, #12H ;Upper byte
        mov r1, #34H ;Lower byte
        mov r2, #08H ;Number of bits per byte
        mov r3, #00H ;Zero counter
        mov r4, #00H ;One counter

her :    mov a, r0      ;Load upper byte to A
        rlc a          ;Left rotate A
        jc nozer       ;Jump to label NOZER if CF = 1
        inc r3         ;Increment zero counter
        sjmp don
nozer:   inc r4         ;Increment one counter
don:    djnz r2, her    ;Repeat till r2 is 0
```



2. Write an ALP using 8086 to count odd and even numbers in list.

Aim: To count the number of even and odd numbers in a list using 8086.

Algorithm:

- Move data to accumulator and then to data segment.
- Move offset of list to SI register, count to CL register.
- Move value 00H to registers DH and DL to act as even and odd counters respectively.
- Under label HERE, move value at address pointed by SI register to BL register.
- Mask everything but the last bit using AND BL, 01H.
- Jump to OCNT if BL is not equal to 0.
- Increment value of DL register, and jump to DONE.
- Under label OCNT, increment value of DH register.
- Under label DONE, increment SI register.
- Decrement CL register, jump to HERE if CL is not 0.
- Odd and even counts in DH, DL must be stored in odd and eve respectively.

Program:

;Program to count number of odd and even numbers in a list

```
assume ds:data, cs:code
data segment
org 00H
source db 01H, 12H, 23H, 33H, 44H, 56H, 67H, 77H, 80H, 91H    ;Input list
org 10H
eve db 00H                ;Even counter
odd db 00H                ;Odd counter
org 20H
count db 0AH              ;Number of elements
data ends
code segment
start: mov ax, data
mov ds, ax

mov si, offset source
mov cl, count
mov dh, 00H               ;Even counter
mov dl, 00H               ;Odd counter
here: mov bl, [si]
and bl, 01H               ;Mask everything but lsb
jne ocnt
inc dh                    ;Increment even counter
jmp done
```

```

ocnt: inc dl                ;Increment odd counter
done: inc si
dec cl
jnz here                    ;Repeat till CL is 0
mov eve, dh
mov odd, dl
mov ah, 4ch
int 21h
code ends
end start

```

Input/Output:

Input:

01H, 12H, 23H, 33H, 44H, 56H, 67H, 77H, 80H, 91H

Output:

Number of even – 4 (04H)

Number of odd – 6 (06H)

```

0E27:001E FEC9          DEC      CL
-d 0e24:0000
0E24:0000  01 12 23 33 44 56 67 77-80 91 00 00 00 00 00 00  ..#3D0gw.....
0E24:0010  00 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00  .....
0E24:0020  0A 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00  .....
0E24:0030  B8 24 0E 8E D8 BE 00 00-8A 0E 20 00 B6 00 B2 00 00  .$. ....
0E24:0040  8A 1C 80 E3 01 75 04 FE-C6 EB 02 FE C2 46 FE C9 00  ....u.....F..
0E24:0050  75 EE 88 36 10 00 88 16-11 00 B4 4C CD 21 B0 00 00  u..6.....L.?!..
0E24:0060  50 E8 A4 FA 89 46 FA 83-7E FA FF 75 03 E9 BB 00 00  P....F...~...u....
0E24:0070  8B 5E FA 8A 87 B7 2D 88-46 E7 B4 00 3B 06 AA 2C 00  .^.....-F...;...
-g

Program terminated normally
-d 0e24:0000
0E24:0000  01 12 23 33 44 56 67 77-80 91 00 00 00 00 00 00  ..#3D0gw.....
0E24:0010  04 06 00 00 00 00 00 00-00 00 00 00 00 00 00 00  .....
0E24:0020  0A 00 00 00 00 00 00 00-00 00 00 00 00 00 00 00  .....
0E24:0030  B8 24 0E 8E D8 BE 00 00-8A 0E 20 00 B6 00 B2 00 00  .$. ....
0E24:0040  8A 1C 80 E3 01 75 04 FE-C6 EB 02 FE C2 46 FE C9 00  ....u.....F..
0E24:0050  75 EE 88 36 10 00 88 16-11 00 B4 4C CD 21 B0 00 00  u..6.....L.?!..
0E24:0060  50 E8 A4 FA 89 46 FA 83-7E FA FF 75 03 E9 BB 00 00  P....F...~...u....
0E24:0070  8B 5E FA 8A 87 B7 2D 88-46 E7 B4 00 3B 06 AA 2C 00  .^.....-F...;...
-q

```