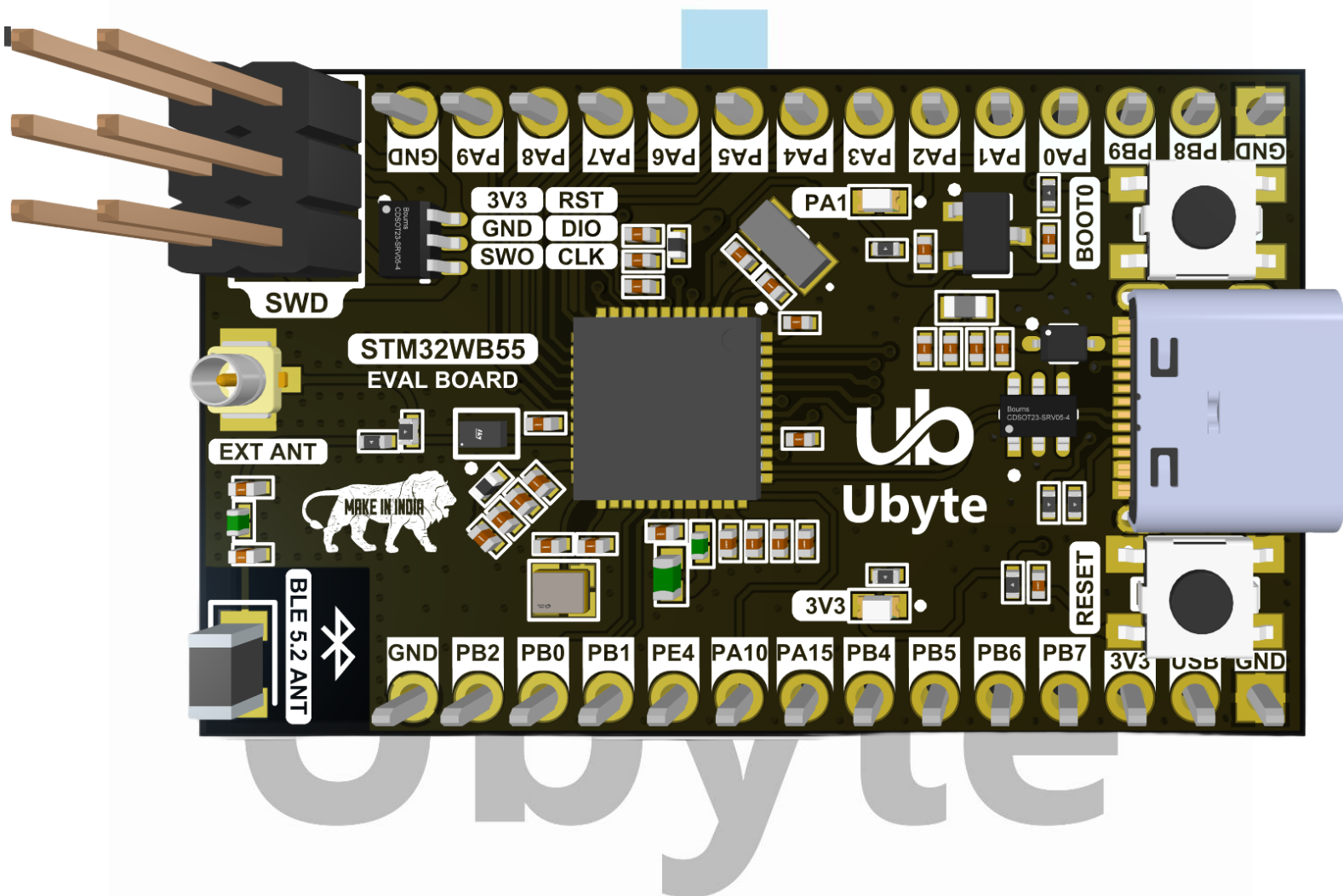
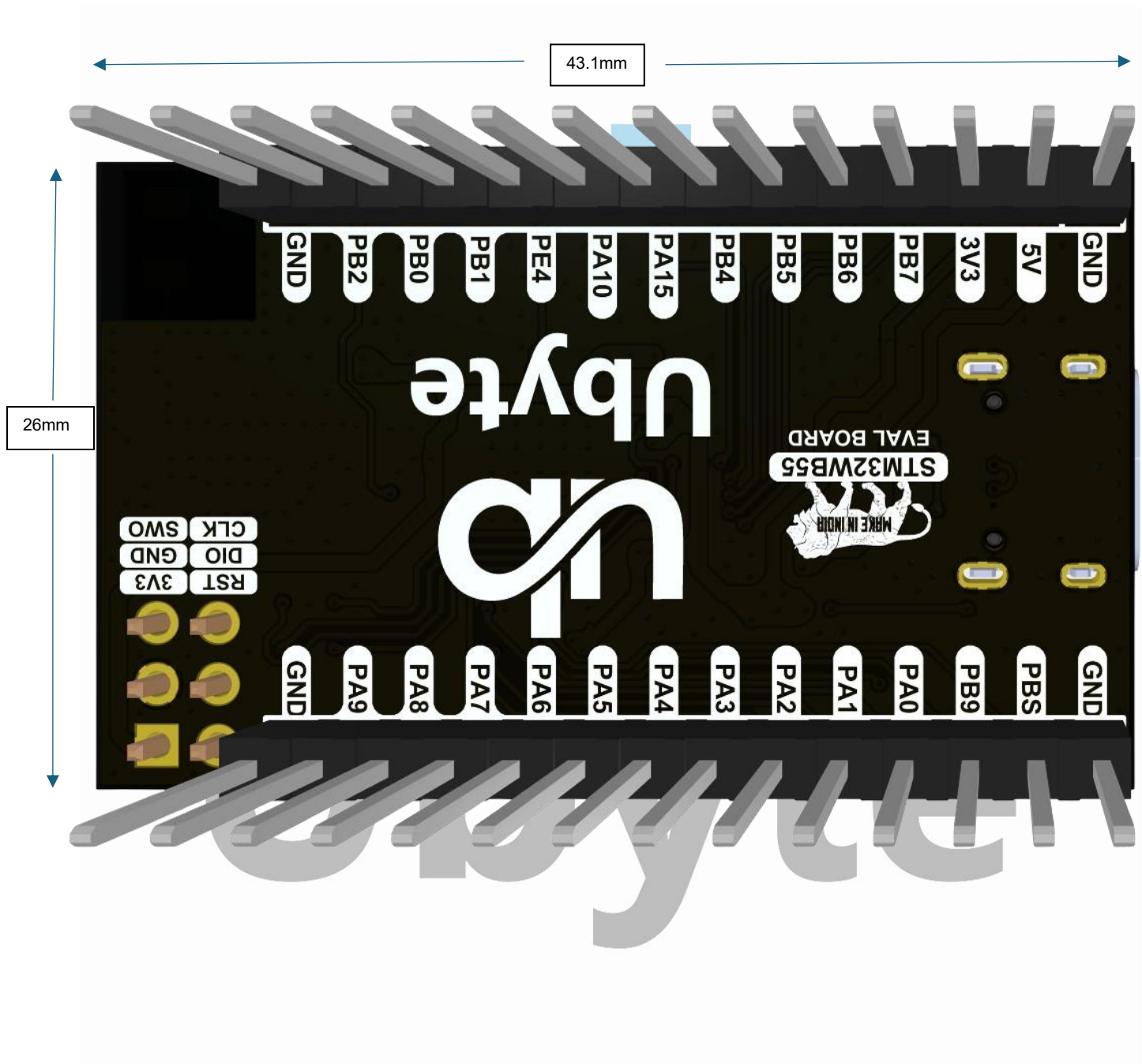
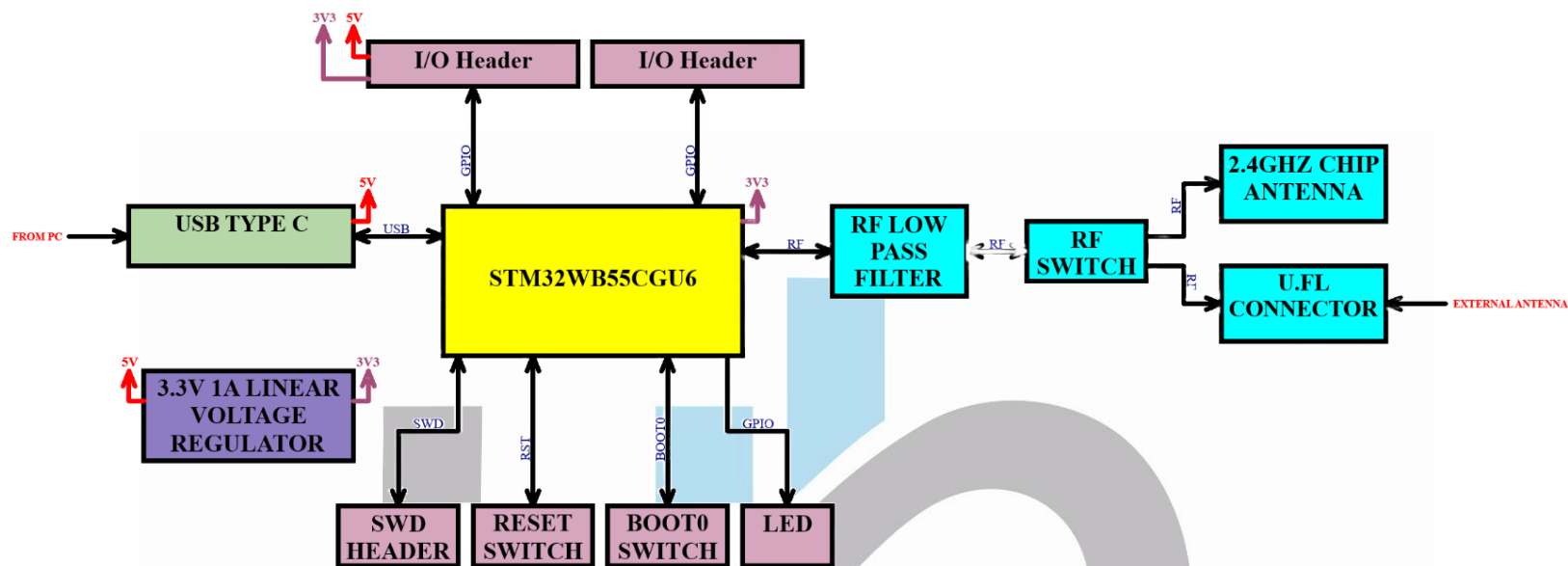


STM32WB55CGU6 Evaluation Board HARDWARE DOCUMENTATION







STM32WB55CGU6 Evaluation Board Hardware Block Diagram

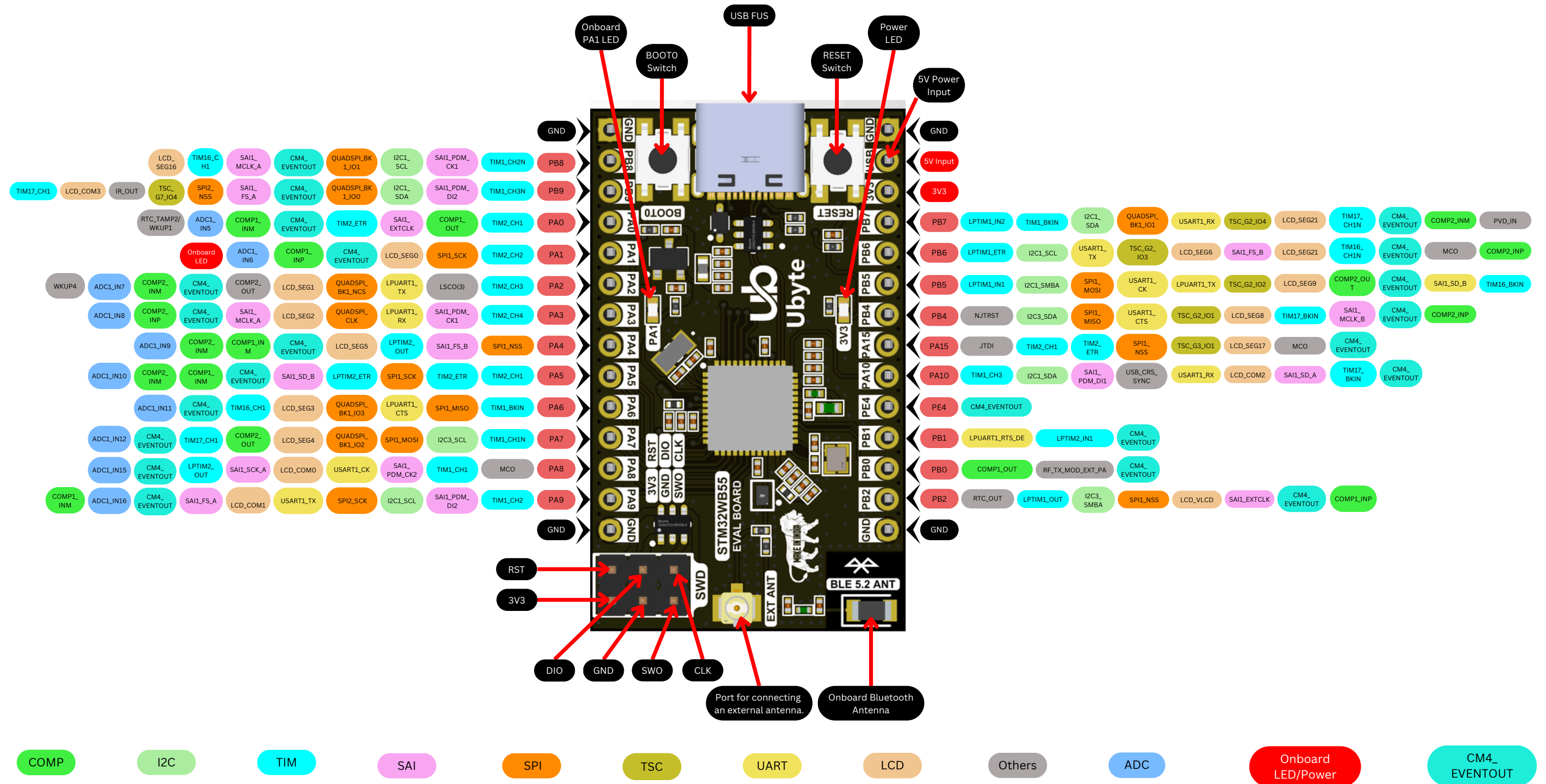
1. Introduction

This document provides a detailed hardware description of the STM32WB55CGU6-based evaluation board. The design integrates power management, a USB interface, RF connectivity, and GPIO access, making it suitable for IoT and embedded system applications.

2. Features

- STM32WB55CGU6 microcontroller (ARM Cortex-M4 & Cortex-M0+ dual-core)
- 3.3V power supply with XC6206P332MR-G (3.3V 200mA LDO) regulation
- USB Type-C connectivity (16-pin)
- Bluetooth 5.0 RF section with onboard and external antenna options
- SWD interface for debugging and programming
- Onboard LEDs for status indication
- External GPIO access via headers
- 32 MHz and 32.768 kHz crystal oscillators

4. Pinout



4. Hardware Description

4.1 Powering the Board

The board offers two power input options for flexibility in various applications:

- **USB Type-C Connector:** The primary method of powering the board, providing regulated 5V input.
- **5V Input via 14-Pin Header:** An alternative power source that allows external 5V input directly through the expansion header pin **5V Input**

4.2 Power Supply

The board operates on a **3.3V** main power supply, regulated by the **XC6206P332MR-G (3.3V 200mA LDO)**.

4.3 Microcontroller Unit (MCU)

- **Part Number:** STM32WB55CGU6
- **Package:** UFQFPN-48
- **Core:** ARM Cortex-M4 & Cortex-M0+
- **Flash Memory:** 1MB
- **SRAM:** 256KB
- **Operating Voltage:** 1.71V to 3.6V

4.4 USB Type-C Interface

The **USB Type-C 16-pin connector (2MD-073)** is used for both power input and data transfer.

- **Input Voltage:** 5V

4.5 Bluetooth 5.0 RF Section

The RF section of the board is designed to provide flexible antenna selection while ensuring proper impedance matching for optimal signal performance. It includes a **matching network**, an onboard chip antenna (**2450AT18B100E**), and an external antenna option through an **IPEX 1.25mm 6GHz 50Ω SMD coaxial connector**.

Antenna Selection Mechanism

The board allows users to switch between the onboard **2450AT18B100E chip antenna** and an external antenna via the **U.FL/IPEX connector (J1)**. This selection is made using a **0 Ω jumper resistor (R6)** in the RF signal path:

- **Onboard Chip Antenna (Default Configuration):**
 - The **0 Ω resistor (R6)** is placed to connect the RF signal directly to the **chip antenna (ANT1)**.
 - The **U.FL connector (J1)** remains disconnected.
- **External Antenna (U.FL Connector Activated):**
 - The **0 Ω resistor (R6)** is removed (DNP - Do Not Populate).
 - This disconnects the onboard chip antenna and directs the RF signal to the **U.FL connector (J1)** for an external antenna connection.

4.6 SWD Interface, Reset, and Boot

The board features an SWD programming and debugging interface using a **2x3-pin header**.

- **SWD Header:** Gold-plated, 3A Direct Insert, 2.54mm pitch, 2x3 Pins
- **Reset (NRST) and Boot (BOOT0) buttons** for system control

4.7 Clock Circuitry

- **Main Clock:** 32 MHz crystal oscillator
- **Low-Frequency Clock:** 32.768 kHz crystal for RTC operation

4.8 Onboard LED Indicator

- **Yellow LED (0603 package):** Connected to **PA1**
- **Red LED (0603 package):** Power indication

4.9 GPIO and Expansion Headers

The board provides external GPIO access via two **14-pin headers**.

- **Header Details:** Gold-plated, 3A, Direct Insert, Round Needle, Pitch=2.54mm
- Supports connections for **USB signals, SWD, and RF control lines**