EE22BTECH11049 - Shivansh Kirar

Question EC 31 2023

The signal-to-noise ratio (SNR) of an ADC with a full-scale sinusoidal input is given to be 61.96 dB. The resolution of the ADC is   GATE EC 2023

Solution:

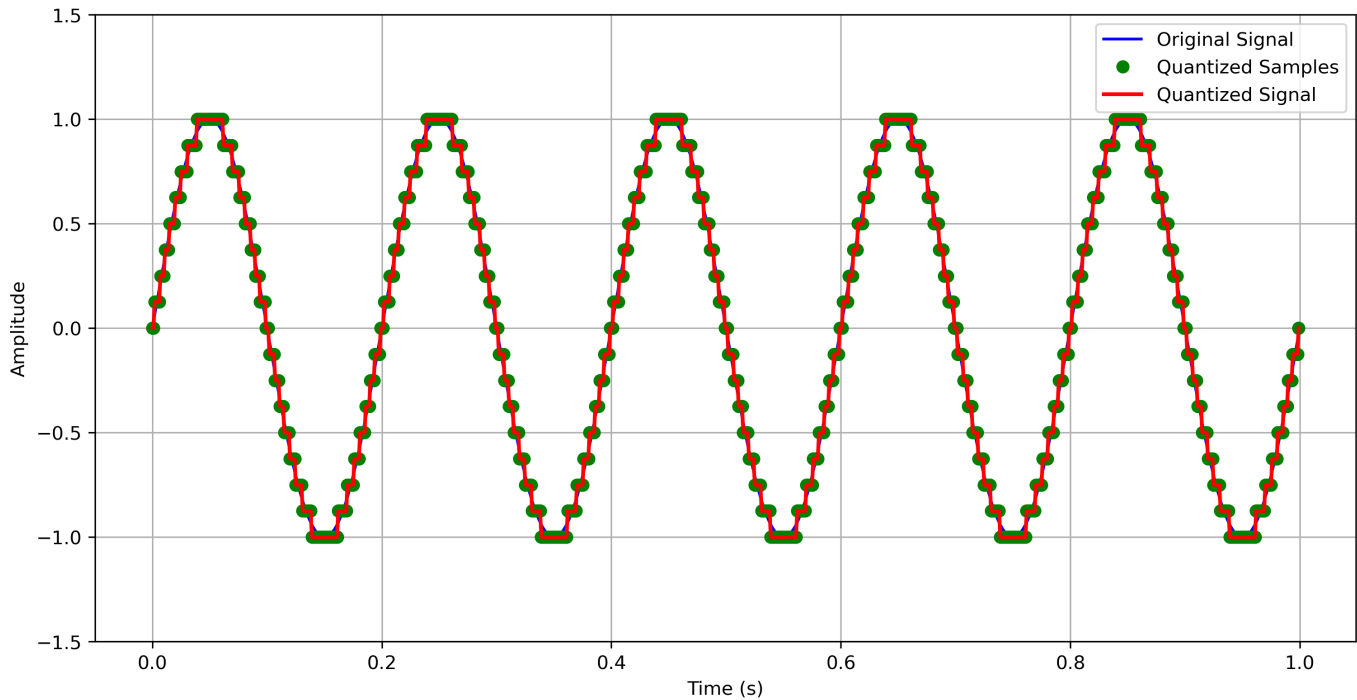| Variable	Defination
T	Time Period
f	Frequency
A	Amplitude
Ps	Signal Power
Pe	Noise Power
q	Quantization step size
n	No of bits
e	Quantization Error

Fig. 1: Quantization of Sinusoidal Signal

1) Signal Power:
   The power of a continuous-time signal is defined as the average value of the square of the signal over a certain time interval. For a sinusoidal signal $x(t) = A\sin(2\pi f t + \phi)$, the power ($P_s$) is calculated as:

$$P_s = \lim_{T\to\infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} |x(t)|^2 dt \tag{1}$$

where ϕ is the phase of the signal.

$$P_s = \lim_{T \to \infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} |A \sin(2\pi ft + \phi)|^2 dt \tag{2}$$

$$P_s = \lim_{T \to \infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} A^2 \sin^2(2\pi ft + \phi) dt \tag{3}$$

$$P_s = \lim_{T \to \infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} A^2 \cdot \frac{1 - \cos(4\pi ft + 2\phi)}{2} dt \tag{4}$$

$$P_s = \frac{1}{2} \lim_{T \to \infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} A^2 dt - \frac{1}{2} \lim_{T \to \infty} \frac{1}{T} \int_{-\frac{T}{2}}^{\frac{T}{2}} A^2 \cos(4\pi ft + 2\phi) dt \tag{5}$$

$$P_s = \frac{1}{2} \cdot A^2 - 0 \tag{6}$$

$$P_s = \frac{A^2}{2} \tag{7}$$

$$\tag{8}$$

Here, A= 1, so:

$$P_s = \frac{1}{2} \tag{9}$$

2) Noise Power:
Quantized to 2^n discrete levels, equivalent to n bits, one quantization step q has a size of

$$q = \frac{2}{2^n} = 2^{-(n-1)} \tag{10}$$

We know,quantization error has a maximum value of plus or minus half the step size, so

$$|e| \leq \frac{q}{2} \text{ and therefore, } |e| \leq 2^{-n} \tag{11}$$

For a large enough number of quantization steps, the probability density function of the quantization error tends toward being flat 1. Pdf of error (e) of quantization is defined as

$$p_r(r = e) = \begin{cases} \frac{1}{q}, & \text{if } -\frac{q}{2} < e < \frac{q}{2} \\ 0, & \text{otherwise} \end{cases} \tag{12}$$
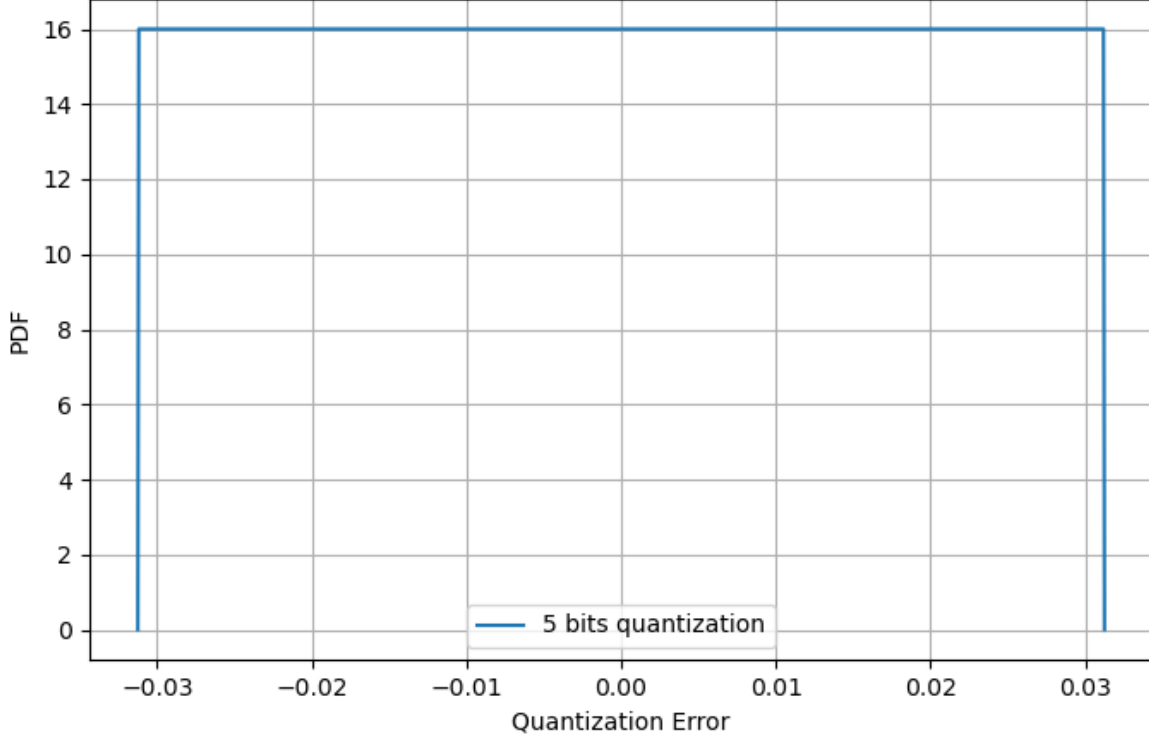
Fig. 2: plot of pdf of Quantization Error

So, we can calculate its mean power or variance as the 2nd moment of its distribution. Since, the distribution of error is uniform hence E[e]=0.

$$E[e^2] = \int_{-\frac{q}{2}}^{\frac{q}{2}} p_r(r = e)e^2 \, de \tag{13}$$

$$= \frac{1}{q} \cdot \frac{1}{3}\left(\left(\frac{q}{2}\right)^3 - \left(-\frac{q}{2}\right)^3\right) \tag{14}$$

$$= \frac{1}{3q} \cdot \frac{q^3}{4} \tag{15}$$

$$= \frac{q^2}{12} \tag{16}$$

$$\tag{17}$$

On putting $q = 2^{-(n-1)}$, we have:

$$E[e^2] \approx \frac{2^{-2n}}{3} \tag{18}$$

$$Pe \approx \frac{2^{-2n}}{3} \tag{19}$$

$$\tag{20}$$

Thus, an ideal ADC would have a signal-to-noise ratio

$$SNR = \frac{Ps}{Pe} = 1.5 \cdot 2^{2n} \tag{21}$$

or, expressed in decibels,

$$SNR = 1.76 + 6.02n \tag{22}$$

Plug in the values:

$$n = \frac{61.96 \text{ dB} - 1.76 \text{ dB}}{6.02 \text{ dB}} \approx 10 \text{ bits} \tag{23}$$

So, the resolution of the ADC is approximately 10 bits.