

EE22BTECH11049 - Shivansh Kirar

Question EC 31 2023

The signal-to-noise ratio (SNR) of an ADC with a full-scale sinusoidal input is given to be 61.96 dB. The resolution of the ADC is GATE EC 2023

Solution: The mean power in a sinusoidal input signal with unit amplitude is simply

$$P_s = \frac{1}{N} \int_0^T \sin^2(\omega t) dt \quad (1)$$

Quantized to 2^n discrete levels, equivalent to n bits, one quantization step q has a size of

$$|e| \leq \frac{q}{2} \text{ and therefore, } |e| \leq 2^{-n} \quad (2)$$

For a large enough number of quantization steps, the probability density function of the quantization error tends toward being flat 1. Its value within the quantization error bounds is

$$p_e \approx \frac{1}{q} \quad (3)$$

So, we can calculate its mean power or variance as the 2nd moment of its distribution:

$$P_e = \int_{-\frac{q}{2}}^{\frac{q}{2}} p(e)e^2 de \approx \frac{2^{-2n}}{3} \quad (4)$$

Thus, an ideal ADC would have a signal-to-noise ratio

$$SNR = \frac{P_s}{P_e} = 1.5 \cdot 2^{2n} \quad (5)$$

or, expressed in decibels,

$$SNR = 1.76 + 6.02n \quad (6)$$

In this formula:

- SNR is the signal-to-noise ratio in dB, which is given as 61.96 dB in our case.
- 1.76 dB is a constant that accounts for quantization noise.
- 6.02 dB is the noise bandwidth factor for a sinusoidal input.

Plug in the values:

$$\text{Resolution (in bits)} = \frac{61.96 \text{ dB} - 1.76 \text{ dB}}{6.02 \text{ dB}} \approx 10 \text{ bits} \quad (7)$$

So, the resolution of the ADC is approximately 10 bits.