For descending.out
L1d = 1kB

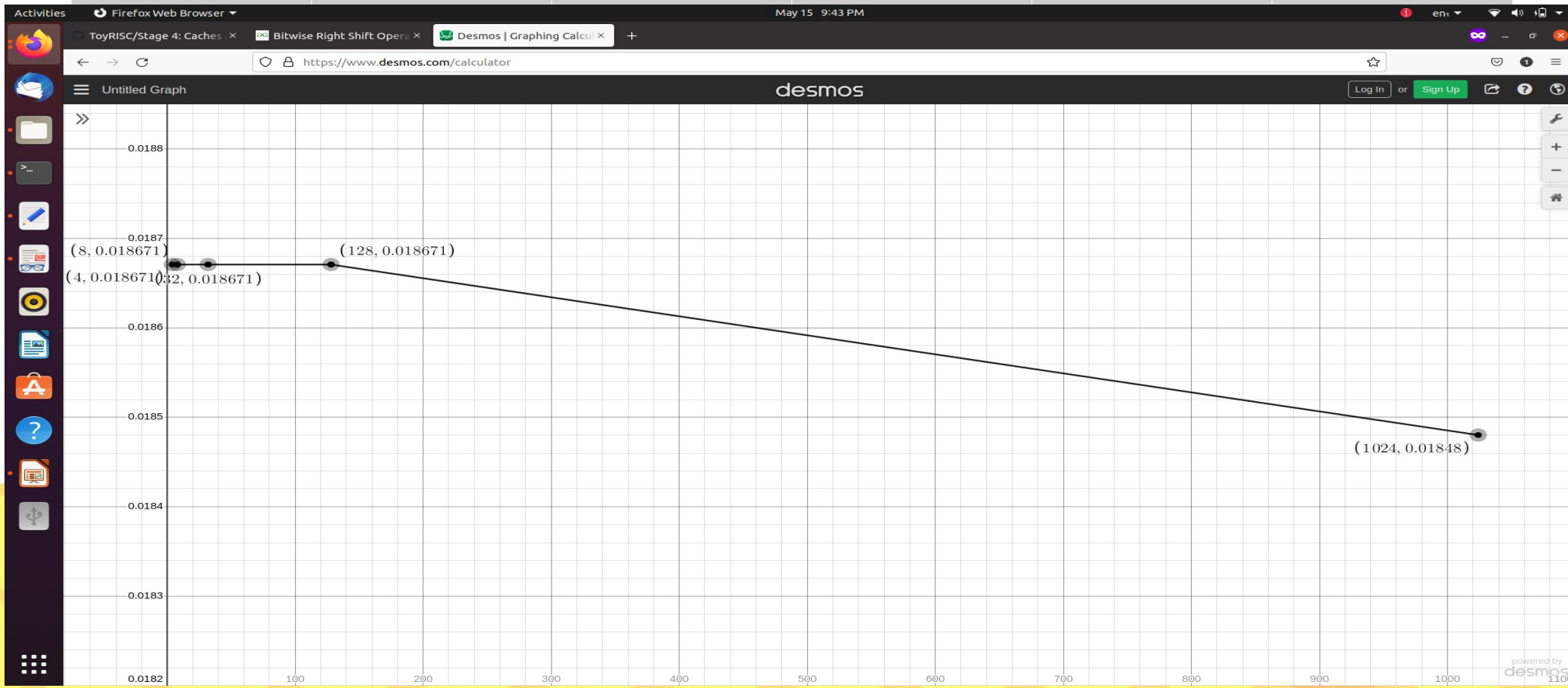| L1i	4 B	8 B	32 B	128 B	1024 B
Instruction	277	277	277	277	277
Cycle	12503	12729	12421	14085	14989
IPC	0.022154683	0.021761332	0.022300942	0.019666312	0.018480219
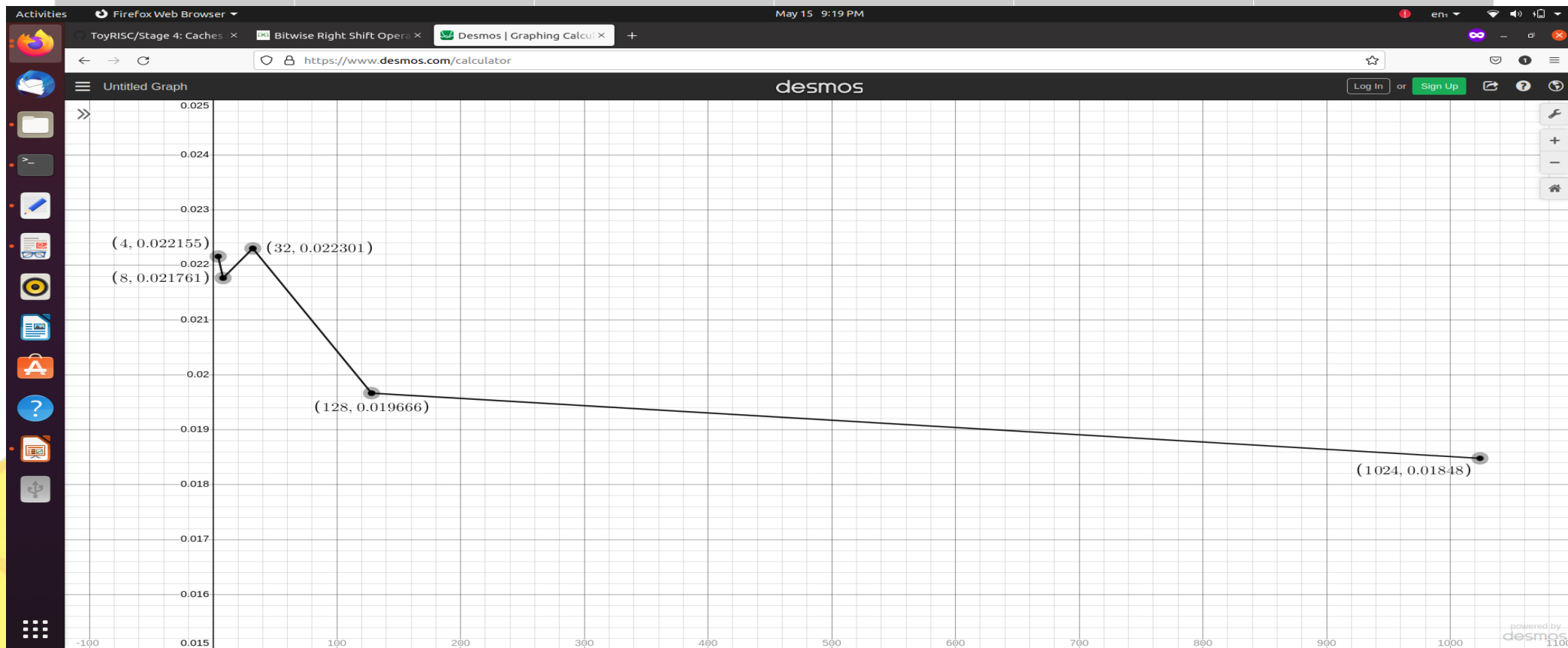
For descending.out
L1i = 1kB

L1d	4 B	8 B	32 B	128 B	1024 B
Instruction	277	277	277	277	277
Cycle	14836	14836	14836	14836	14989
IPC	0.018670801	0.018670801	0.018670801	0.018670801	0.018480219

For evenorodd.out
L1d = 1kB

L1i	4 B	8 B	32 B	128 B	1024 B
Instruction	6	6	6	6	6
Cycle	275	280	290	310	330
IPC	0.021818182	0.021428571	0.020689655	0.019354839	0.018181818
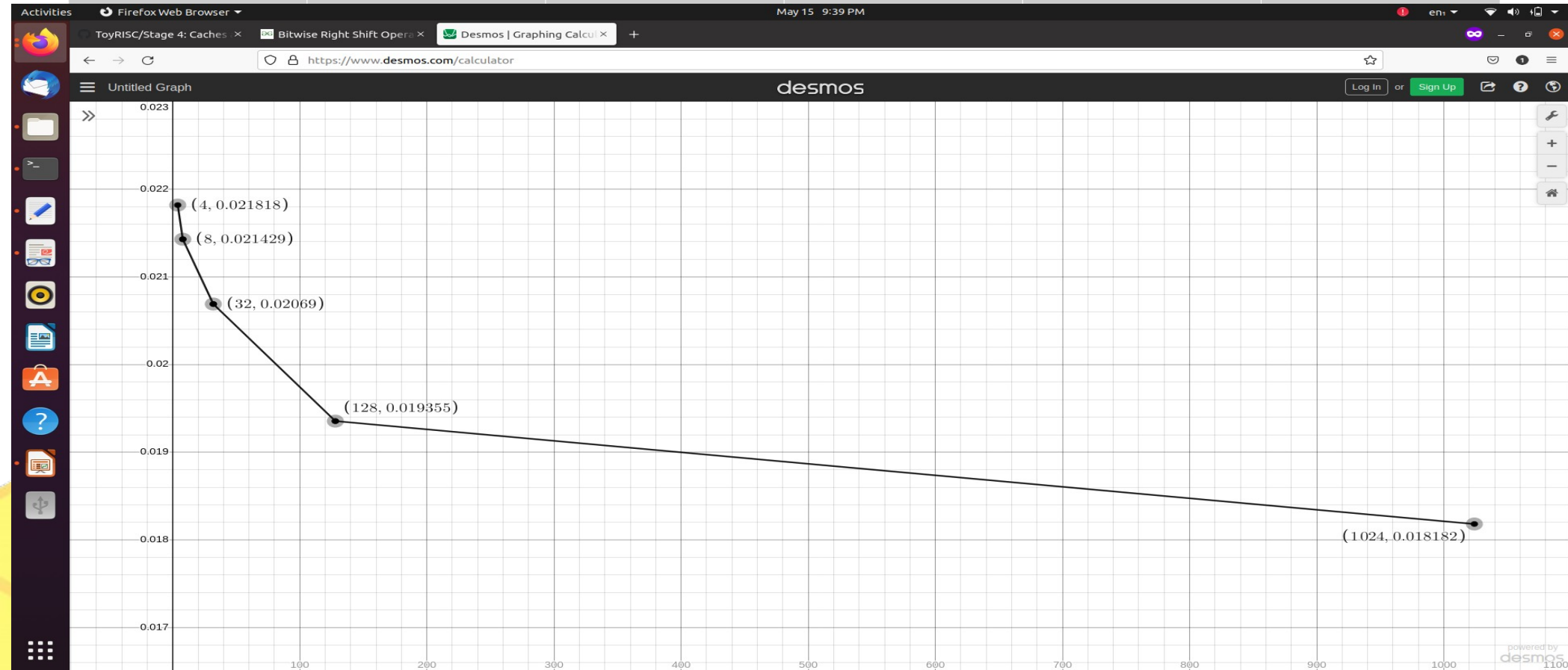
For evenorodd.out
L1i = 1kB

L1d	4 B	8 B	32 B	128 B	1024 B
Instruction	6	6	6	6	6
Cycle	327	327	327	327	330
IPC	0.018348623	0.018348623	0.018348623	0.018348623	0.018181818

Observation

- *On varying L1i-cache, while L1d-cache is constant*
  - IPC is decreasing on increasing *L1i-cache*

- *On varying L1d-cache, while L1i-cache is constant*
  - IPC is first constant than decreasing on increasing *L1d-cache*