Shivraj Muttagi

shivrajsm0219@gmail.com

+91-7204696349

Vijayapura,India

Career Objective

Seeking a progressive career which utilizes my technical and leadership skills with an opportunity for professional growth and excellence

Education

Technical Skills

B.E in Electronics and Communication Engineering

Year of Graduation : 2019

Result : 7.5 CPGA

Science in Higher Secondary School

Year of Passing : 2015

Result : 86.16 %

1. Assembly Languages : C , HDL .
2. Programming: JAVA ,JavaScript and Python
3. Backend: Node.js, Express
4. Frontend: CSS3 , React , Redux ,Context API
5. SQL Database : MYSQL
6. No-SQL : MongoDB

Presentation Skills

Training

1. Parasites and Power Analysis in CMOS VLSI

   Description: Explains tradeoff between time analysis , power consumption and size of integrated Chip.

2. DIGITAL TWINS

   Description :Represents the convergence of the physical and virtual world where industrial product will get a dynamic digital representation.

3. CYBERFLURO

   Description: Robotic garden which uses machine learning and motion sensors .

Unified Course on Java,Python and SQL

July 2019 - October 2019

Curriculum:

Core Java , Advanced Java , J2EE , Servlets MVC Architecture, Introduction to Spring Collections in python,Introduction to Data Science Libraries , SQL Database administration.

Academic Project

Design of Gilbert Mixer and its layout using Cadence Virtuoso Tool

Description:

Popular Circuit required for inherent matching of transistors when designing on the integrated chip resulting in most common topology for designing mixers on the integrated chip.

Certifications

1. CMOS Digital VLSI Design (NPTEL-61%)
2. The Complete 2020 Web Development Bootcamp using MERN(Udemy)
3. Blockchain Essentials(IBM)
4. Information Security Awareness(Infysec)

Internships

1. CMOS Analog Circuit Design using Cadence Virtuoso Tool(3 months)
   Company:Terox R&D and Consultancy Services Pvt LTD.
   Description: Internship includes integrated chip design namely Schematic , Simulation & Layout. The Layout verification of analog circuit have considered parameters such as DRC,ERC and RC Extraction.
2. Advanced Digital Switching Systems & Optical Fiber Communication

Interpersonal Skills

1. Professional English
2. Leadership Quality
3. Aspirant to new innovative ideas

Social Links

LinkedIn:https://www.linkedin.com/in/shivraj-muttagi-3a88b1137/