



Certificate no: UC-57594d18-2ac7-4ac8-89a1-4e539eca5f8f

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Reference Number: 0004

CERTIFICATE OF COMPLETION

Verification Series Part 1 : SystemVerilog Fundamentals

Instructors **Kumar Khandagle**

Shiva Prasad Anjanappa

Date **Dec. 25, 2024**

Length **14.5 total hours**