

## DD2360 Assignment 3

Group 35

Shiyuan Shan

Github repository: [ShiyuanShan/DD2360HT23 \(github.com\)](https://github.com/ShiyuanShan/DD2360HT23)

Exercise 1:

### 1.1

- 1) Atomic operations are used to update histogram bins in shared memory to avoid race conditions when multiple threads write to the same bin simultaneously.
- 2) The loops that initialize shared memory bins in `histogram_kernel` and clean up bins in `convert_kernel` are unrolled. Loop unrolling can potentially improve instruction throughput by reducing loop overhead.
- 3) All `threadDim.x` can be replaced by the declared constant.

1.2 1) and 2) are the two major optimizations taken. 3) is not taken because it turns the code harder to read.

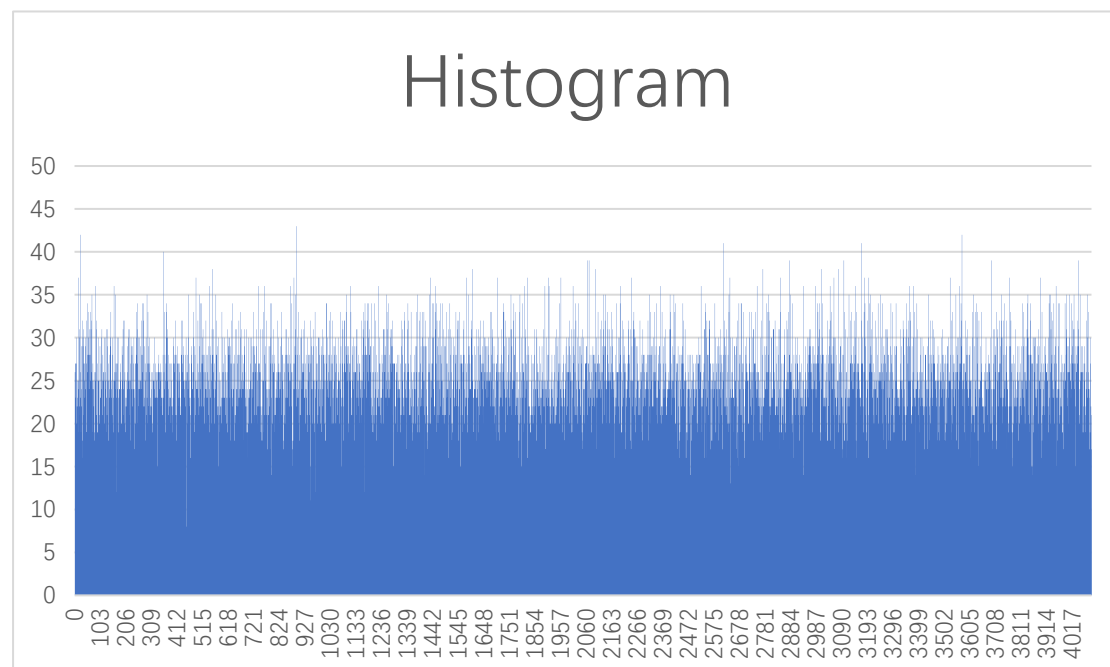
1.3 In total `input_length` global memory reads. Each element is visited only once.

1.4 In total `input_length + num_bins` atomic adds. Each element is categorized only once, and each shared bin is copied to the global bin once in the end.

1.5 In total `num_bins * sizeof(unsigned int)` shared memory. Bin size should be equal to the global bin size.

1.6 If every element in the input array has the same value, each thread within a block will attempt to update the same bin in the shared `_bins` array. This leads to contention, as multiple threads are competing to atomically increment the count in the same bin, resulting in low computation parallelism.

1.7 Input length is 100000, 256 threads per block and in total 391 blocks.



1.8 code `/usr/local/cuda-11/bin/nv-nsight-cu-cli` is not working any more on google colab. I guess it is because google colab has a revised version of cuda now.

## Exercise 2:

### 2.1

I use google colab for this exercise. Delete all “arch” related code in makefile.

Lines used for simulation are:

```
%cd sputniPIC/  
!mkdir data  
!make  
!./bin/sputniPIC.out ./inputfiles/GEM_2D.inp
```

2.2 In mover\_PC\_gpu() is a parallelized version of mover\_PC() by having part->nop threads, which means one kernel for one particle each time. The original for (int i = 0; i < part->nop; i++) loop gets unrolled.

2.3 Results are the same.

### 2.4

CPU:

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Tot. Simulation Time (s) = 81.7802

Mover Time / Cycle (s) = 3.4422

Interp. Time / Cycle (s) = 4.44722

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GPU:

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Tot. Simulation Time (s) = 50.7898

Mover Time / Cycle (s) = 0.228208

Interp. Time / Cycle (s) = 4.55658

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