

Module name	LUT	FF	IO	BUFG
regm	121	48	113	1
Uart_Bps_Module	17	14	4	1
Uart_Tx_Module	10	17	14	1
a429_rx_iface	42	93	39	1
a429_tx_iface	74	56	47	2
ae18_core	959	446	96	1
spi_master	48	62	26	3
dmx_dpram	7462	4112	53	1
dmx_rx	134	141	48	1
dmx_tx	66	66	59	1
fht	14	30	18	1
qadd	157	16	96	1
qdiv	142	132	99	1
anb_master	3770	345	281	1
gng_ctg.v	101	241	68	1
gng_coef	200	53	62	1
gng_lzd	51	0	67	0
registerInterface	22	40	90	1
ima_adpcm_dec	123	51	49	1
ima_adpcm_enc	220	85	48	1
equivalence_resolver	78	19	232	1
feature_accumulator	130	54	82	1
table_reader	127	85	252	1
fht_bfly	20	16	37	1
signed_mult_const_asic	34	8	20	1
ahb	88	4	556	1
ahb_fifo_entry	47	45	113	1
ahb_fifo_counter	91	33	36	1
ahb_fifo	74	5	126	1
apb_bridge	170	104	433	1
clk_aligner	22	11	75	1
gpio_apbif	49	64	139	1
gpio_ctrl	48	33	126	2