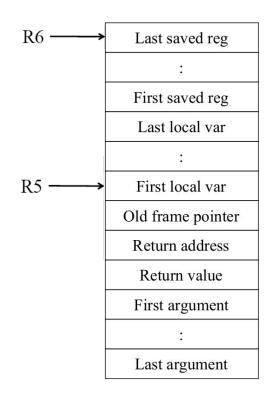
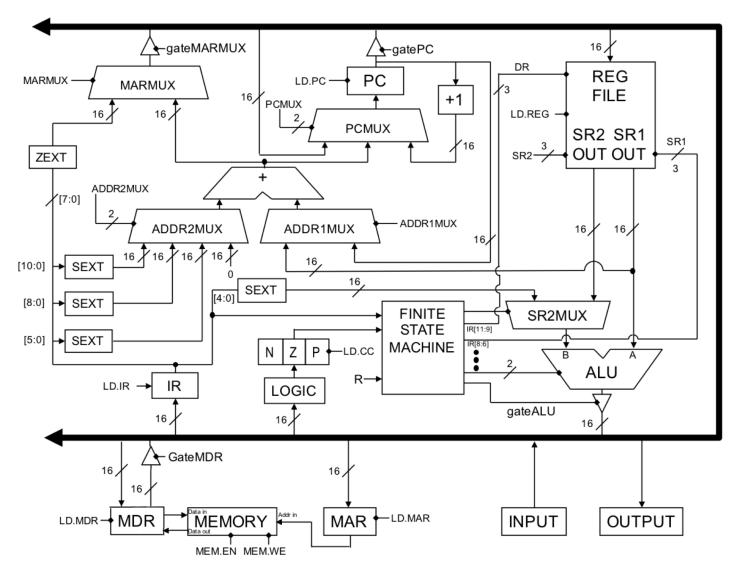
CS2110 REFERENCE SHEET

ADD	0001	DR	SR1	0	00	SR2
ADD	0001	DR	SR1	1	in	nm5
AND	0101	DR	SR1	0	00	SR2
AND	0101	DR	SR1	1	ir	nm5
BR	0000	n z p	, , ,	C	offsetS	
JMP	1100	000	BaseR		000	000
JSR	0100	1	PCc	offs	et11	
JSRR	0100	0 00	BaseR		000	000
LD	0010	DR	, , ,	C	offset9	
LDI	1010	DR		C	offset9	
LDR	0110	DR	BaseR		offs	et6
LEA	1110	DR		C	offsetS	
NOT	1001	DR	SR		111	111
ST	0011	SR	, , ,	C	offset9	
STI	1011	SR	, i	C	offset9	
STR	0111	SR	BaseR		offs	set6
TRAP	1111	0000		tra	apvec	t8

Trap Vector	Assembler Name
x20	GETC
x21	OUT
x22	PUTS
x23	IN
x25	HALT

Device Register	Address
Keybd Status Reg	xFE00
Keybd Data Reg	xFE02
Display Status Reg	xFE04
Display Data Reg	xFE06





Boolean Signals				
LD.MAR	GateMARMUX			
LD.MDR	GateMDR			
LD.REG	GatePC			
LD.CC	GateALU			
LD.PC	LD.IR			
MEM.EN	MEM.WE			

MUX Name	Possible Values
ALUK	ADD, AND, NOT, PASSA
ADDR1MUX	PC, BaseR
ADDR2MUX	ZERO, offset6, PCoffset9, PCoffset11
PCMUX	PC+1, BUS, ADDER
MARMUX	ZEXT, ADDER
SR2MUX	SR2, SEXT