Experiment number	Experiment name	Page number
1	Verify the operation of basic logic gate. (AND, OR, NOT)	
2	Implementation of NAND and NOR gate using basic gate	
3	Verify the universality of NAND gate	
4	Verify the universality of NOR gate	
5	Verify the operation of XOR and XNOR gate using basic gate	
6	Design and implement of Half Adder Circuit and verify it's operation	
7	Design and implement of Full Adder Circuit and verify it's operation	
8	Verify the following equation: $\bar{A}BCin + A\bar{B}Cin + AB\bar{C}in + ABCin$	
9	Design and implementation of half sub tractor circuit and verify it's operations.	
10	Design and construct and observation of J-K flip-flop	