



Department Of Computer Science and Engineering
Institute of Science & Technology, National University
Lab Report

Submitted To:

Teacher's Name	Lutfi Habiba
Designation	Lecturer
Course Title	Digital System Design Lab
Course Code	510222
Date of Submission	

Submitted By:

Name	Md Shoaibur Islam
Roll	23090
Semester	2 nd
Registration No	22502004662
Date of Submission	

Index

Experiment No	Experiment Name	Page No.
1	Verification of Ohm's Law for resistance used in laboratory.	
2	Verification of Kirchhof's Voltage Law (KVL).	
3	Verification of Kirchhof's Current Law (KCL).	
4	Verification of Superposition Theorem.	
5	Determine the V-I characteristics of a P-N junction diode	
6	Determine the V-I characteristics of a zener diode.	
7	Verification of voltage division rule (VDR).	