



Department Of Computer Science and Engineering
Institute of Science & Technology, National University
Lab Report

Submitted To:

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| Teacher's Name | Md. Shamim Hasan |
| Designation | Lecturer |
| Course Title | Electrical and Electronic Circuit Lab |
| Course Code | 510204 |
| Date of Submission | 10 November 2024 |

Submitted By:

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| 1 | Verification of Ohm's Law for resistance used in laboratory. | |
| 2 | Verification of Kirchhof's Voltage Law (KVL). | |
| 3 | Verification of Kirchhof's Current Law (KCL). | |
| 4 | Verification of Superposition Theorem. | |
| 5 | Determine the V-I characteristics of a P-N junction diode | |
| 6 | Determine the V-I characteristics of a zener diode. | |
| 7 | Verification of voltage divided rule (VDR). | |