VE311 Electronic Circuit Homework 8

Due: Jul 22nd 11:59a.m.

Note:

- 1) Please use A4 size paper or page.
- 2) Please clearly state your final result for each question.

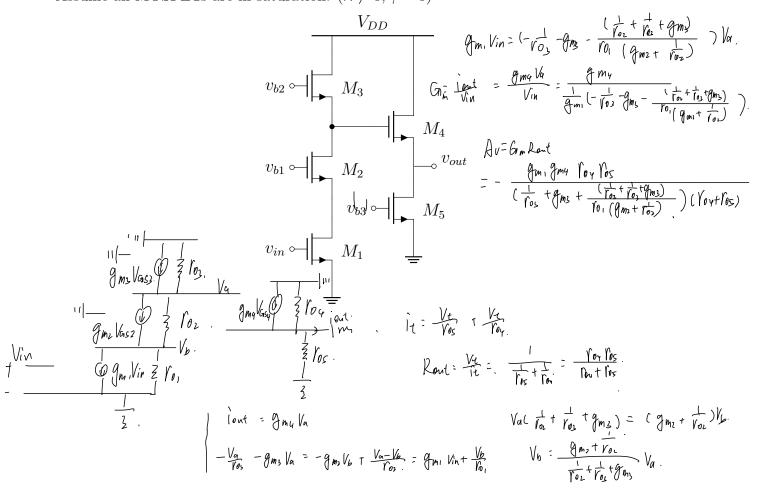
Many years ago, there was a student named Watt Nobody, who was enrolled in ECE3110J course. The course was very interesting, but hard to understand. Mr. Nobody found himself hard to grasp the cutting-edge directions.

HW8 would be due at noon the next day, yet he still had no clue where to begin. Suddenly, Watt Nobody's Apple Pencil broke and could no longer write. With less than a day left before the deadline, there was no time to buy a new one. So, Watt decided to seek help from Professor Xuyang Lu.

After inspection, Xuyang told him that the issue was likely a malfunction in the amplifier module of the Apple Pencil, which prevented the output signal generated by the tip's pressure sensor from being properly amplified. As a result, Watt decided to redesign the amplifier module using MOSFETs.

Question 1. Cascode Amplifier

He tried to cascade the cascode amplifier with a source follower first. Please help him derive the expression of voltage gain A_v and output impedance R_{out} of the cascaded circuit. Assume all MOSFETs are in saturation. ($\lambda \neq 0, \gamma = 0$)



After reviewing the first design proposal, Xuyang pointed out that while it could provide high gain and low output impedance, it might not be suitable for mobile devices due to limited space, high power consumption, and insufficient output swing.

As a result, Watt Nobody decided to modify the design by folding the cascode structure to optimize performance within the constraints. Since Watt wasn't very familiar with this approach, he first designed a simpler two-MOSFET circuit to test the concept.

Question 2. Folded Cascode

A cascode MOSFET structure can be converted to its equivalent folded cascode topology. The PMOS and current source I_2 replace an NMOS in the simple cascode structure.

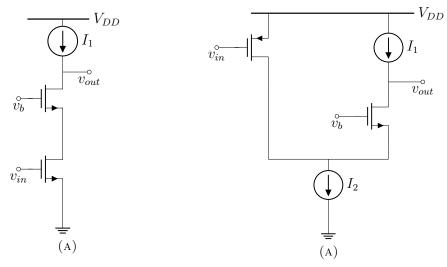
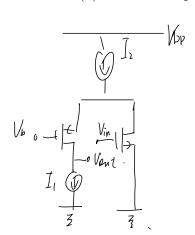
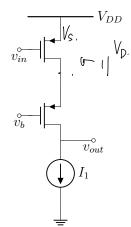


FIGURE 1. Cascode Structure

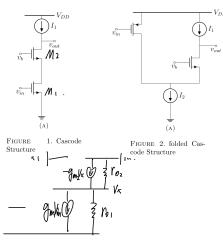
FIGURE 2. folded Cascode Structure

(a) Draw the equivalent folded cascode structure for the PMOS cascode structure.





(b) Find the gain for small-signal and R_{out} of the cascode Figure. 1 and folded-cascode Figure. 2. Assume that the current sources are ideal. Assume you already know g_m and r_o for each MOSFET, you don't need to consider the body effect.



In Figure 2, it's the same as that in figure 1.

As
$$V_{DD}$$

The signal model is equivalent in figure 1.

The structure V_{DD}

The signal model is equivalent in figure 1.

The structure V_{DD}

The signal model is equivalent in figure 1.

The figure 2 it's the same as that in figure 1.

And V_{DD}

The signal model is equivalent in figure 1.

The figure 1 it's the same as the signal model is equivalent in figure 1.

$$\hat{I}_{t} = -q_{p} y_{k} + \frac{V_{t} \cdot V_{x}}{Y_{02}} = \frac{V_{x}}{Y_{01}}.$$

$$= \frac{V_{t}}{Y_{01}} = (\frac{1}{Y_{01}} + \frac{1}{Y_{02}} + q_{102}) V_{x},$$

$$V_{t} = Y_{02} (\frac{1}{Y_{01}} + \frac{1}{Y_{01}} + q_{102}) V_{x},$$

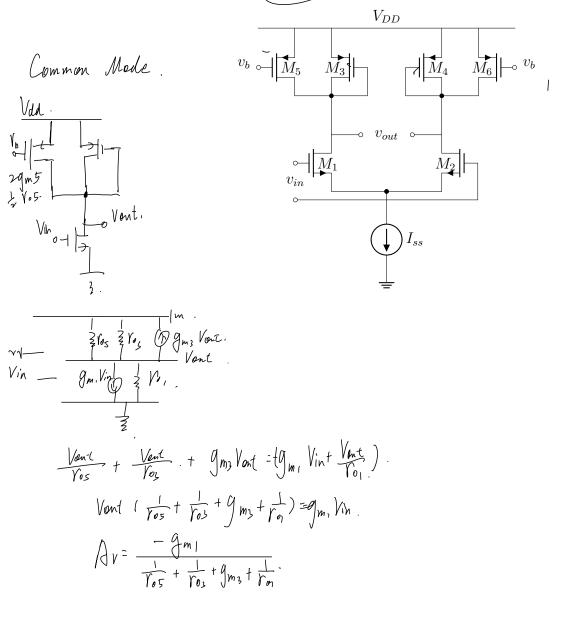
$$V_{t} = \frac{V_{02}}{\hat{I}_{t}} = \frac{V_{01}}{\hat{I}_{t}} = \frac{V_{01}}{\hat{I}_{t}} = \frac{V_{01}}{\hat{I}_{t}} = \frac{V_{01}}{\hat{I}_{t}} = \frac{V_{01}}{\hat{I}_{t}} + q_{102} +$$

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Xuyang was pleased with the design but pointed out that, as a single-ended circuit, it might suffer from noise susceptibility. To improve noise immunity, he suggested Watt consider switching to a differential amplifier configuration.

Question 3. Differential Circuit

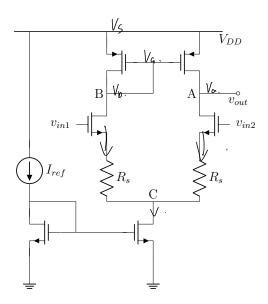
So Watt Nobody raised out the new design. Consider the following differential circuit. M_1 is same as M_2 , M_3 is same as M_4 , M_5 is same as M_6 . Calculate differential gain of the circuit using variables including g_m and r_o . Consider the channle length modulation for all MOSFETs and ignore body effect. Half-circuit Method is recommended.



Xuyang praised Watt Nobody's progress, saying, "This is already excellent, but there's room for optimization. In practice, we don't always need to pursue the highest gain, while sacrificing some gain in exchange for better linearity, stability, and improved CMRR can be a worthwhile trade-off."

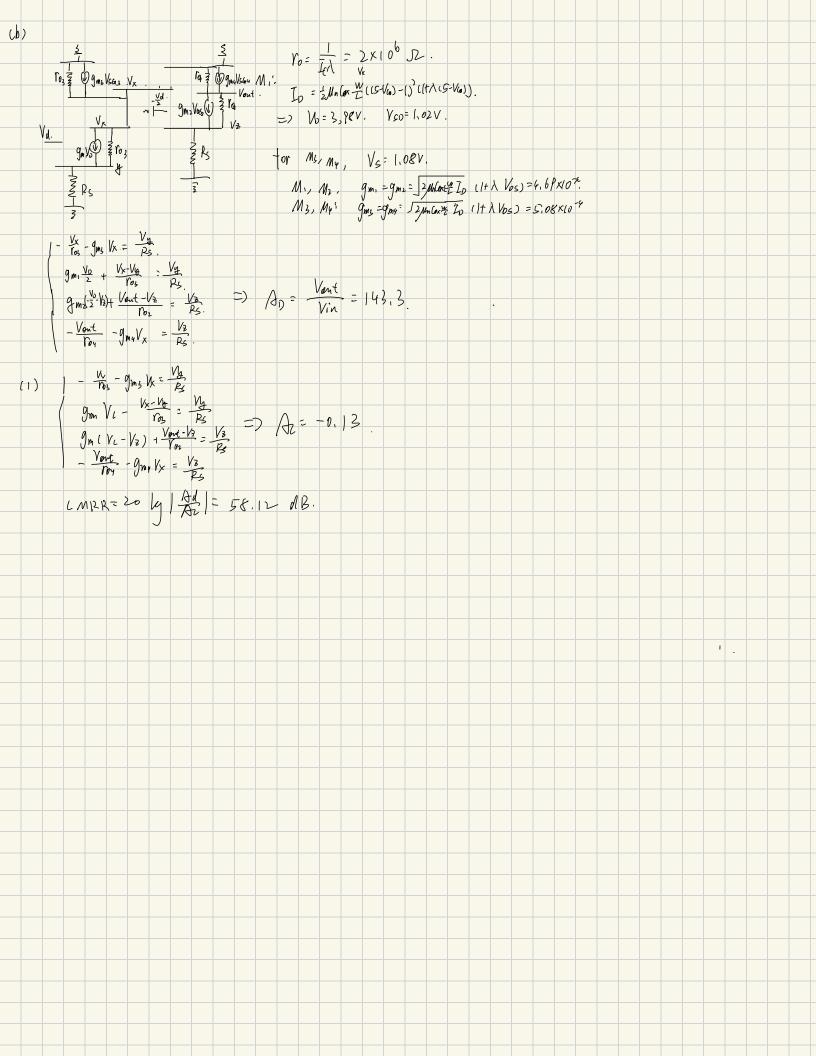
Watt Nobody had a lightbulb moment and proceeded to enhance the differential input pair by adding Source Degeneration R_S .

Question 4. Differential Pairs with Source Degeneration



Assume All MOSFET have identical $\frac{W}{L}$ and $\frac{1}{2}\mu_n C_{ox} \frac{W}{L} = \frac{1}{2}\mu_p C_{ox} \frac{W}{L} = 10mA \cdot V^2$, $\lambda = 0.1V^{-1}$, $V_{TH} = 1V$, $R_s = 10k\Omega$, $I_{ref} = 10\mu A$, $V_{DD} = 5V$

- (a). Determine the DC currents and voltages at A and B. Ignore channel length modulation for this question only.
- (b). Determine the differential gain for the small signal by using the half circuit. C can be regarded as the ground for AC small signals.
- (c). Determine the common mode gain for a common mode small signal input and derive the value of CMRR.



In the end, Watt Nobody successfully repaired his Apple Pencil and finished HW8 on time. Thanks to this experience, Nobody no longer found 311 as suffering and ultimately achieved an excellent grade in the course.

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