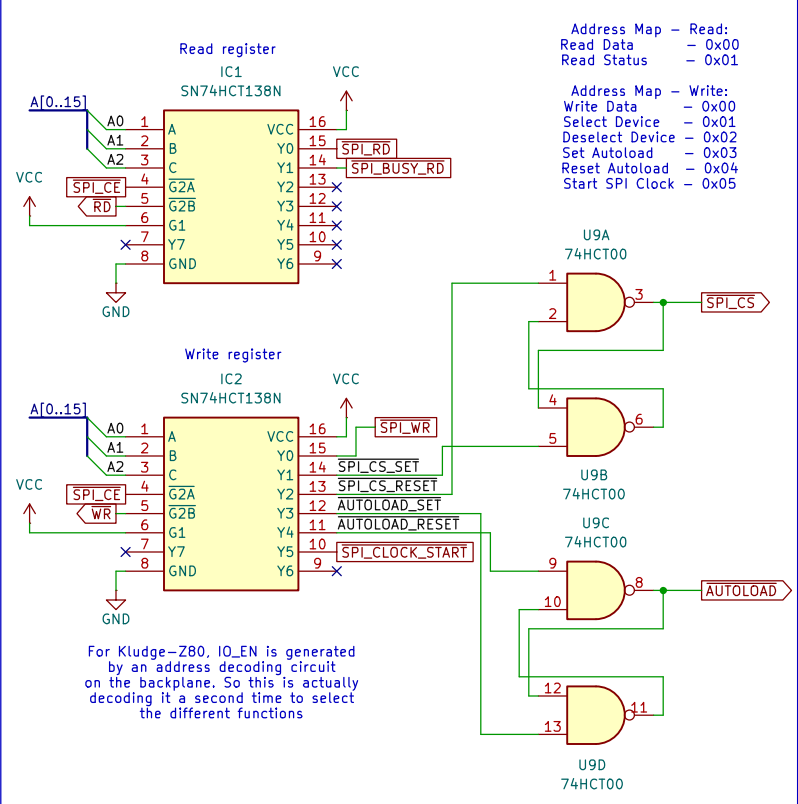
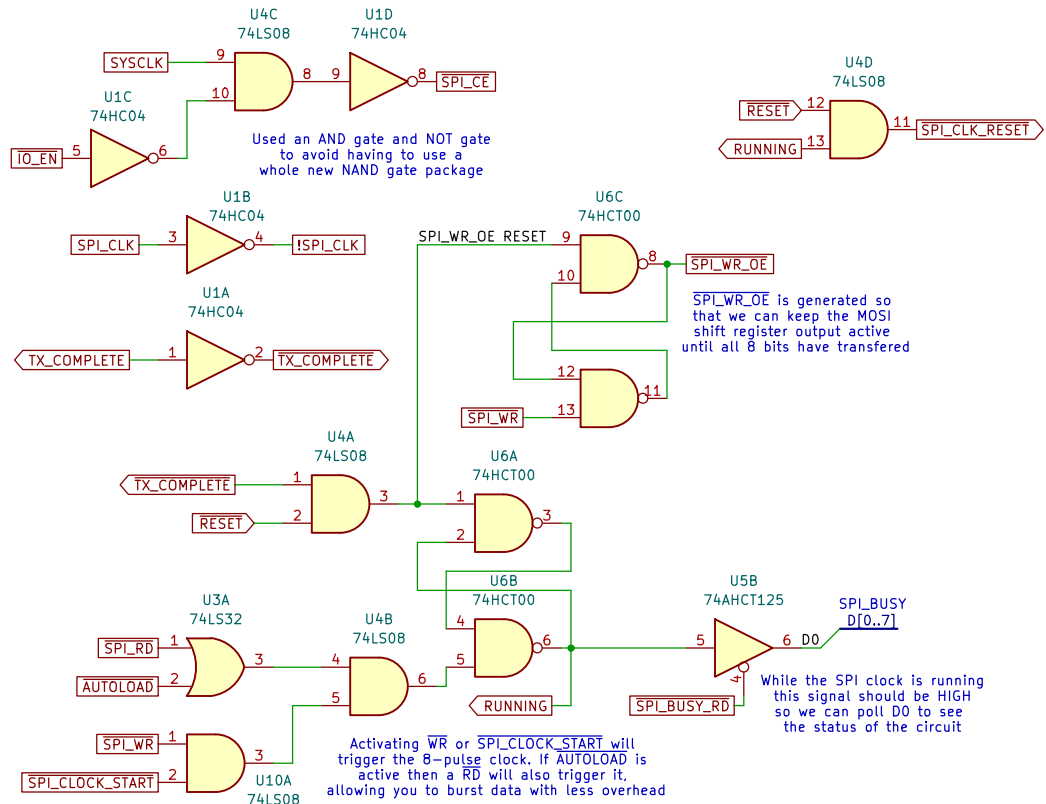
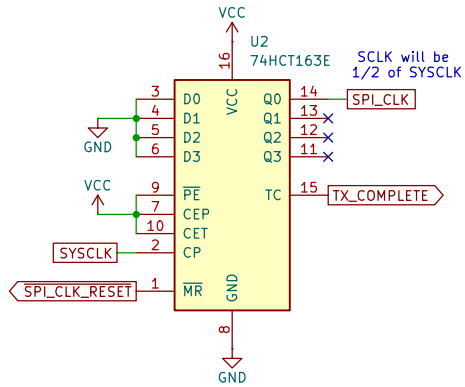


8-Bit Parallel to SPI Circuit

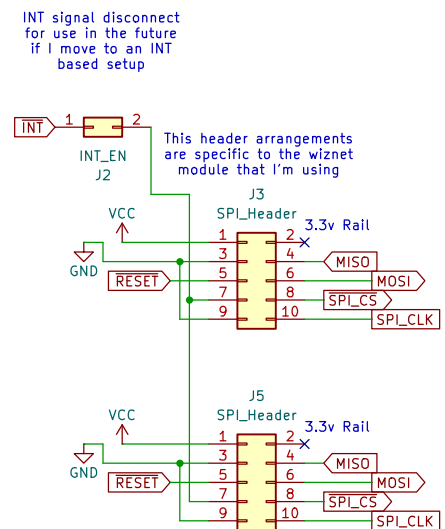
Control and status signals

Register and function select

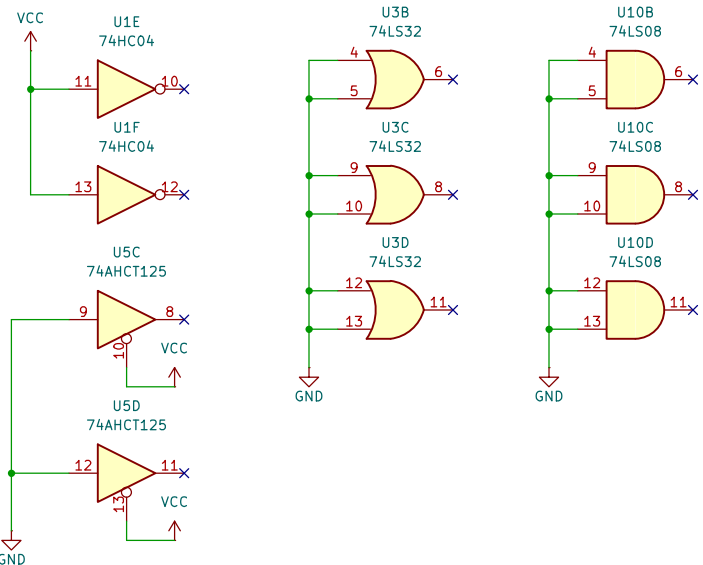
SPI Clock Circuit



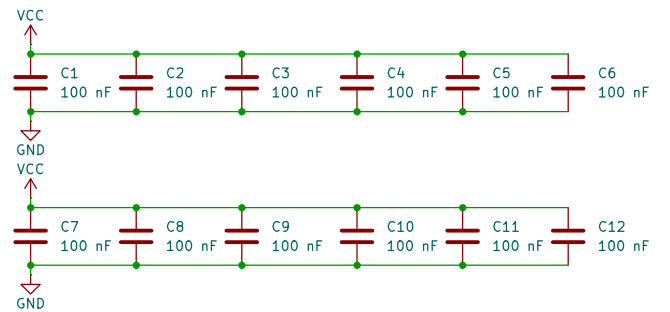
SPI Header



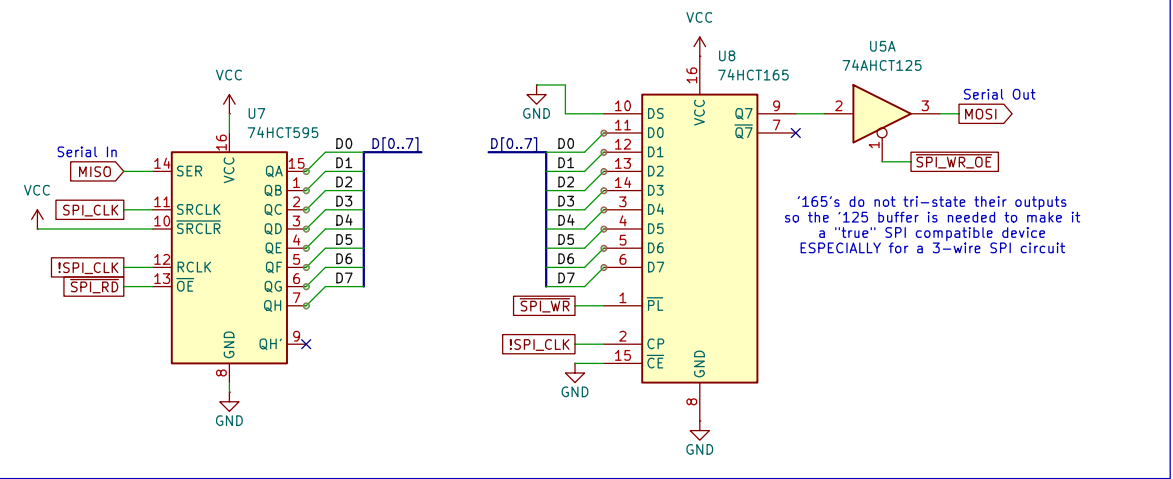
Unused Gates



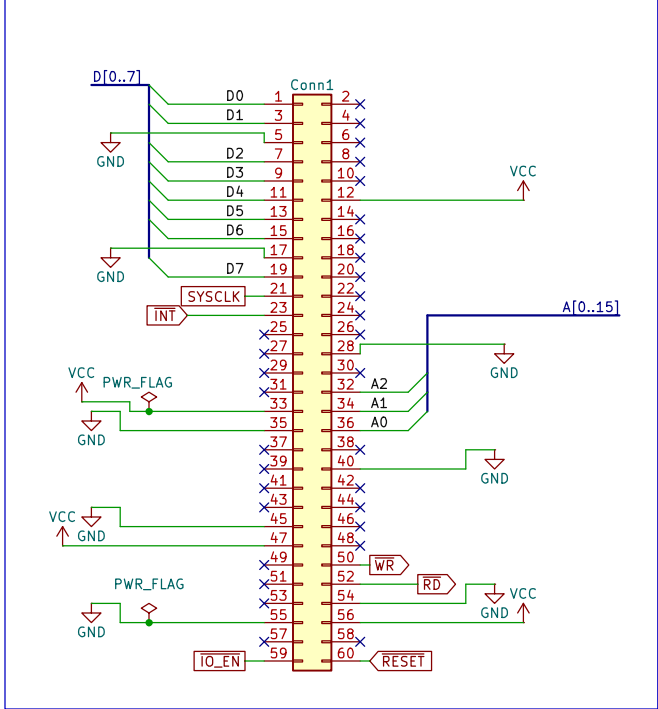
IC decoupling capacitors



MISO/MOSI



Backplane Bus



VCC/GND Connections

