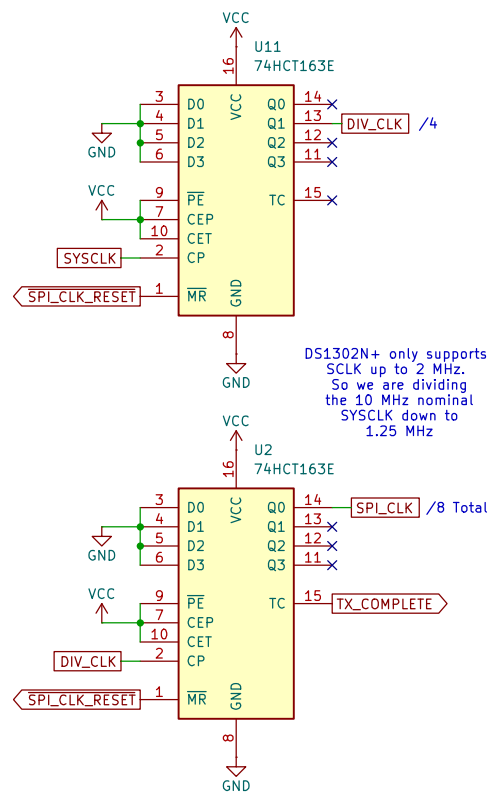
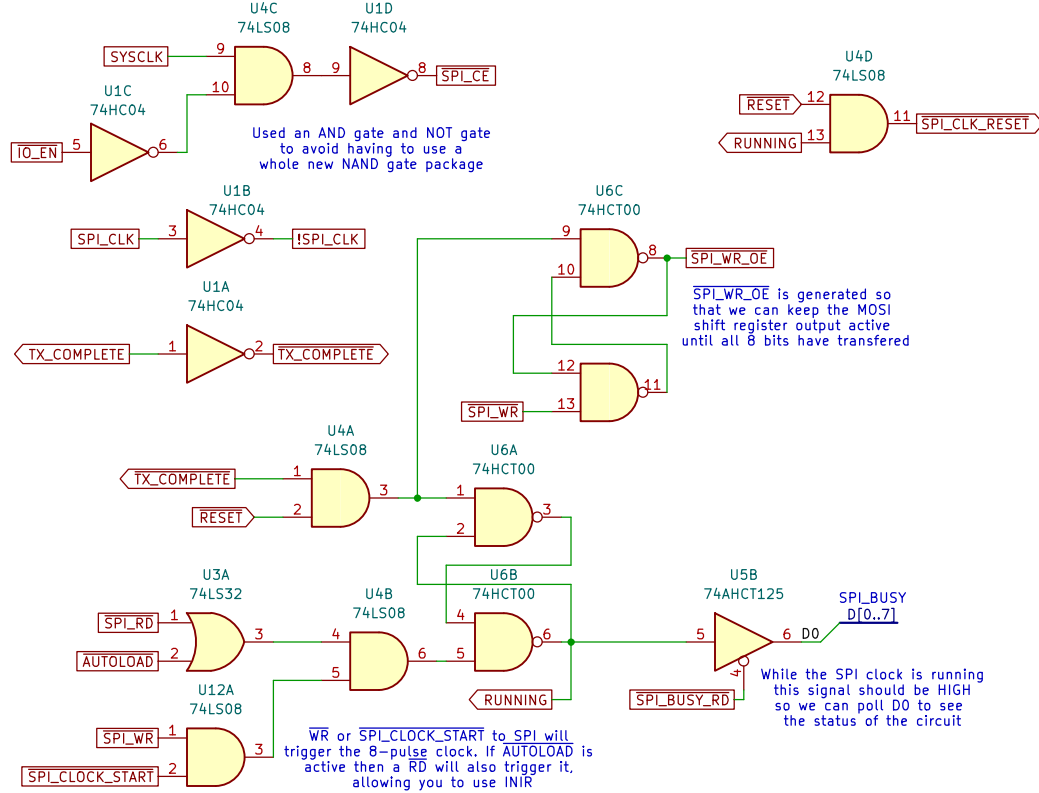


8-Bit Parallel to SPI Circuit

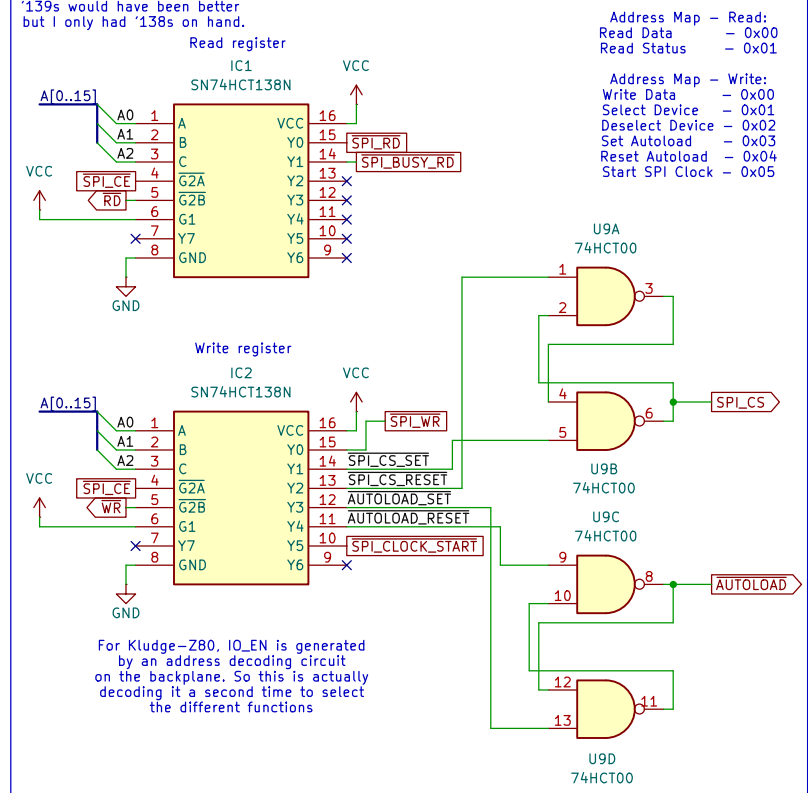
SPI Clock Circuit



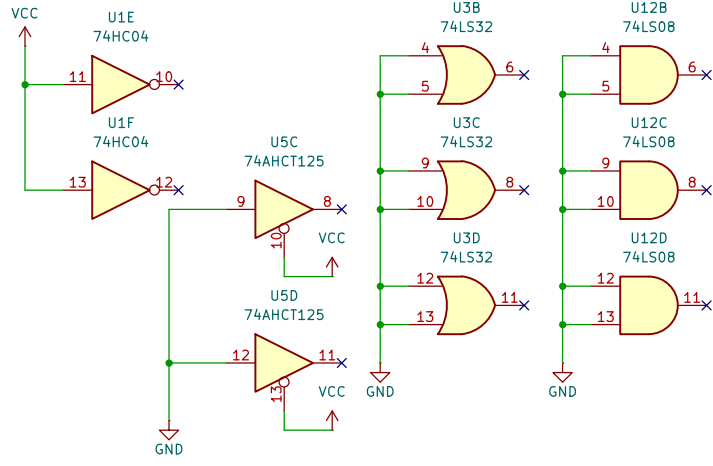
Control and status signals



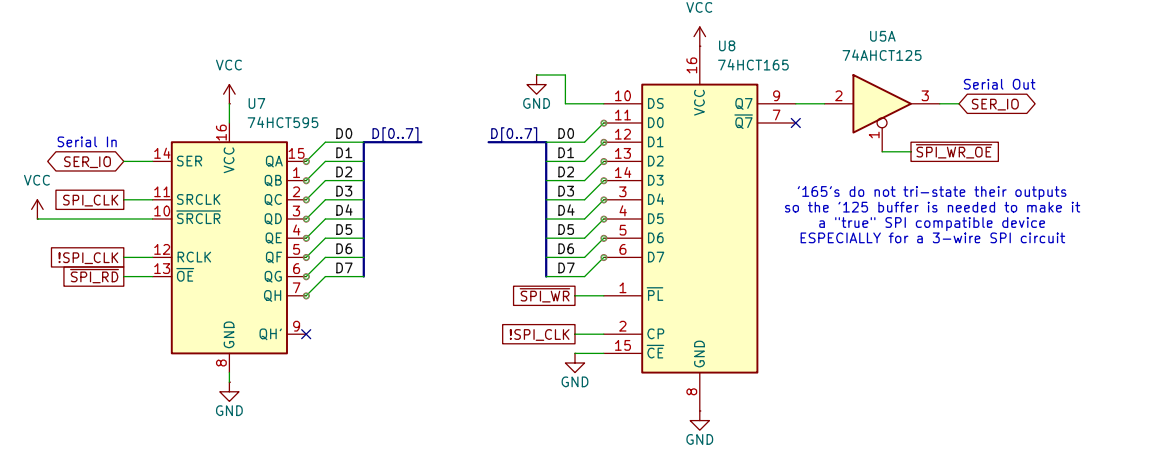
Register and function select



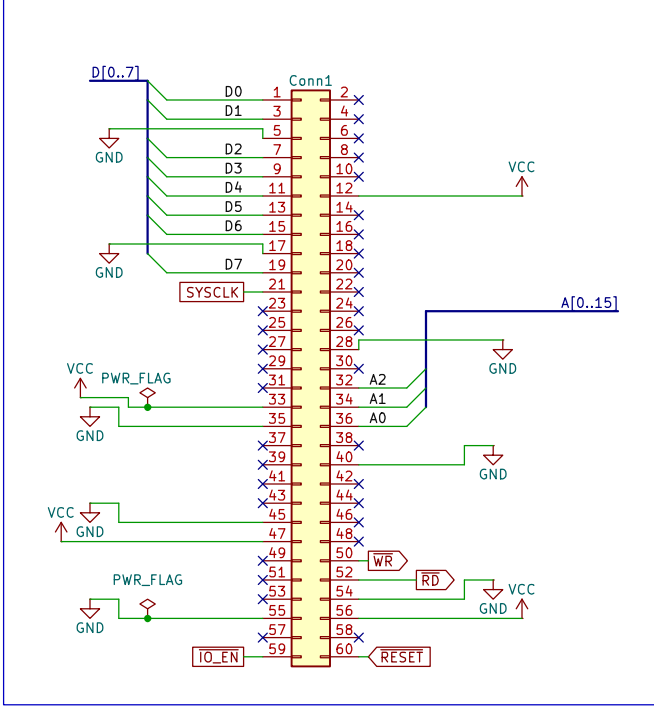
Unused gates



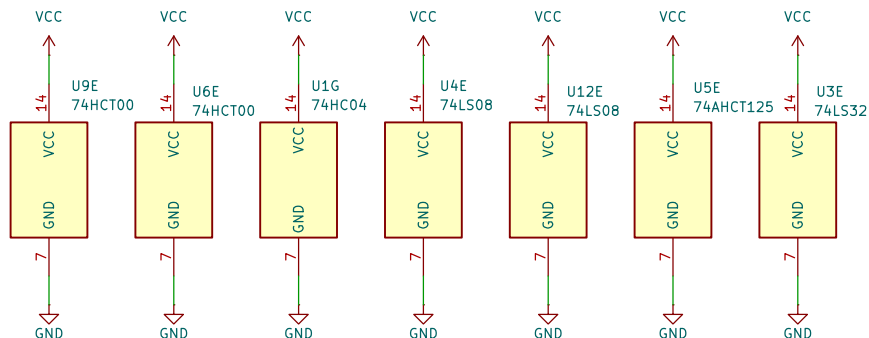
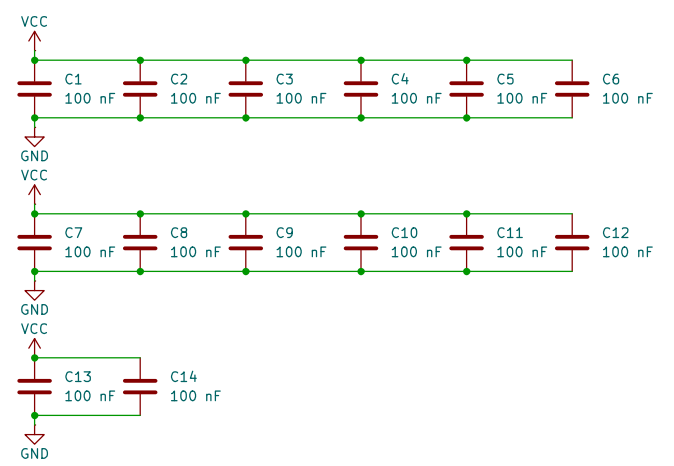
MISO/MOSI



Backplane Bus



IC decoupling capacitors



SPI Device

