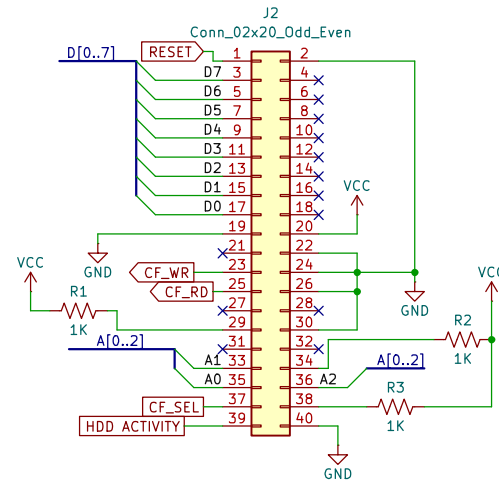
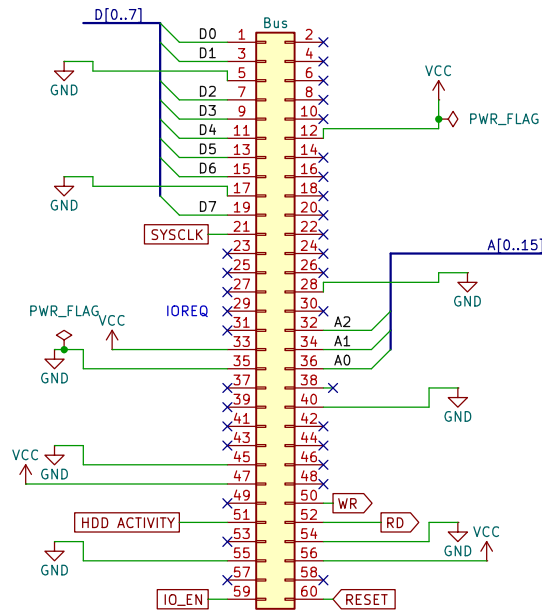
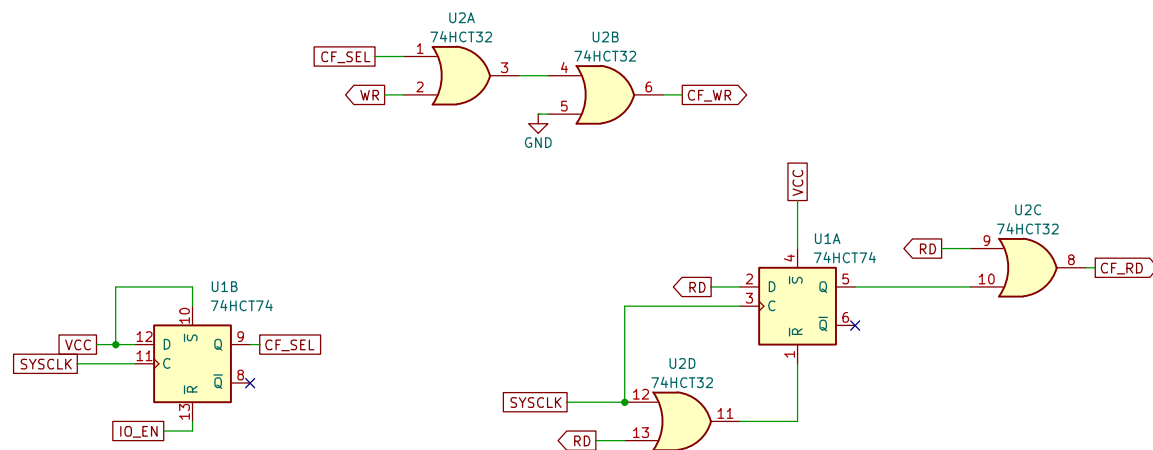


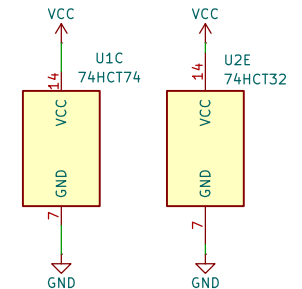
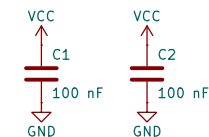
Based on Tadeusz Pycio's
compact flash circuit for RC2014
<http://www.vtsys.pl/interface-compact-flash/>



Signal delay circuit
Needed for full CF standard compatibility



Decoupling Caps - All ICs



| | |
|-------------------------------|----------|
| Sheet: / | |
| File: ide_card.kicad_sch | |
| Title: Kludge IDE Card | |
| Size: A4 | Date: |
| KiCad E.D.A. 8.0.4 | Rev: 1.0 |
| | Id: 1/1 |