

	Port						Reserved Pins		Features			Pin name	Package Pin		
	1b	4b	8b	16b	32b	Link	QSPI	RGMII	I/O rail	BANK	Drive mA		TQ128	PCB Naming	Code Naming
Tile 0	P1A0								IOL	0	4	X0D00	30		
	P1B0					xlink3_tx2	SS		IOL	0	4	X0D01	27	QSPI_SS	PORT_SQI_CS
		P4A0	P8A0	P16A0	P32A20				IOL	0	4	X0D02	37	TILE0_SPARE1	
		P4A1	P8A1	P16A1	P32A21				IOL	0	4	X0D03	38	TILE0_SPARE2	
		P4B0	P8A2	P16A2	P32A22		IO0		IOL	0	4	X0D04	31	QSPI_D0	PORT_SQI_SIO
		P4B1	P8A3	P16A3	P32A23		IO1		IOL	0	4	X0D05	33	QSPI_D1	PORT_SQI_SIO
		P4B2	P8A4	P16A4	P32A24		IO2		IOL	0	4	X0D06	34	QSPI_D2	PORT_SQI_SIO
		P4B3	P8A5	P16A5	P32A25		IO3		IOL	0	4	X0D07	35	QSPI_D3	PORT_SQI_SIO
		P4A2	P8A6	P16A6	P32A26				IOL	0	4	X0D08	39	TILE0_SPARE3	
		P4A3	P8A7	P16A7	P32A27				IOL	0	4	X0D09	40	TILE0_SPARE4	
	P1C0					xlink3_tx3	QSCLK		IOL	0	4	X0D10	28	SPI_CLK	PORT_SQI_SCLK
	P1D0								IOL	0	4	X0D11	32	CLK_SRC_SEL	PORT_CLK_SRC_SEL
	P1E0								IOR	1	4	X0D12	62	MIC_CLK	PORT_PDM_CLK
	P1F0								IOR	1	4	X0D13	63		
		P4C0	P8B0	P16A8	P32A28				IOR	1	4	X0D14	57	MC_01_DATA	PORT_PDM_DATA_0_to_7
		P4C1	P8B1	P16A9	P32A29				IOR	1	4	X0D15	58	MC_02_DATA	PORT_PDM_DATA_0_to_7
		P4D0	P8B2	P16A10		xlink4_rx4			IOR	1	4	X0D16	68	MC_03_DATA	PORT_PDM_DATA_0_to_7
		P4D1	P8B3	P16A11		xlink4_rx3			IOR	1	4	X0D17	69	MC_04_DATA	PORT_PDM_DATA_0_to_7
		P4D2	P8B4	P16A12		xlink4_rx2			IOR	1	4	X0D18	70	MC_05_DATA	PORT_PDM_DATA_0_to_7
		P4D3	P8B5	P16A13		xlink4_rx1			IOR	1	4	X0D19	71	MC_06_DATA	PORT_PDM_DATA_0_to_7
		P4C2	P8B6	P16A14	P32A30				IOR	1	4	X0D20	59	MC_07_DATA	PORT_PDM_DATA_0_to_7
		P4C3	P8B7	P16A15	P32A31				IOR	1	4	X0D21	61	MC_08_DATA	PORT_PDM_DATA_0_to_7
	P1G0								IOR	1	4	X0D22	64		
	P1H0								IOR	1	4	X0D23	66		
	P1I0					xlink7_rx0			IOR	2	4	X0D24	88		
	P1J0					xlink7_tx0			IOR	2	4	X0D25	89		
		P4E0	P8C0	P16B0		xlink7_tx3			IOR	2	4	X0D26	93	MC_09_DATA	PORT_PDM_DATA_8_to_15
		P4E1	P8C1	P16B1		xlink7_tx4			IOR	2	4	X0D27	94	MC_10_DATA	PORT_PDM_DATA_8_to_15
		P4F0	P8C2	P16B2					IOR	2	4	X0D28	96	MC_11_DATA	PORT_PDM_DATA_8_to_15
		P4F1	P8C3	P16B3					IOR	2	4	X0D29	98	MC_12_DATA	PORT_PDM_DATA_8_to_15
		P4F2	P8C4	P16B4					IOR	2	4	X0D30	99	MC_13_DATA	PORT_PDM_DATA_8_to_15
		P4F3	P8C5	P16B5					IOR	2	4	X0D31	100	MC_14_DATA	PORT_PDM_DATA_8_to_15
		P4E2	P8C6	P16B6					IOR	2	4	X0D32	95	MC_15_DATA	PORT_PDM_DATA_8_to_15
		P4E3	P8C7	P16B7					IOR	2	4	X0D33	97	MC_16_DATA	PORT_PDM_DATA_8_to_15
	P1K0					xlink7_tx1			IOR	2	4	X0D34	90		
	P1L0					xlink7_tx2			IOR	2	4	X0D35	91		
	P1M0		P8D0	P16B8					IOL	3	4	X0D36	3	MCLK_TILE0	PORT_PDM_MCLK
	P1N0		P8D1	P16B9		xlink0_rx4			IOL	3	4	X0D37	4	MCLK_WCLK	PORT_WCLK_MCLK
	P1O0		P8D2	P16B10		xlink0_rx3			IOL	3	4	X0D38	5	WCLK_OUT	PORT_WCLK_OUT
	P1P0		P8D3	P16B11		xlink0_rx2			IOL	3	4	X0D39	7		
			P8D4	P16B12		xlink0_rx1			IOL	3	4	X0D40	8	XL_DN1	
			P8D5	P16B13		xlink0_rx0			IOL	3	4	X0D41	9	XL_DN0	
			P8D6	P16B14		xlink0_tx0			IOL	3	4	X0D42	10	XL_UP0	
			P8D7	P16B15		xlink0_tx1			IOL	3	4	X0D43	12	XL_UP1	

	Port						Reserved Pins		Features			Pin name	Package Pin		
	1b	4b	8b	16b	32b	Link	QSPI	RGMII	I/O rail	BANK	Drive mA		TQ128	PCB Naming	Code Naming
Tile 1	P1A0					xlink7_rx2			IOR	0	4	X1D00	85		
	P1B0					xlink7_rx1			IOR	0	4	X1D01	86		
		P4A0	P8A0	P16A0	P32A20	xlink4_rx0			IOR	0	4	X1D02	72		
		P4A1	P8A1	P16A1	P32A21	xlink4_tx0			IOR	0	4	X1D03	74		
		P4B0	P8A2	P16A2	P32A22	xlink4_tx1			IOR	0	4	X1D04	75		
		P4B1	P8A3	P16A3	P32A23	xlink4_tx2			IOR	0	4	X1D05	76		
		P4B2	P8A4	P16A4	P32A24	xlink4_tx3			IOR	0	4	X1D06	77		
		P4B3	P8A5	P16A5	P32A25	xlink4_tx4			IOR	0	4	X1D07	79		
		P4A2	P8A6	P16A6	P32A26	xlink7_rx4			IOR	0	4	X1D08	82		
		P4A3	P8A7	P16A7	P32A27	xlink7_rx3			IOR	0	4	X1D09	84		
	P1C0								IOT	0	4	X1D10	121		
	P1D0								IOT	0	4	X1D11	122		
		P4C0	P8B0	P16A8	P32A28				IOR	1	4	X1D14	51	TILE1_SPARE1	
		P4C1	P8B1	P16A9	P32A29				IOR	1	4	X1D15	53	TILE1_SPARE2	
		P4D0	P8B2	P16A10		xlink3_rx1			IOL	1	4	X1D16	22	PLL_SYNC	PORT_PLL_REF
		P4D1	P8B3	P16A11		xlink3_rx0			IOL	1	4	X1D17	23		
		P4D2	P8B4	P16A12		xlink3_tx0			IOL	1	4	X1D18	25		
		P4D3	P8B5	P16A13		xlink3_tx1			IOL	1	4	X1D19	26		
		P4C2	P8B6	P16A14	P32A30				IOR	1	4	X1D20	54	TILE1_SPARE3	
		P4C3	P8B7	P16A15	P32A31				IOR	1	4	X1D21	55	TILE1_SPARE4	
		P4E0	P8C0	P16B0				tx_clk	IOT	2	8	X1D26	112		PORT_MCLK_COUNT
		P4E1	P8C1	P16B1				tx_ctl	IOT	2	8	X1D27	113		
		P4F0	P8C2	P16B2				rx_clk	IOT	2	4	X1D28	114		
		P4F1	P8C3	P16B3				rx_ctl	IOT	2	4	X1D29	115		
		P4F2	P8C4	P16B4				rx0	IOT	2	4	X1D30	116		
		P4F3	P8C5	P16B5				rx1	IOT	2	4	X1D31	117		
		P4E2	P8C6	P16B6				rx2	IOT	2	4	X1D32	118		
		P4E3	P8C7	P16B7				rx3	IOT	2	4	X1D33	119		
	P1L0					xlink0_tx3			IOL	2	4	X1D35	13	MCLK_TILE1	PORT_MCLK_IN
	P1M0		P8D0	P16B8		xlink0_tx4			IOL	3	4	X1D36	15		
	P1N0		P8D1	P16B9		xlink3_rx4			IOL	3	4	X1D37	18		
	P1O0		P8D2	P16B10		xlink3_rx3			IOL	3	4	X1D38	20		
	P1P0		P8D3	P16B11		xlink3_rx2			IOL	3	4	X1D39	21		
			P8D4	P16B12				tx3	IOT	3	8	X1D40	106		
			P8D5	P16B13				tx2	IOT	3	8	X1D41	107		
			P8D6	P16B14				tx1	IOT	3	8	X1D42	108	I2C_SCL	
			P8D7	P16B15				tx0	IOT	3	8	X1D43	109	I2C_SDA	