

Device Address	0x4e						
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Address	0x01		Device ID					
Bit	7	6	5	4	3	2	1	10
Description	Device ID					Revision		
<i>Default Val</i>	0	0	0	0	0	X	X	X
INT CLK	r	r	r	r	r	r	r	r
EXT CLK	r	r	r	r	r	r	r	r

Address	0x02		Device Ctrl					
Bit	7	6	5	4	3	2	1	0
Description	Unlock	Reserved				AuxOutDis	ClkOutDis	
<i>Default Val</i>	X	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0

Address	0x03		Device Config 1					
Bit	7	6	5	4	3	2	1	0
Description	RmodSel		Reserved	Reserved	AuxOutSrc		EnDevCfg1	
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	1	0	1
EXT CLK	0	0	0	0	0	1	0	1

Address	0x05		Global Config					
Bit	7	6	5	4	3	2	1	0
Description	Reserved			Freeze	Reserved		EnDevCfg2	
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	1
EXT CLK	0	0	0	0	0	0	0	1

Address	0x06		R_UD[31:24]					
Bit	7	6	5	4	3	2	1	0
Description	31	30	29	28	27	26	25	24
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK								

Address	0x07		R_UD[23:16]					
Bit	7	6	5	4	3	2	1	0
Description	23	22	21	20	19	18	17	16
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	1	1	0	0	0	1	0	0
EXT CLK								

Address	0x08		R_UD[15:8]					
Bit	7	6	5	4	3	2	1	0
Description	15	14	13	12	11	10	9	8
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	1	0	0	1	1	0	1	1
EXT CLK								

Address	0x09		R_UD[7:0]					
Bit	7	6	5	4	3	2	1	0
Description	7	6	5	4	3	2	1	0
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	1	0	1	0	0	1	0	1

EXT CLK								
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Address	0x16			Funct Cfg 1				
Bit	7	6	5	4	3	2	1	0
Description	ClkSkipEn	AuxLockCfg	Reserved	RefClkDiv		Reserved		
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	1	0	0	0
EXT CLK	0	0	0	1	0	0	0	0

Address	0x17			Funct Cfg 2				
Bit	7	6	5	4	3	2	1	0
Description	Reserved			ClkOutUnl	LFRatioCfg	Reserved		
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	1	0	0	0

Address	0x1E			Funct Cfg 3				
Bit	7	6	5	4	3	2	1	0
Description	Reserved	ClkIn_BW			Reserved			
<i>Default Val</i>	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0

Binary	Hex	Dec	R_UD Calculation		
			Desired Output Freq (Hz)	12288000	
			CLK_IN (Hz)	1000000	
			High Resolution → x2^20		
rrrrrrrr	READ ONLY	READ ONLY	R_UD (Dec)	12884902	0
rrrrrrrr	READ ONLY	READ ONLY			
			R_UD (Hex)	00C49BA5	196
					155
			High Multiplication → x2^12		165
			R_UD (Dec)	50332	
			R_UD (Hex)	0000C49B	
00000000	00	0			
00000000	00	0			
00000101	05	5			
00000101	05	5			
00000001	01	1			
00000001	01	1			
		c0		11000000	
00000000	00	0			
	00	0			
11000100	C4	196			
	00	0			
10011011	9B	155			
	00	0			
10100101	A5	165			

00	0
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00001000	08	8
00010000	10	16

00000000	00	0
00001000	08	8

00000000	00	0
00000000	00	0

1,23E+07

1,00E+06

12,288

12884902

Bit	7	6	5	4	3	2	1	0
Address	0x01		Device ID					
Description	Device ID					Revision		
Default Val	0	0	0	0	0	X	X	X
INT CLK	r	r	r	r	r	r	r	r
EXT CLK	r	r	r	r	r	r	r	r
Address	0x02		Device Ctrl					
Description	Unlock	Reserved				AuxOutDis	ClkOutDis	
Default Val	X	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0
Address	0x03		Device Config 1					
Description	RmodSel		Reserved	Reserved	AuxOutSrc		EnDevCfg1	
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	1	0	1
EXT CLK	0	0	0	0	0	1	0	1
Address	0x05		Global Config					
Description	Reserved			Freeze	Reserved		EnDevCfg2	
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	1
EXT CLK	0	0	0	0	0	0	0	1
Address	0x06		R_UD[31:24]					
Description	31	30	29	28	27	26	25	24
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK								
Address	0x07		R_UD[23:16]					
Description	23	22	21	20	19	18	17	16
Default Val	0	0	0	0	0	0	0	0
INT CLK	1	1	0	0	0	1	0	0
EXT CLK								
Address	0x08		R_UD[15:8]					
Description	15	14	13	12	11	10	9	8
Default Val	0	0	0	0	0	0	0	0
INT CLK	1	0	0	1	1	0	1	1
EXT CLK								
Address	0x09		R_UD[7:0]					
Description	7	6	5	4	3	2	1	0
Default Val	0	0	0	0	0	0	0	0
INT CLK	1	0	1	0	0	1	0	1
EXT CLK								
Address	0x16		Funct Cfg 1					
Description	ClkSkipEn	AuxLockCfg	Reserved	RefClkDiv		Reserved		
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	1	0	0	0
EXT CLK	0	0	0	1	0	0	0	0
Address	0x17		Funct Cfg 2					
Description	Reserved		ClkOutUnl	LFRatioCfg	Reserved			
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	1	0	0	0
Address	0x1E		Funct Cfg 3					
Description	Reserved	ClkIn_BW			Reserved			
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0

