Device Address			0x4e					
A ddraga	0	04	Device ID					
Address Bit	<b>0</b> x	<b>01</b>	5	4	3	1 <b>ce 1D</b> 2	1	10
Description	1	U	Device ID	4	3		Revision	10
Default Val	0	0		0	0	X		X
INT CLK	r	r	r		r	r		r
EXT CLK	r	r	r		r	-		r
EXT OLIC	<u> </u>	<u>'</u>	•	•	•	<u> </u>	<u> </u>	•
Address	0x					ce Ctrl		
Bit	7	6	5	4	3	2	1	0
Description	Unlock			Reserved			AuxOutDis	ClkOutDis
Default Val	Χ	0		0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0
Address	0x	n3			Device	Config 1		
Bit	7	6	5	4	3	2	1	0
Description		RmodSel			Reserved	AuxO		EnDevCfg1
Default Val	0	0	0	0	0	0	0	O
INT CLK	0	0	0	0	0	1	0	1
EXT CLK	0	0	0	0	0	1	0	1
						_		
Address	0x	05			Global	Config		
Bit	7	6	5	4	3	2	1	0
Description		Rese	erved		Freeze	Rese	erved	EnDevCfg2
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	1
EXT CLK	0	0	0	0	0	0	0	1
A ddraga	0	00			D 11D	[04.04]		
Address Bit	0x		Г	4		[31:24]	1	0
	31	6 30	5 29	28	3 27	2 26	1 25	0 24
Description Default Val	0	0	0	0	0	0	25	0
INT CLK	0			0	0	0		
EXT CLK	U	U	U	U	0	U	0	0
LXT OLK								
Address	0x	07			R_UD	[23:16]		
Bit	7	6	5	4	3	2	1	0
Description	23	22	21	20	19	18	17	16
Default Val	0	0	0	0	0	0	0	0
INT CLK	1	1	0	0	0	1	0	0
EXT CLK								
A ddraga	0.4	00			D 115	NE4 E . 0.1		
Address		<b>08</b> 6	Е	4		<b>[15:8]</b>	1	0
Bit Description	7 15	14	5 13	4 12	3	2 10		0
Description Default Val	0	0		0	11 0	0		0
INT CLK	1	0	0	1	1	0	1	1
EXT CLK	1	0	0		Т.	0		<u>_</u>
Address	0x					D[7:0]		
Bit	7	6		4	3			0
Description	7	6		4	3	2		0
Default Val	0	0		0	0	0		<i>0</i>
INT CLK	1	0	1	0	0	1	0	1

EXT CLK				

Address	0x16		Funct Cfg 1						
Bit	7	6	5	4	3	2	1	0	
Description	ClkSkipEn	AuxLockCfg	Reserved	RefC	lkDiv		Reserved		
Default Val	0	0	0	0	0	0	0	0	
INT CLK	0	0	0	0	1	0	0	0	
EXT CLK	0	0	0	1	0	0	0	0	

Address	0x17		Funct Cfg 2					
Bit	7	6	5	4	3	2	1	0
Description		Reserved		ClkOutUnl	LFRatioCfg		Reserved	
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	1	0	0	0

Address	0x1E		Funct Cfg 3					
Bit	7	6	5	4	3	2	1	0
Description	Reserved		ClkIn_BW		Reserved			
Default Val	0	0	0	0	0	0	0	0
INT CLK	0	0	0	0	0	0	0	0
EXT CLK	0	0	0	0	0	0	0	0

Binary	Hex	Dec		88000 00000
rrrrrrr rrrrrrrr		READ ONLY READ ONLY	High Resolution → x2^20  R_UD (Dec) 1288  R_UD (Hex) 00C4	
00000000 00000000	00 00	0	R_UD (Hex) 0000	155 165 50332 C49B
00000101 00000101	05 05	5 5		
00000001 00000001	01 01	1		10
00000000	00 00	0		
11000100	C4 00	196 0		
10011011	9B 00	155 0		

165

10100101 A5

	00	0
00001000	08	8
00010000	10	16
00000000 00001000	00 80	0 8
00000000	00	0
00000000	00	0

1,23E+07 1,00E+06 12,288 12884902

Bit	7	6	5	4	3	2	1	0
Address	0x					ce ID	_	
Description	0,1		Device ID		2001		Revision	
Default Val	0	0	0	0	0	X	X	Χ
INT CLK		r		r	r	r	r	r
EXT CLK	•	•	•	•	r	r	r	r
Address	0x	-		!	Devic	o Ctrl	ı	li .
Description	Unlock	UZ		Reserved	Devic	Cui	AuxOutDis	ClkOutDis
	X	0	0	0	0	0		
INT CLK	0	0	0	0	0			_
EXT CLK	0	0	0	0	0			
Address	0x		U	U	Device		0	0
Description	UX	RmodSel		Reserved	Reserved		outSrc	EnDevCfg1
Default Val	0	0	0	0	0			
INT CLK	0	0	0	0	0			
EXT CLK	0	0	0	0	0			
Address	0x		U	U		± Config	U	
Description	UX.	Rese	nyed		Freeze		erved	EnDevCfg2
Description Default Val	0	0	0	0	0			
INT CLK	0	0	0	0	0			
EXT CLK	0	0	0	0	0			
Address	0x		J	U	R_UD		0	
Description	31	30	29	28		26	25	24
Default Val	0	0	0	0	0			
INT CLK	0	0	0	0	0			
EXT CLK	U	U	U	U	U	0	U	U
Address	0x	07			D IID	[23:16]		
Description	23	22	21	20	19		17	16
Default Val	0	0	0	0	0			
INT CLK	1	1	0	0	0			
EXT CLK			J	0			J	J
Address	0x	na			R_UD	[15·8]		
Description	15	14	13	12	11	-	9	8
Default Val	0	0	0	0	0			
INT CLK	1	0	0	1	1	0		1
EXT CLK		U	U					
Address	0x	09			R III	D[7:0]		
Description	7	6	5	4	3		1	0
Default Val	0	0	0	0	0			
INT CLK	1	0	1	0	0			_
EXT CLK	_		_			_		_
Address	0x.	16			Funct	Cfg 1		
Description		AuxLockCfg	Reserved	RefC			Reserved	
Default Val	0	0 / tax200k0ig	0	0	0	0	1	0
INT CLK	0	0	0	0	1			
EXT CLK	0	0	0	1	0			
Address	0x.			-		Cfg 2		
Description	OX.	Reserved		ClkOutUnl			Reserved	
Default Val	0	0	0	0	0			0
INT CLK	0	0	0	0	0			
EXT CLK	0	0	0	0	1	0		
Address	0x.		3		Funct	Cfg 3		
Description	Reserved		ClkIn_BW				erved	
Default Val	0	0	0	0	0		1	0
INT CLK	0	0	0	0	0			
EXT CLK	0	0	0	0	0			
_/\. OLI\	ı O	O <sub>1</sub>	U	O <sub>1</sub>				