Project Name :Bubblegum96_S900

Table of Contents:

Revision History :

Rev.	Description	Editor	Date
0.9	Initial		2015.04.17
1.0	UPDATE		2015.11.06

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B	01_HISTORY	0.95

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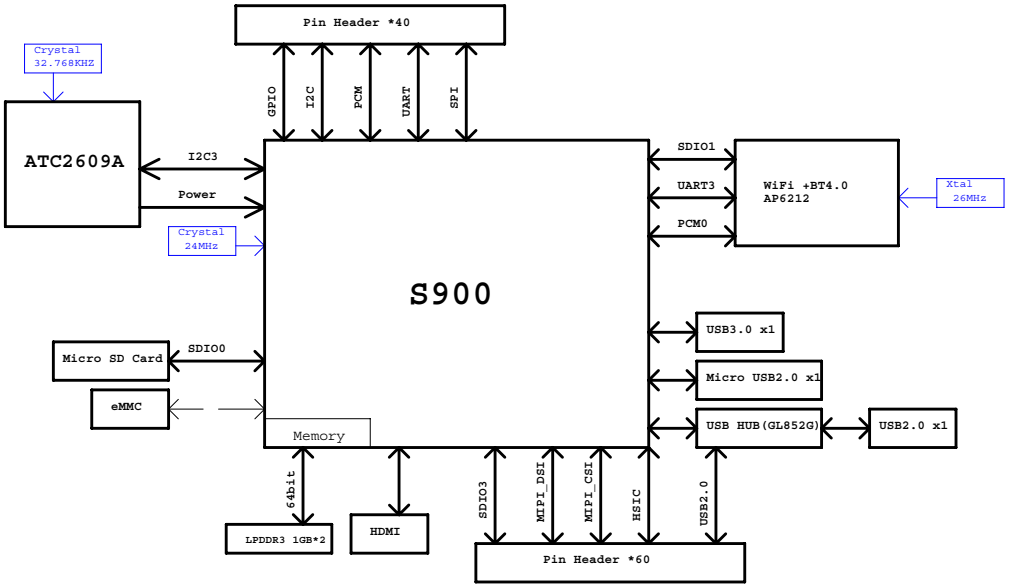
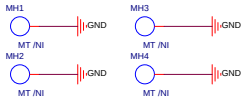
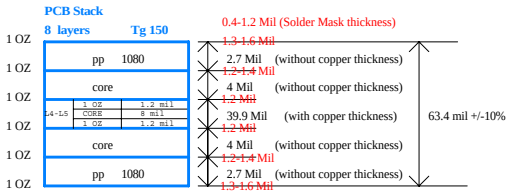
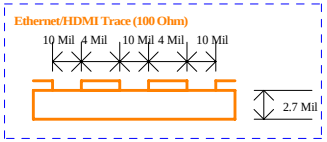
Bubblegum-96 Block_Diagram

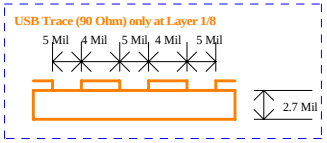


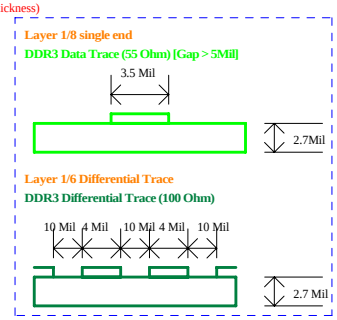


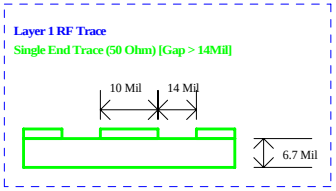
- Operating Temp:
- Storage Temp:
- Dimension: 90mmx60 mm
- Thickness : 8L/1.6mm

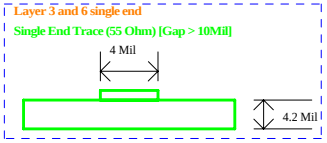


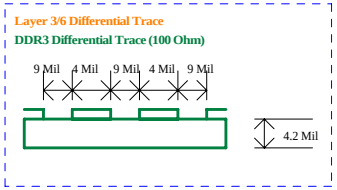












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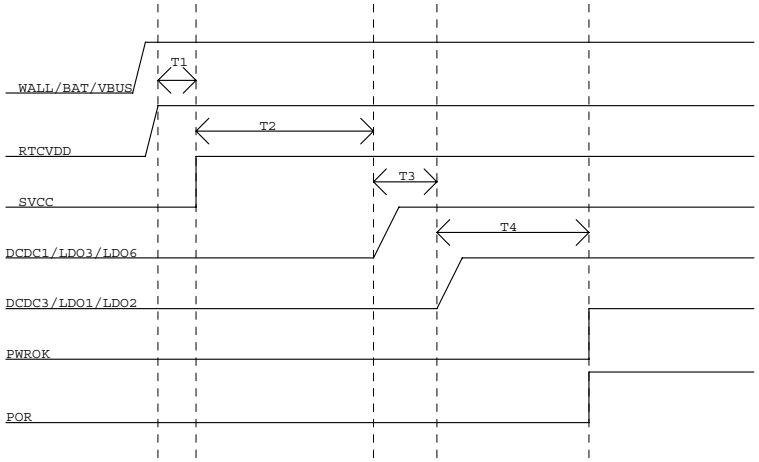
ATC2609 Application frame

1. PMU(ATC2609) Power on Status

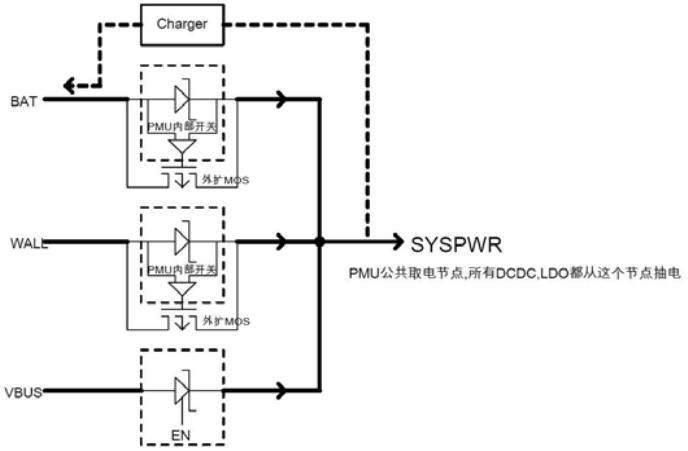
V:Power ON O:Power ON form SW Control X:Power OFF

		Assignment	Imax@Vout	Default Power State @Diff SYS Mode			
				S1:Working Mode	S2:Standby Mode	S3:Sleep Mode	S4:DeepSleep Mode
DC-DC 類	DC-DC0	VDD_CORE	2000mA@0.9V	O	O	X	X
	DC-DC1	CPU_VDD	3000mA@0.9V	O	O	X	X
	DC-DC2	LP_VDDQ_1V2	2000mA@1.2V	O	O	X	X
	DC-DC3	VCC3V1	1200mA@3.1V	O	O	X	X
	DC-DC4		2000mA@0.9V	O	O	X	X
LDO類	LDO0	SD_VCC	500mA@3.1V	O	O	X	X
	LDO1	WiFi_3V3	500mA@3.3V	O	O	X	X
	LDO2	AVCC3V1	500mA@3.1V	V		X	X
	LDO4	AVCC1V8#	2000mA@1.8V	O	O	X	X
	LDO6	AVDD1V0	2000mA@1.0V	O	O	X	X
	LDO7	VCC1V8_IO	200mA@1.2/1.5/1.8V	O	O	X	X
	LDO8	TPVCC3V1	2000mA@3.1V	O	O	X	X

2. PMU(ATC2609) Power On Sequence



3. PMU(ATC2609) APDS frame



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DC Adapter IN

PCB Layout Trace Width

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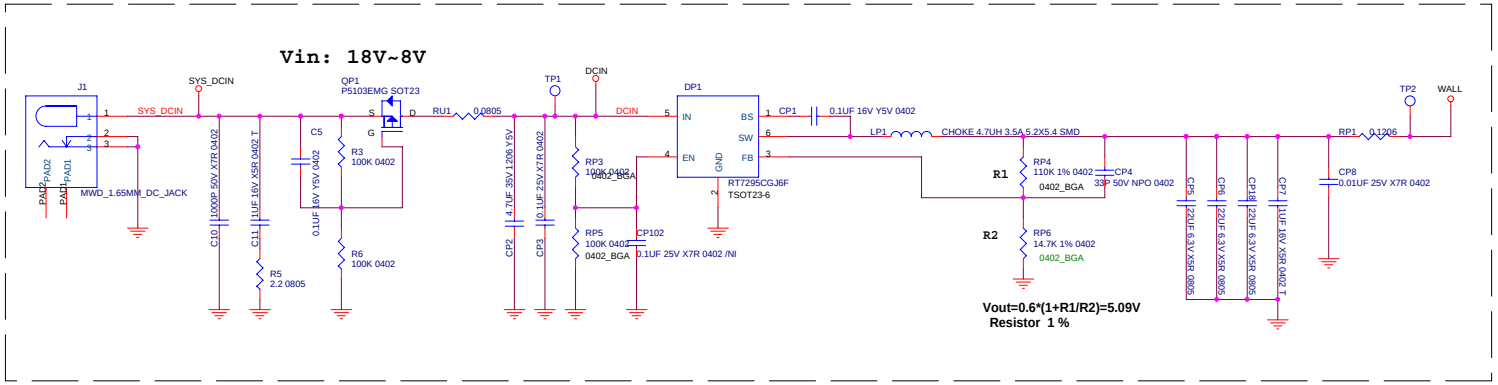
- 50Mil
- 20Mil



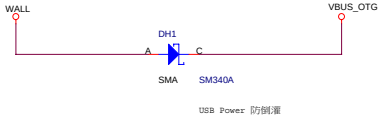
PCB

96BOARDS_S900_V1.0

HEATSINK

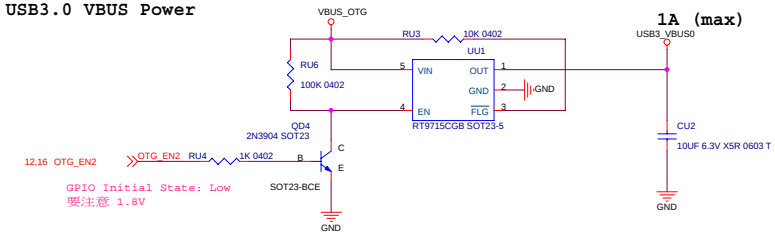
HEATSINK 21x21x5.2(B)

USB1/USB2/USB3 HOST Power

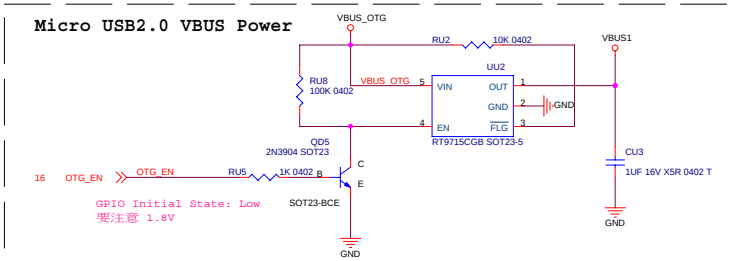


USB Power 防倒灌

USB3.0 VBUS Power



Micro USB2.0 VBUS Power



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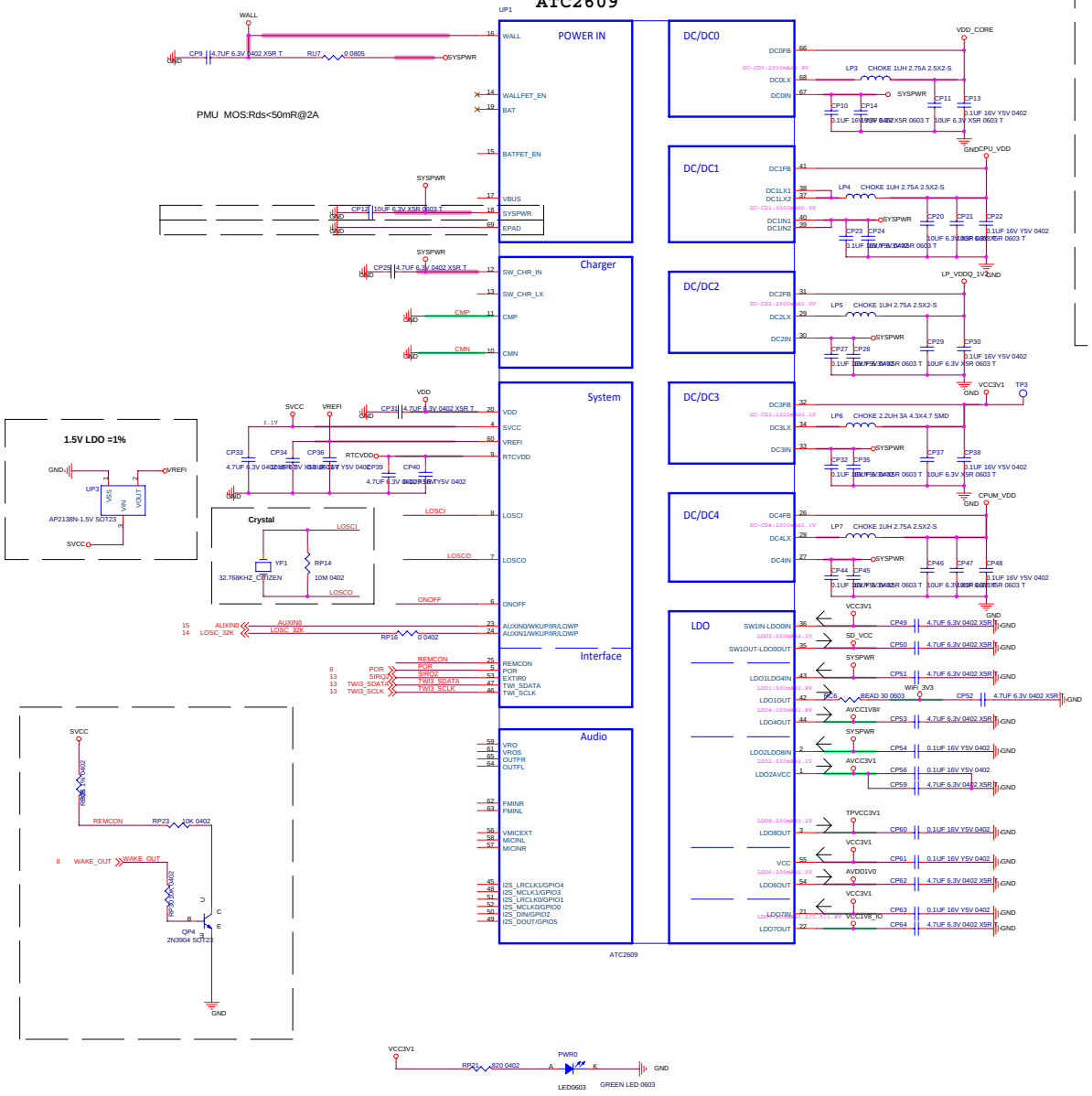
PMIC

ATC2609



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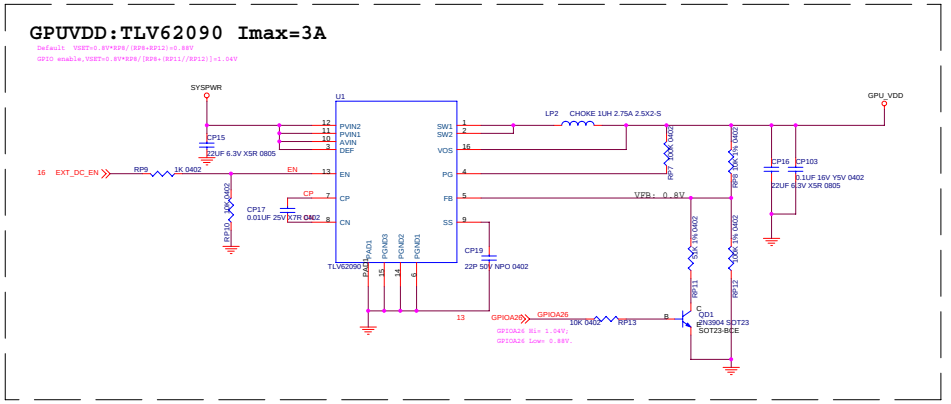

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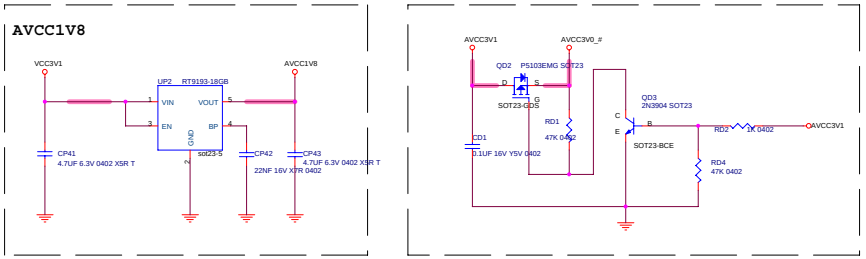

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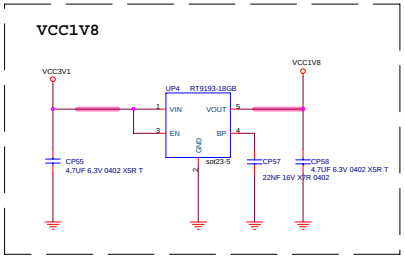

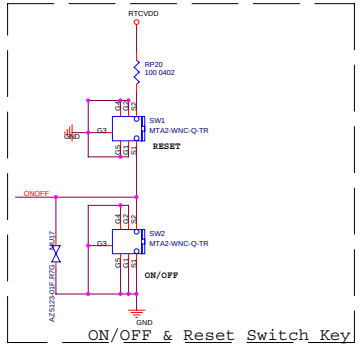

PCB Layout Trace Width

- 80Mil
- 50Mil
- 20Mil

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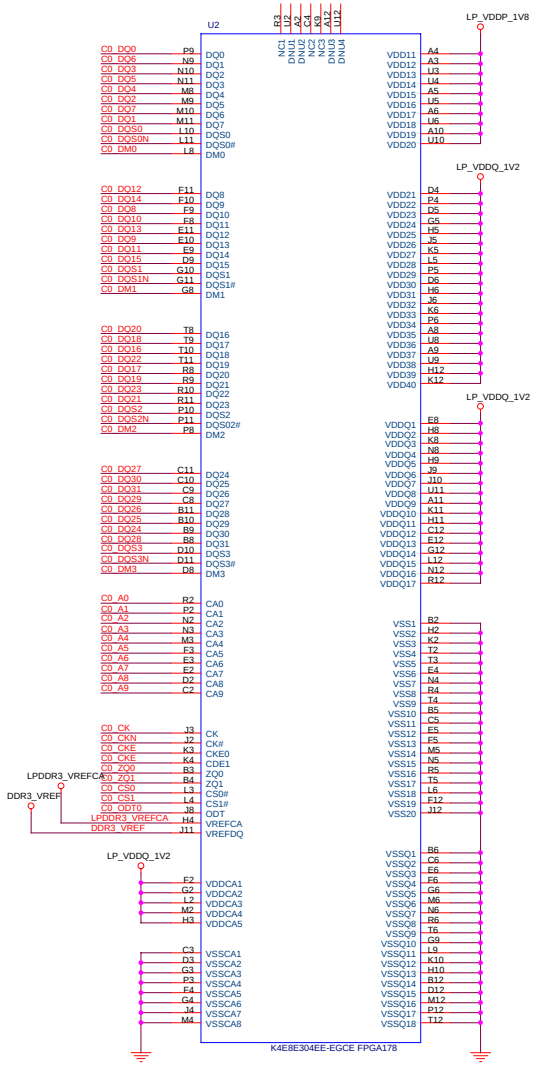
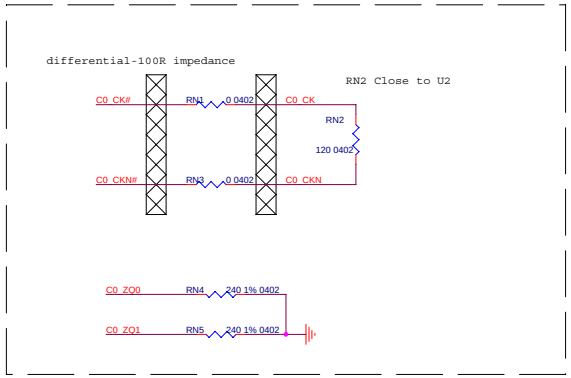
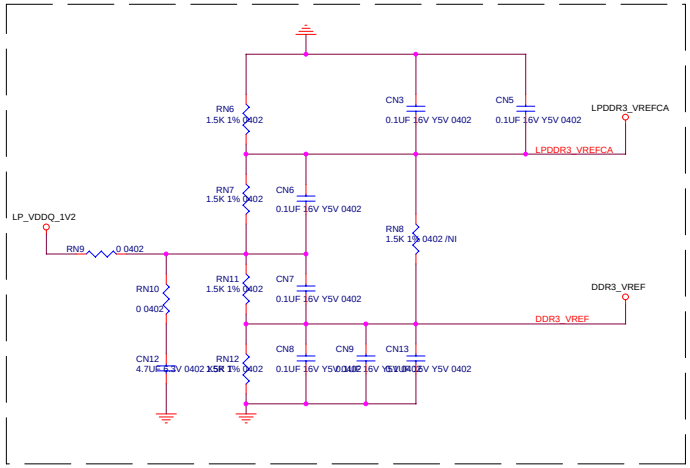
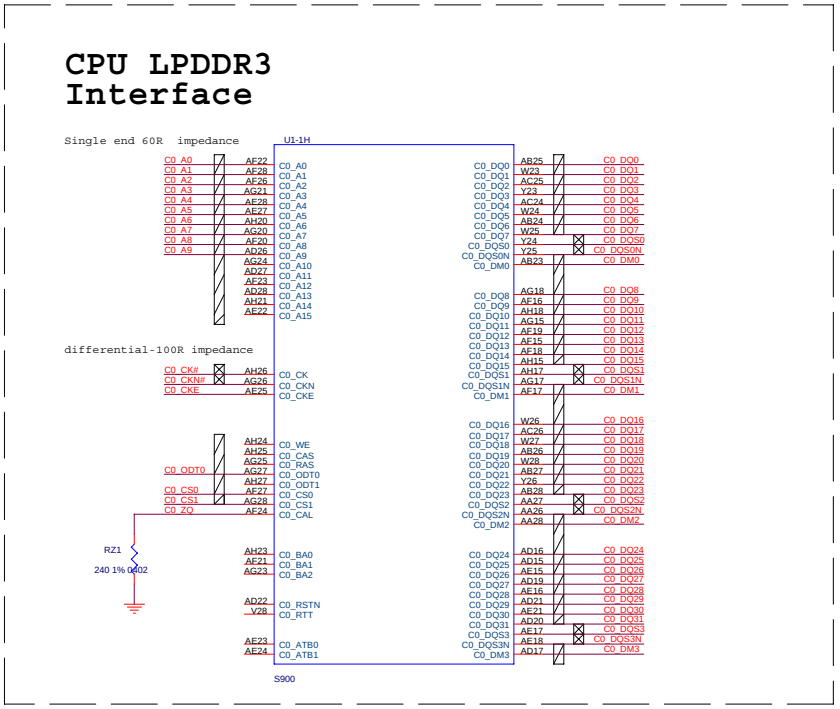
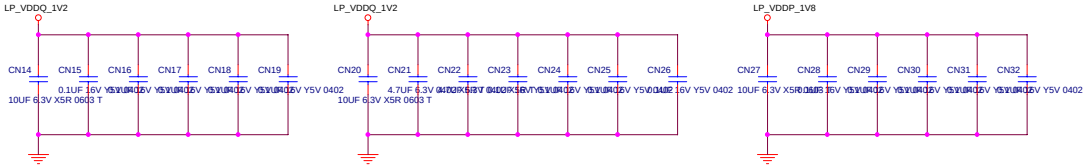
LPDDR3 512M*32bits











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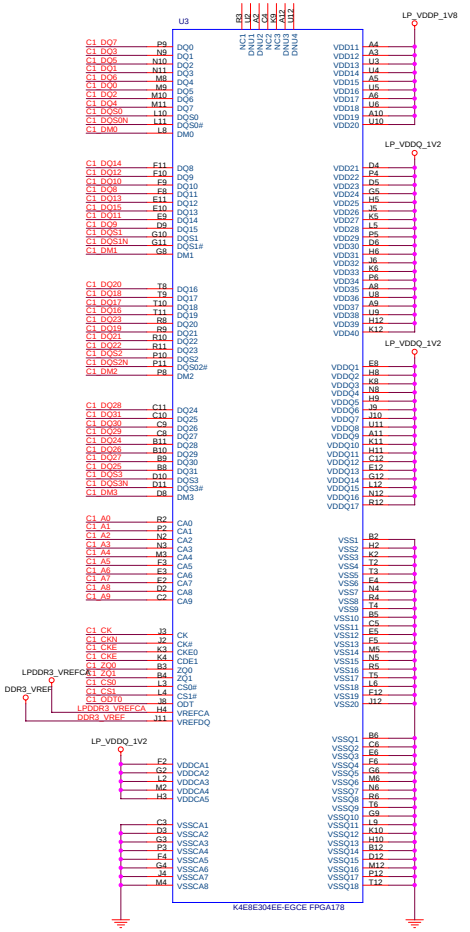
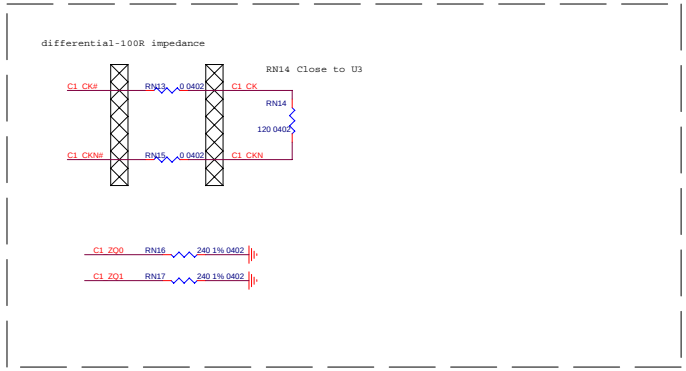
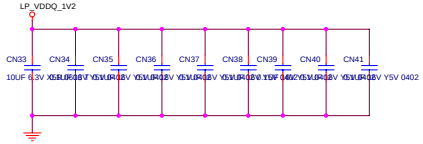
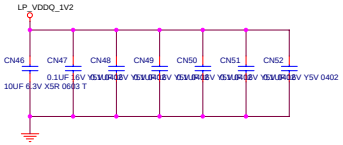
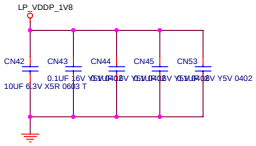
LPDDR3 512M*32bits

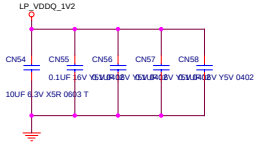




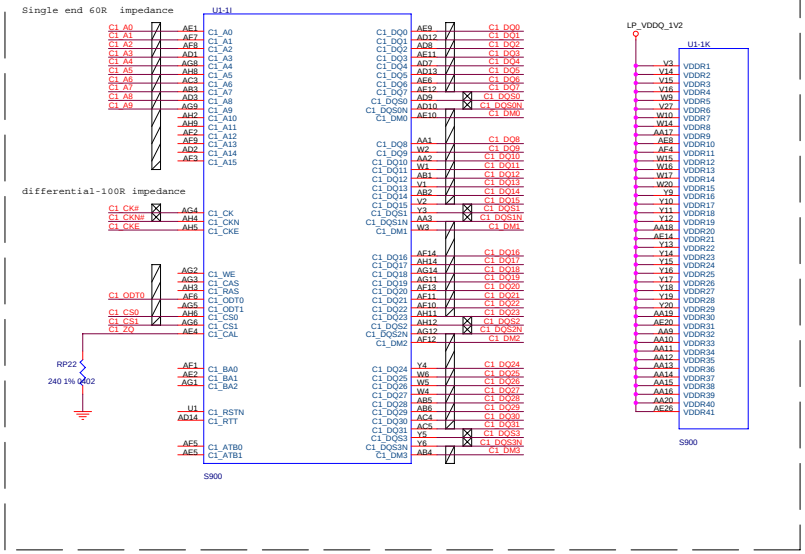


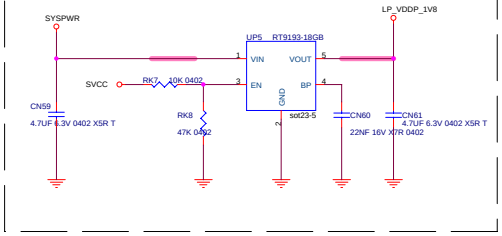






CPU LPDDR3 Interface





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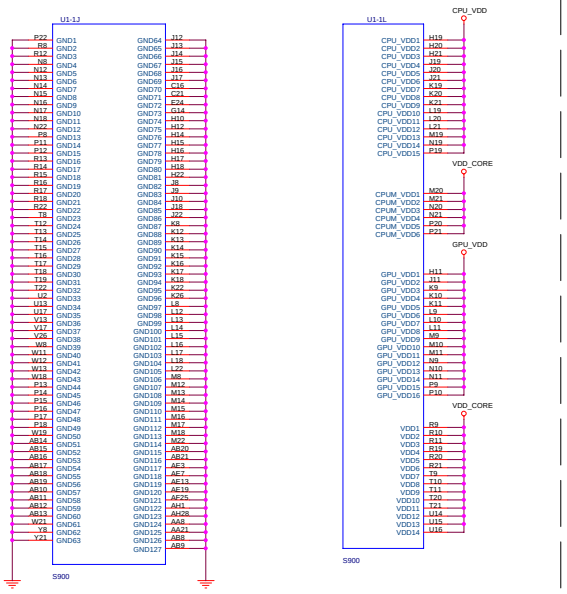
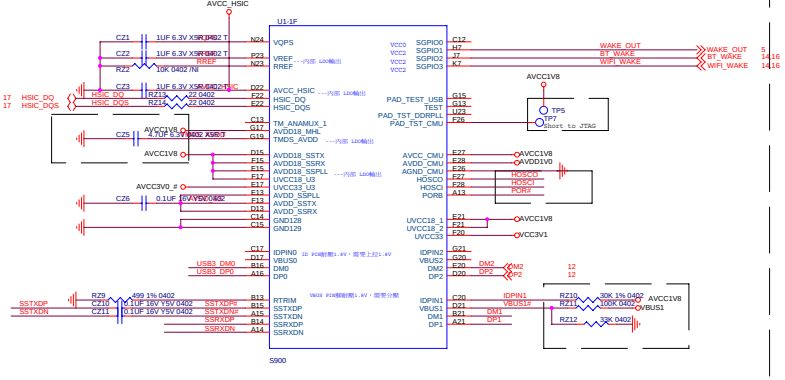
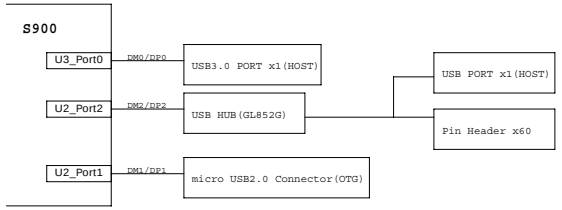
CPU Power Interface



CPU USB WIFI Interface





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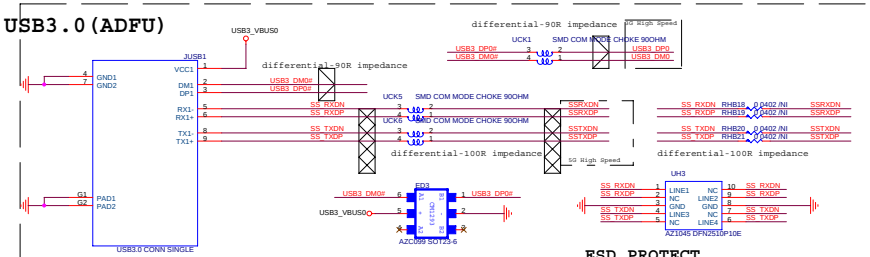

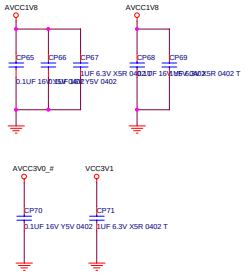

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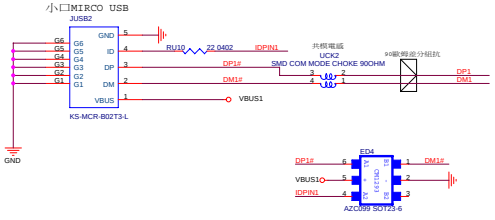
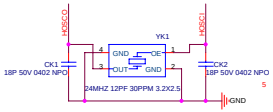
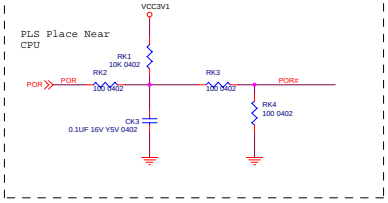
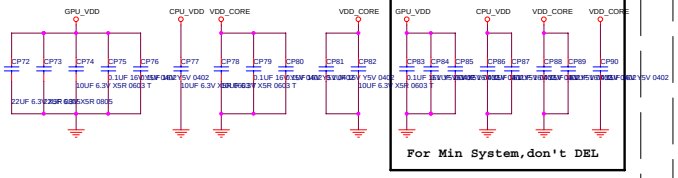


CPU Crystal and Reset

Freq.Tolerance<40ppm
ESR <50 ohm
PLS Place Near CPU







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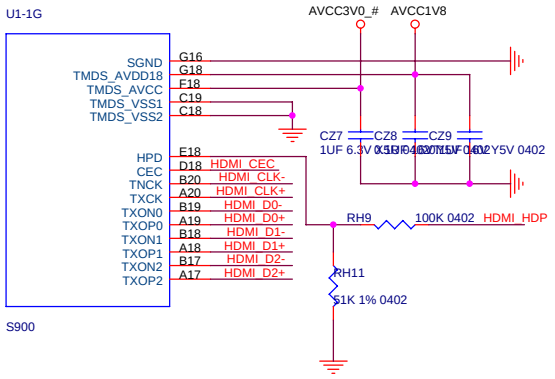
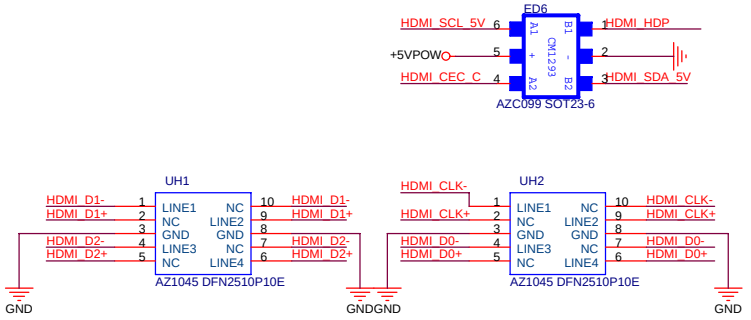
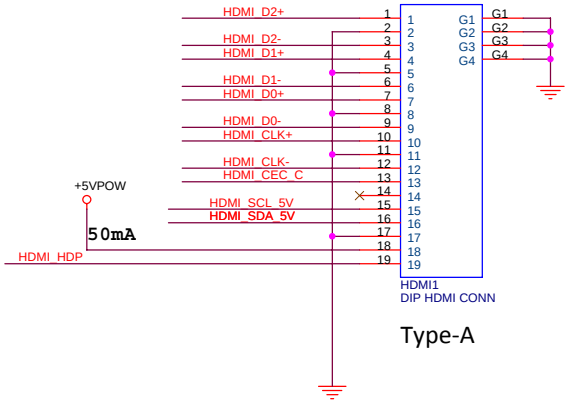
HDMI

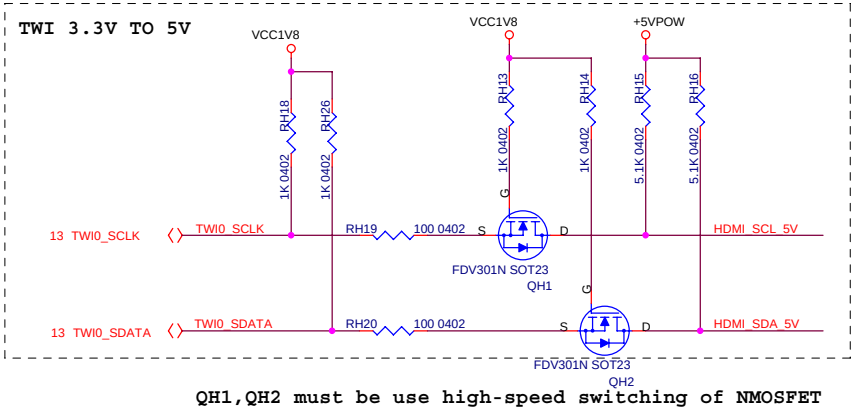
CPU HDMI Interface

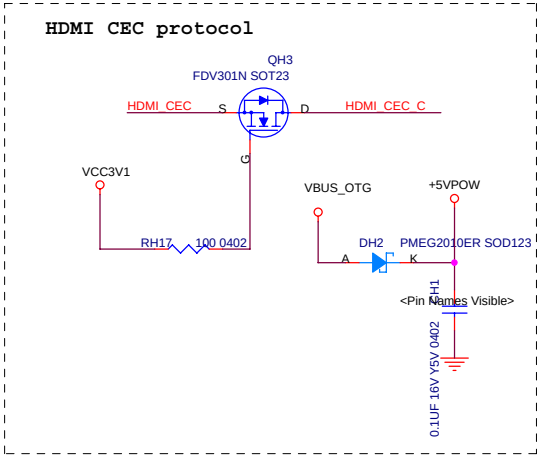


HDMI









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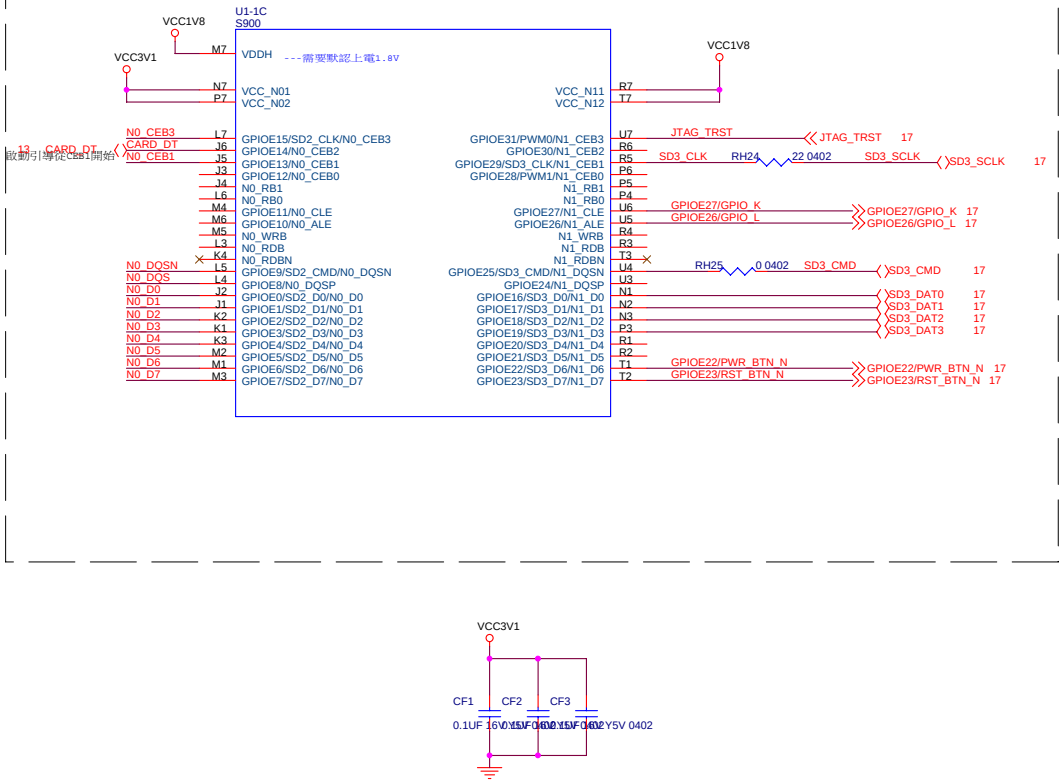
CPU NAND/eMMC Interface



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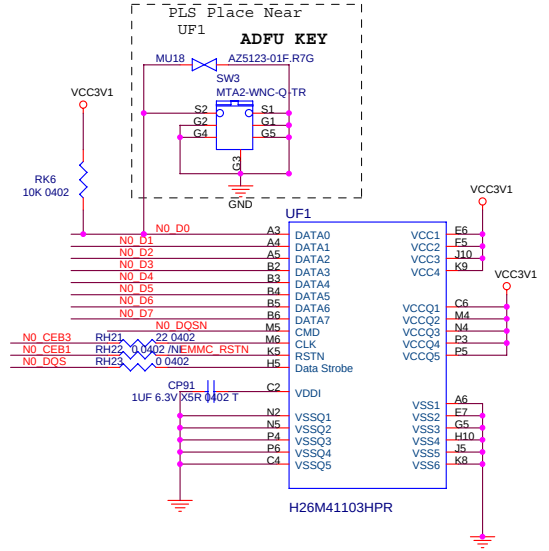

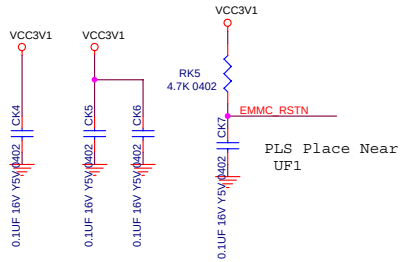

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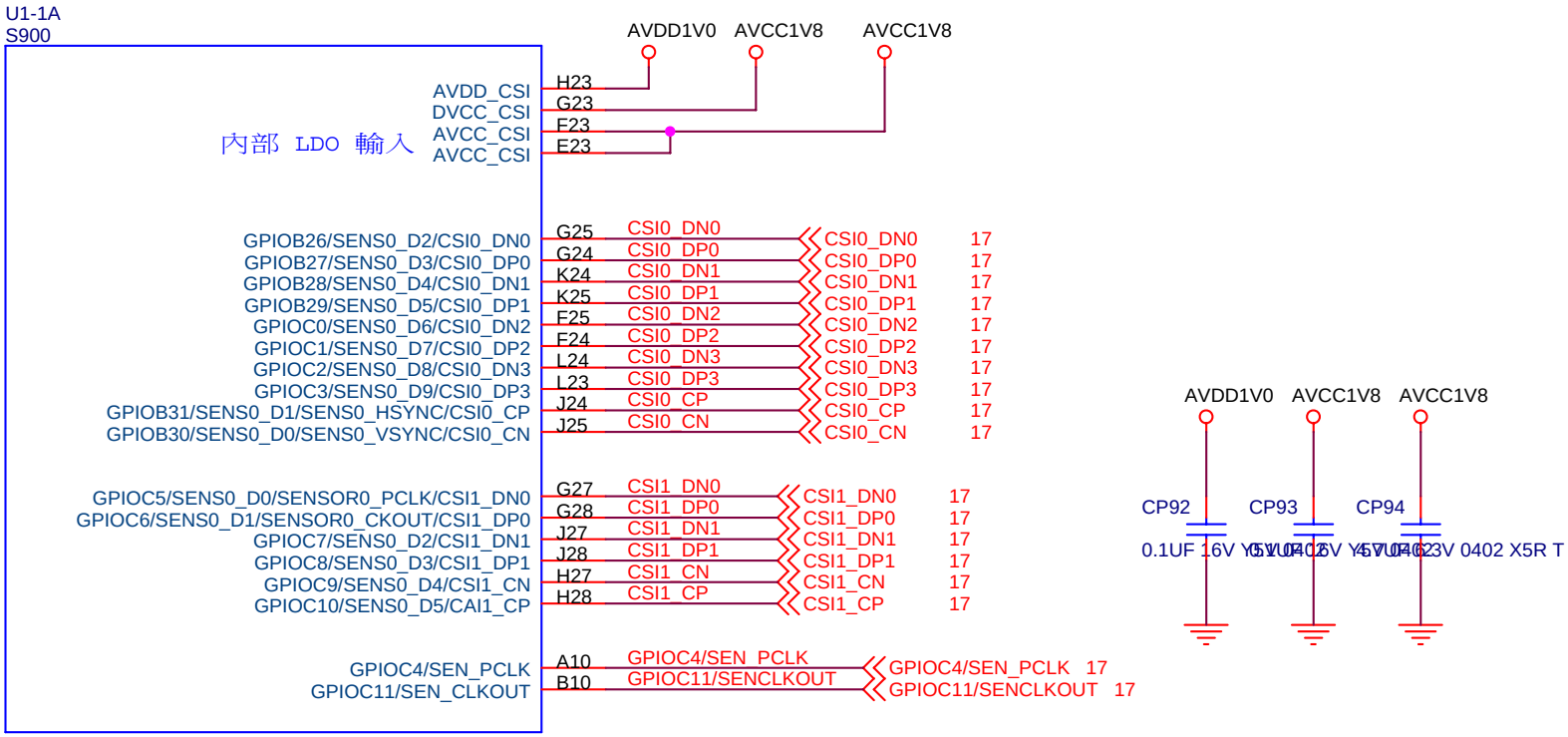
MIPI_CSI



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A	11_MIPI_CSI	0.95
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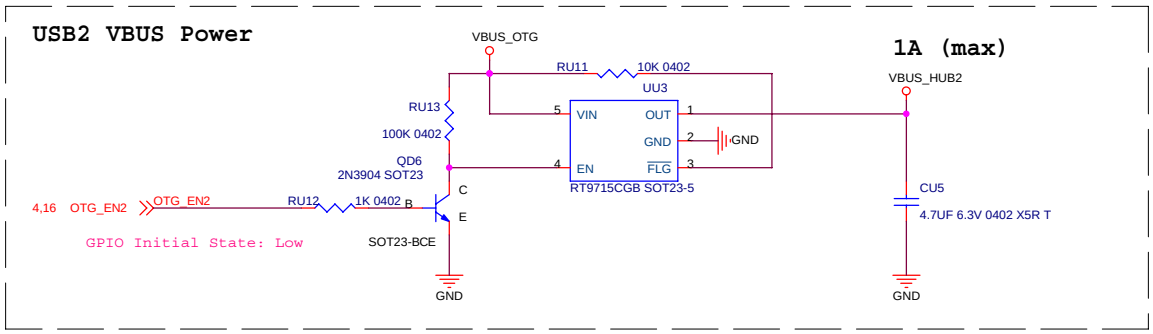

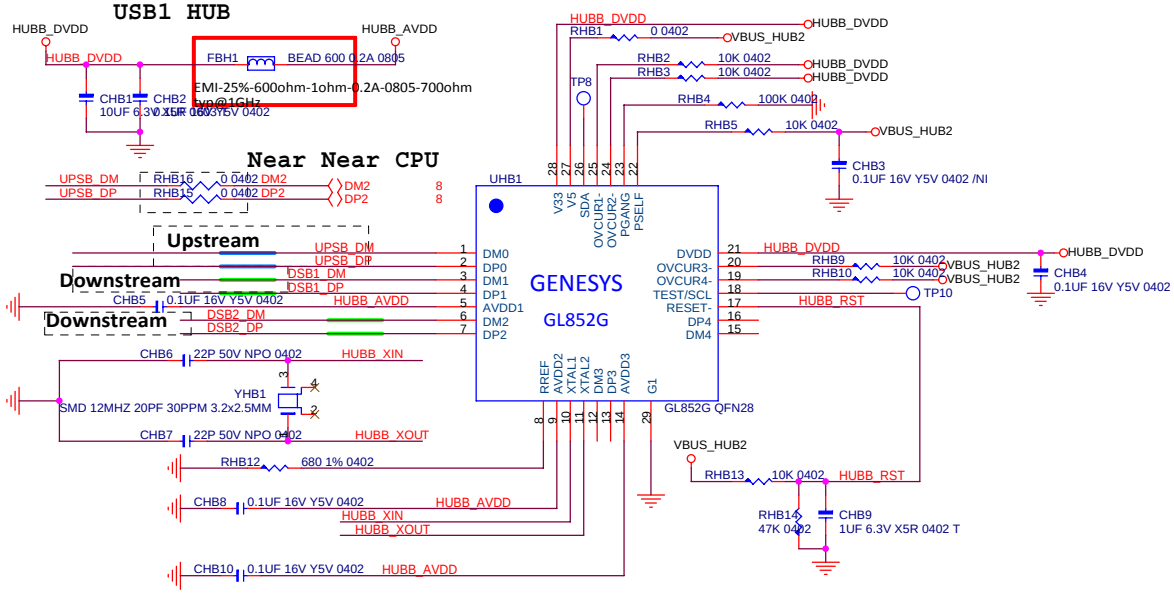

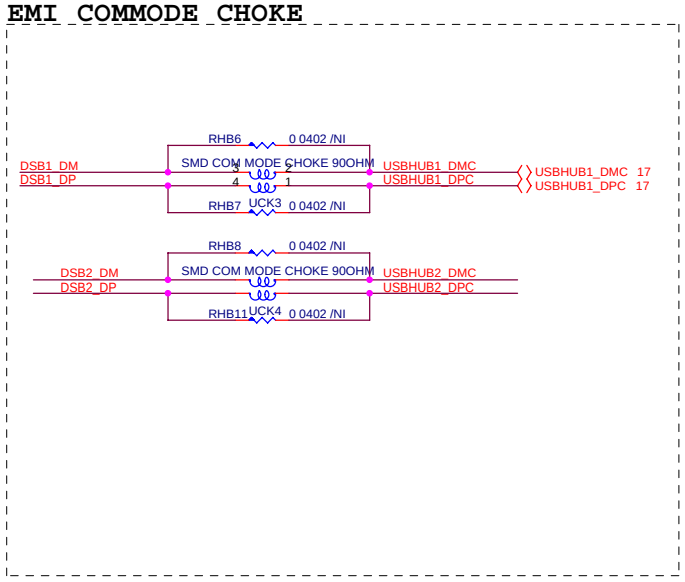

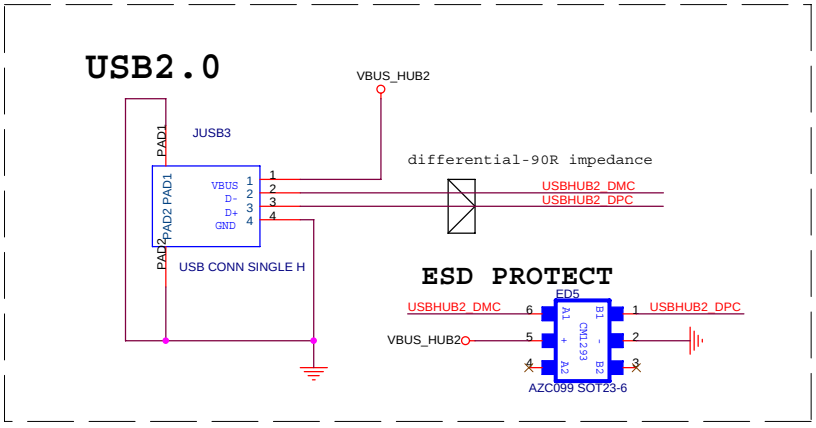

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Custom	12._USB2 to HUB(GL852G)	0.95
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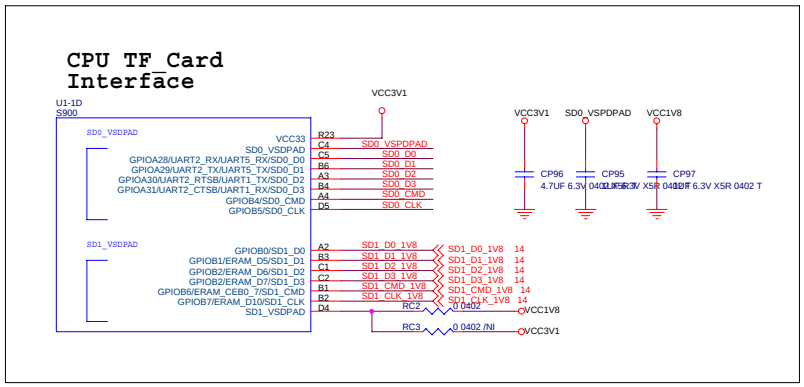

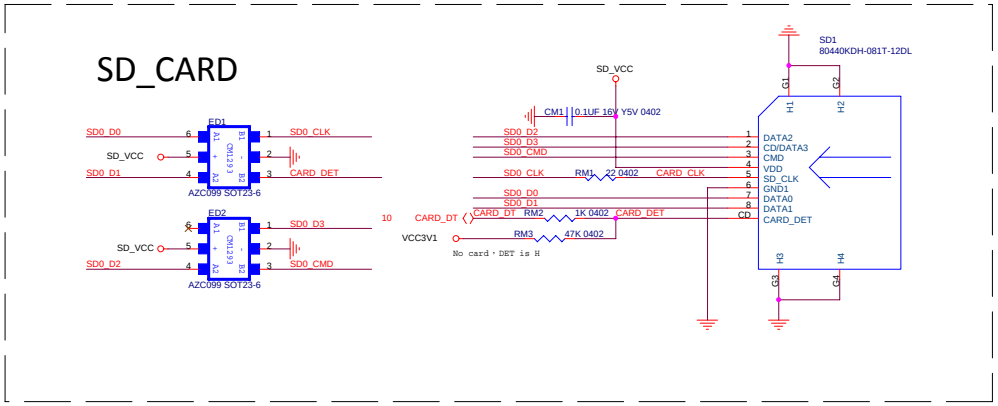

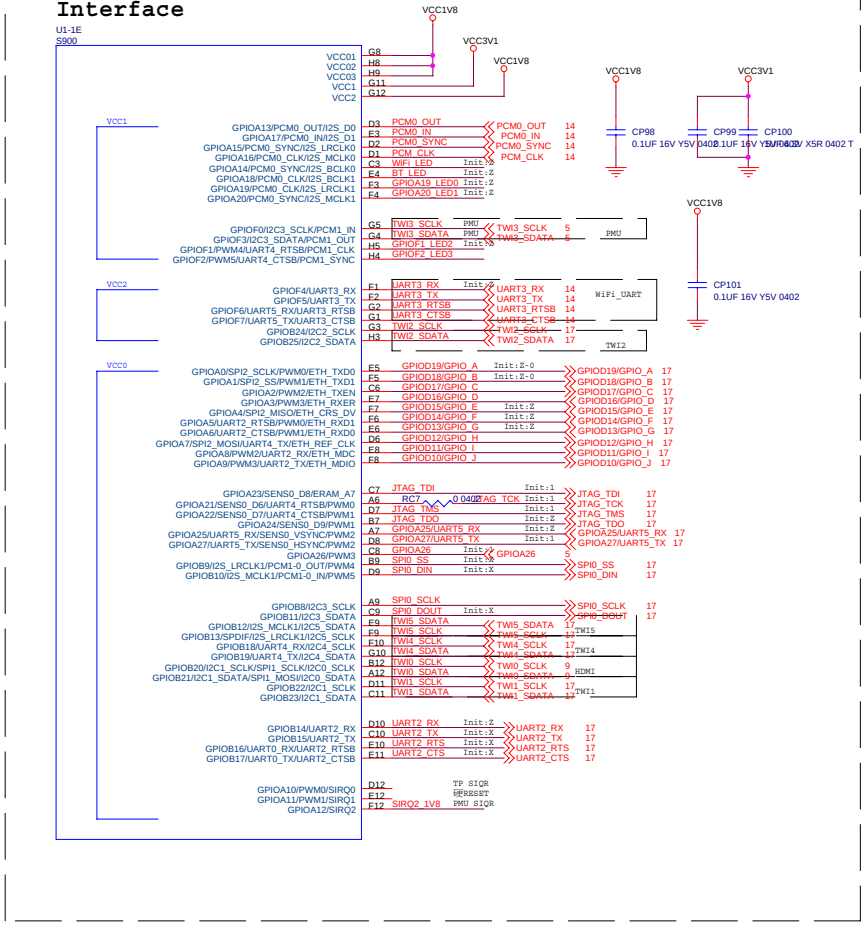

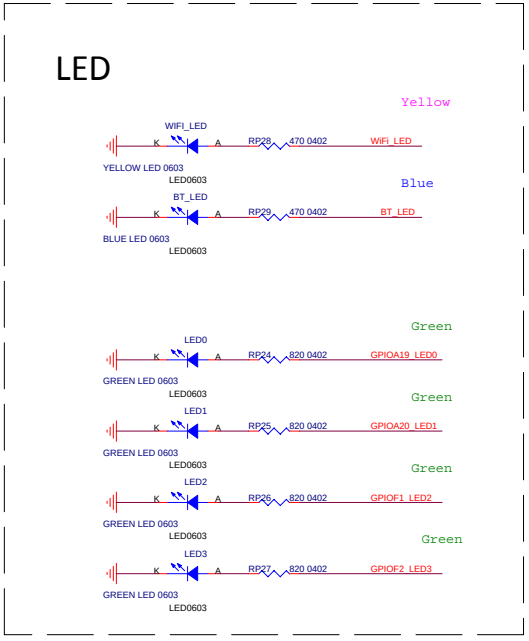

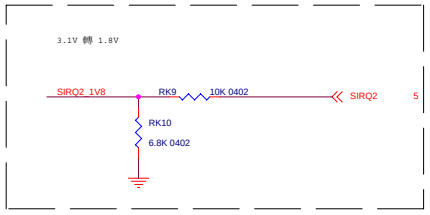

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Custom	13_TF_CARD & Interface	0.95
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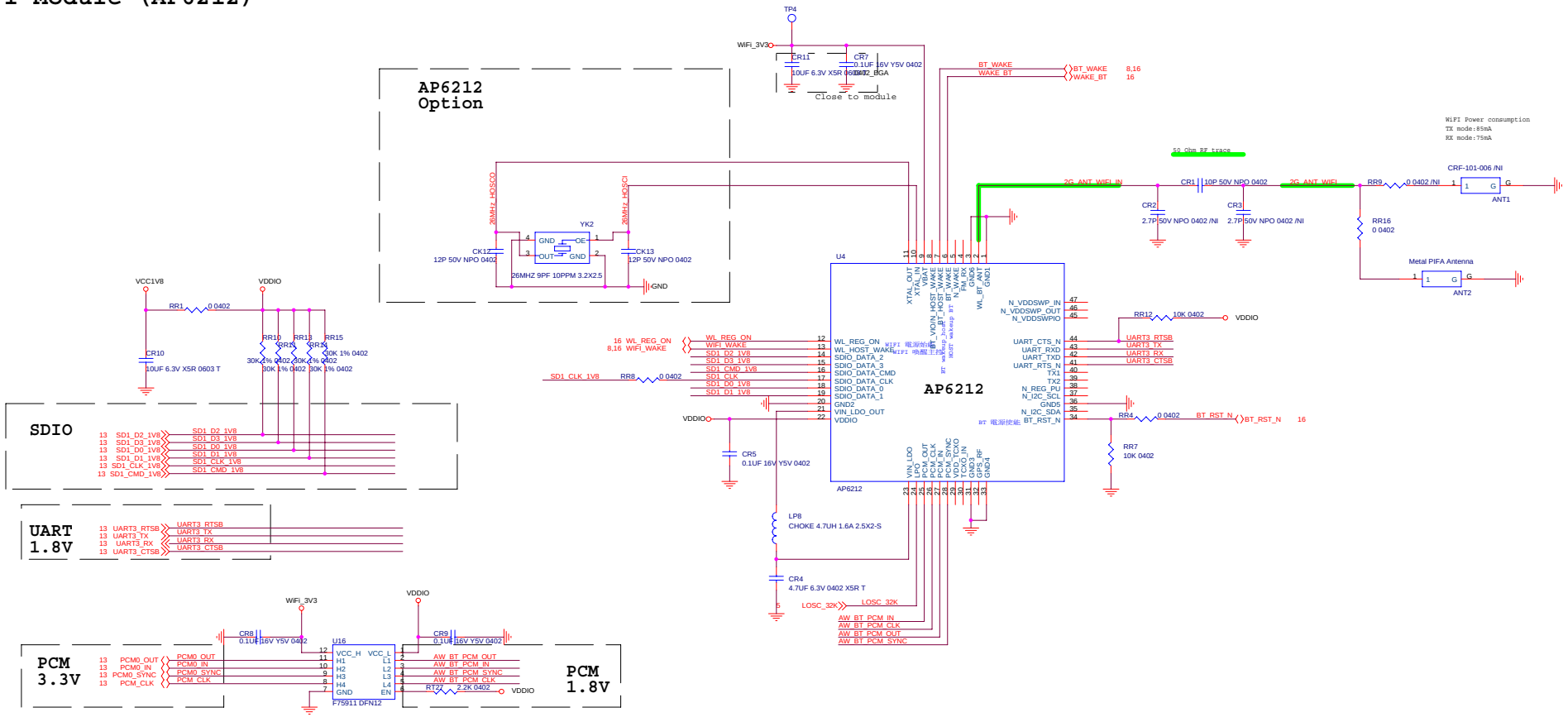
WiFi Module (AP6212)



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ADC KEY

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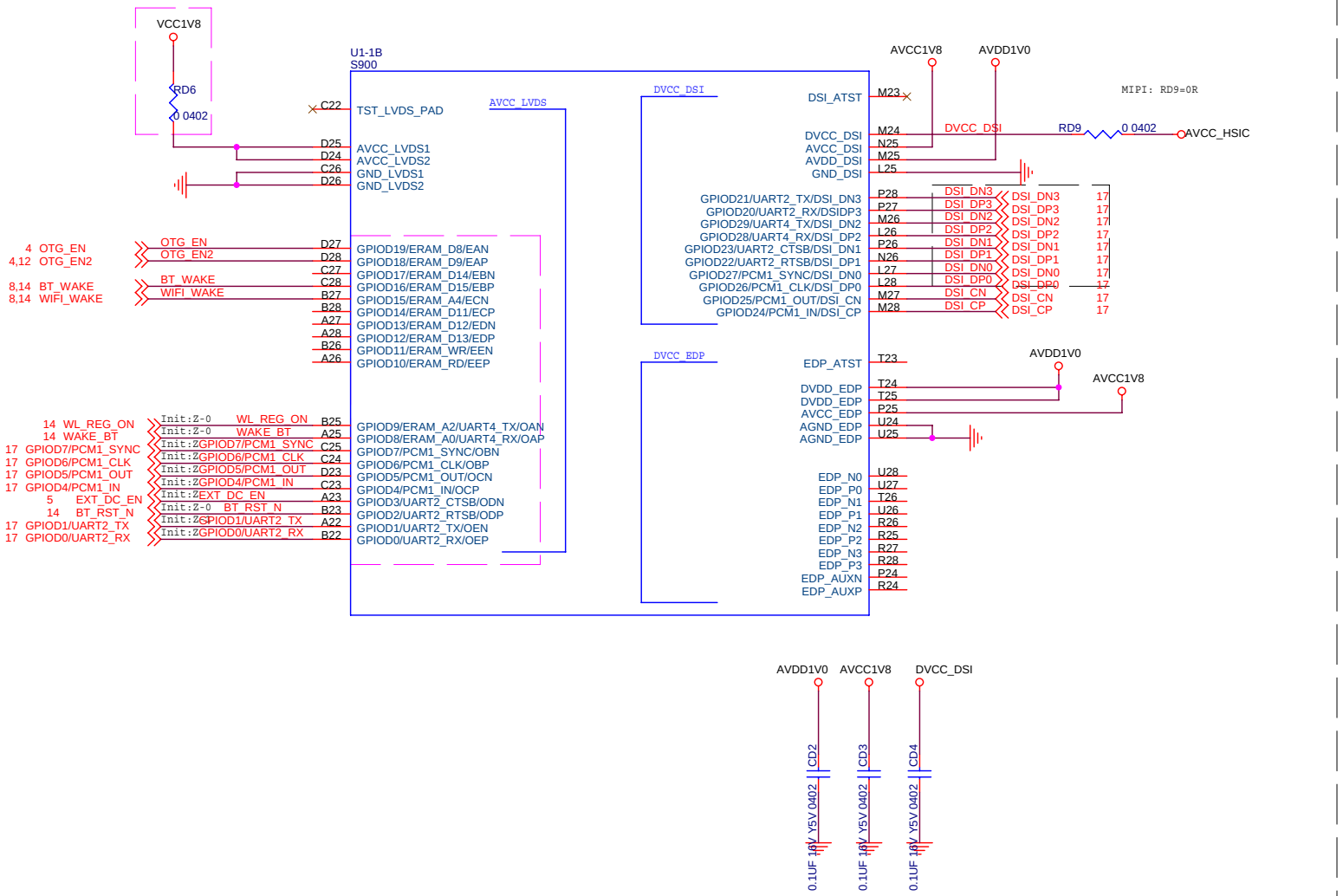
CPU MIPI_DSI&EDP Interface

Note:if don't need use LVDS Panel,and use the gpio functions that The AVCC_LVDS need use VCC1V8 Power Source.



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Custom	16_MIPI_DSI&EDP	0.95
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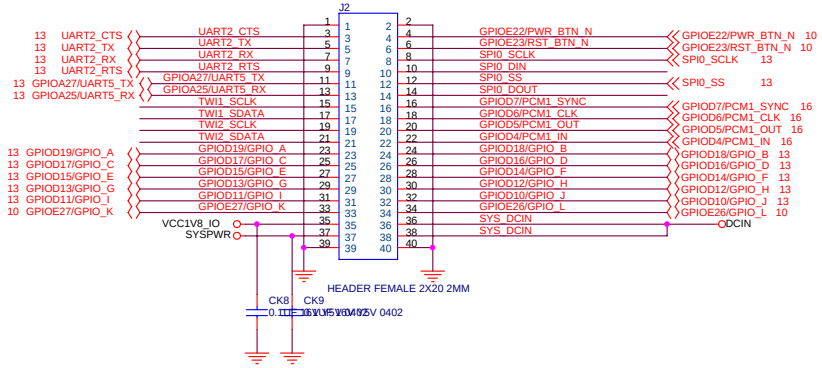
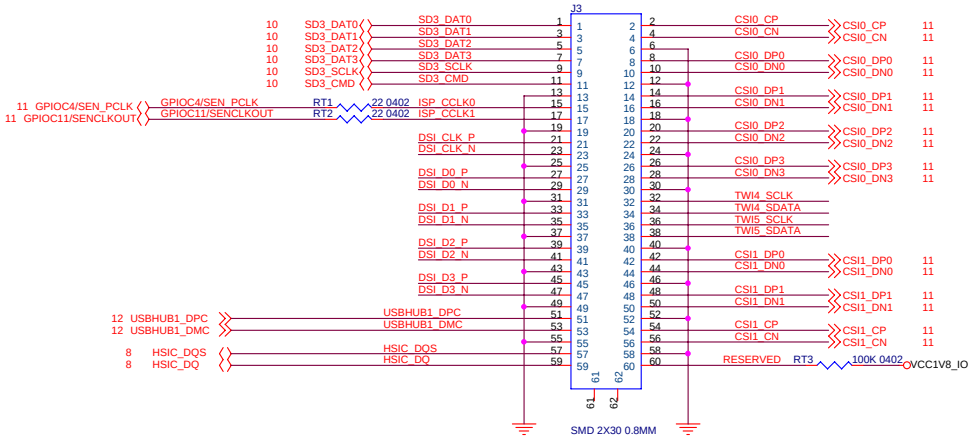
Pin Header x40

UART0/UART1/I2C0/I2C1/GPIO/SPI/PCM/PWR_BTN/RST_BTN/1V8/5V/SYS_DCIN/GND

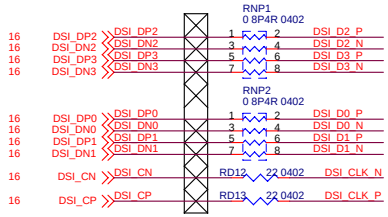


Pin Header x60

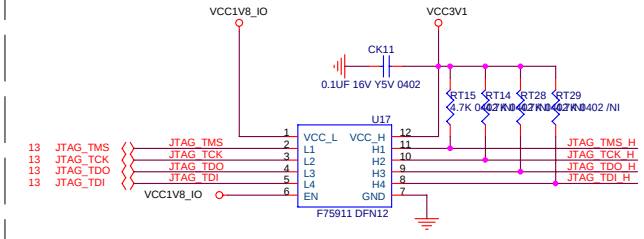
MIPI_DSI/MIPI_CSI/USB2.0/I2C

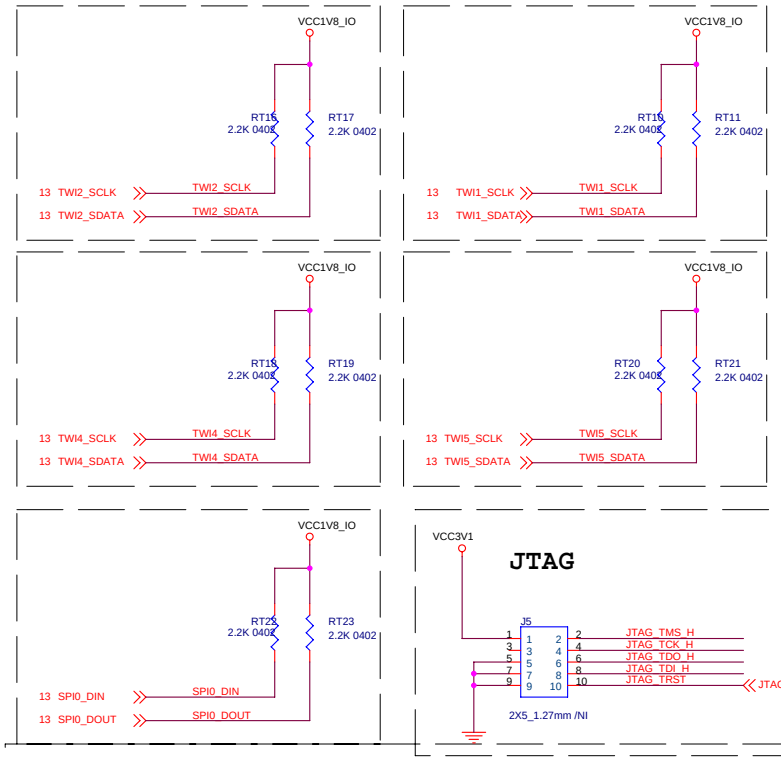


differential-100R impedance

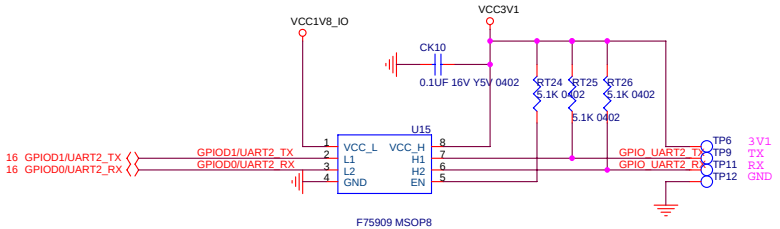


JTAG_Level_Shift(1.8V to 3.3V)





GPIOUART2 for Kernel Log (Debug use)



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