Sylhet Engineering College, Sylhet
(Shahjalal University of Science & Technology)
Department of Computer Science & Engineering

Final Examination, 2020 **3rd year 1st semester**
Course No: CSE 503 **Set # 2** **Course Title: Computer Architecture**
Time: 03 (Three) hours **Full Marks: 30**

N.B. : (i) Answer every question from each PART (ii) Use separate answer scripts for each PART
(iii) Marks allotted are indicated in the margin (iv) Special Instruction (if any)----------N/A--------

PART-A

1. (a)Consider the following C statement: D = A+B*C; 5
Rewrite the statement using 3, 2, 1, 0 Address instruction set.

(b) What will be happened if we send code word with parity bit 101101 and receiver receive as follows: (i) 101101 (ii) 100101 (iii) 110101 (iv) 001000 8

(c) Perform subtraction (1010100 – 1000100) using 2's complement method. 2

PART-B

2. (a)Represent -12 in 8 bit architecture 2

(b) What is the final content of Register R at the end of the following Assembly Language code? 6

```
i.    MOVE  R,A6H      ii.  MOVE   R,F0H
CSR   R                CSL    R
ASR   R                SR     R
CSL   R                ASR    R
AND   R,FOHCSR         R
HLT   OR       R,0FH
                              HLT
```

(c) (-4)*(6)=(-24) for this equation show the tracing table of signed multiplication using Booth's Algorithm. 7