Sylhet Engineering College, Sylhet
Department of Computer Science and Engineering (CSE)

3<sup>rd</sup> year 1<sup>st</sup> Semester Term Test, 2019

Course No.: CSE-507	Course Title: Operating System
Q1. Write the goals and functions of Operating System.	0.4
Q2. Differentiate between Multiprogramming and Multitasking.	03
O3 Define Spooling with example	03

## Sylhet Engineering College, Sylhet

Department of Computer Science and Engineering 2<sup>nd</sup> year 2<sup>nd</sup> Semester Term Test, 2019

Course No.: CSE-507

Course Title: Operating System

Q1. Describe Process States.

O2-Consider five processes, arrival time and their burst time. Calculate Avg. waiting time, Avg. turnaround time and Avg. response time, wing HRRN algorithm.

05

Process	Arrival time	Burst tme
P1	1	5
P2	2	2
P3	4	3
P4	6	6
P5	8	4

# 3rdyear 1st Semester Final Examination – 2017 Department of Computer Science & Engineering Course No: CSE 507

## **Course Title: Operating System**

Time: 3.00 Hours

Tir	ne: 3.00 Hours											To	tal Marks	: 70
	[A	nswe	r any	fou	qu			ng <u>t</u>	wo f	rom ea	ch part)			
1 al	Why is it not possil	ale to	store	الحو	0115	PAR'			. ma = 1	in				
ı.a,	solution to this pro	blem	31011 ?	e all	oui	uata a	na pr	ogra	ams	ın maii	n memoi	y? Wha	t is the	2
b)				ems	wit	h two	or mo	re r	roce	ssors?				3
c)	What are the eff	ects	of ti	me	qua	ntum	on t	urn	arou	nd tim	ne? How	v can t	hev be	3
	minimized?													
d)	Describe the scena	rios v	vhen	a pa	ren	t proc	ess ca	n te	rmir	ate a	child pro	cess.		2.5
e)	Describe a socket v	vith e	xamı	ple.	Drav	w the I	ZINIX	ope	ratio	n Syste	em archi	tecture.		3+4
2.a)	What are the adva	ntage	s of I	Dara	ا اما ا	Syston	.2							3
b)	Draw the Diagram						11:							2.5
c)	Observe the follow													12
		٦		oces	s	Burs	Time	e	Pric	rity	1			
		آ		P1			10	$\overline{}$			i			
		Ì		P2		<u> </u>	1			1	1			
		Ì		Р3			2			4	7			
				P4			1			5				
				P5			5			2				
•	Now, answer the f													
	i) Draw the Ga	ntt C	hart	for	the	Sched	uling	Alg	orith	m FCI	FS, SJF,	Priority	and RR	) 
	(Time Quantu	m = 4	1).											
	ii) Calculate the	Tur	narou	und	Tim	e of	each	pro	cess	of e	ach of	the me	entioned	
	algorithms.													
	iii) Calculate the	Waiti	ng Ti	me c	of ea	ich pro	cess	of e	ach	of the	mentior	ied algo	rithms.	
3.a)	What services doe	s the	oper	atin	g sy	stem p	provid	le? I	Desc	ribe in	detail.			4
b)												duler? I	Draw the	e 2
	process state diag													
c)												em?		2
d)	How does a share	d mei	mory	syst	em	work?	Desc	ribe	wit	n figur	e.			2
								_						
						PAR	<u>₹T –</u>	R						
4.a)	Describe multithre	eadin	g mo	dels										2
b)	Consider the follow					a syste	m:							, 4
	Process		Alloc	atio	n	. —	M				Availa		Nee	_d
		Α	В	_C	D	A	В	С	D	A	B C			
	P1	0	0	1	2	3	4	1	2	3	4 1	3		
	P2	2	1	0	0	5	7	6	3				00	2
	P3	3	3	3	1	3	3	5_	6	3	344		0 0	-
	P4	0	1	4	2	7	6	5	2					
	P5	0	0	1	2	0	6	7	6					<b>.</b> .
	i) Determine whe	ther	the s	yste	m i	s in sa	afe st	ate	or r	ot. If	yes, wr	te dow	n the sa	re
	sequence. 🏑													
	ii) If a request fron	n pro	cess	РЗ а	rriv	es for	(0, 0,	1, 2	2), <b>c</b> a	n it bo	grante	d imme	diately.	
c)	Explain demand pa													
d)	What is a deadlock			re th	ie n	ecessa	ry co	ndi	tions	for a	deadloc	:k?		
e)	How can a request	of si	ze n	be s	atis	fied fr	om a	list	of fr	ee hol	es?			
٠,	now can a request	. 01 31	26 11	ne 2	atio	iicu ii	J u		11					

5.a) b) c)	Compare between paging and segmentation.  Describe the following: i) address binding ii) Dynamic linking  Write down Peterson's solution and show that it satisfies all three requirements for	3 4 3+2.5
	the critical section problem	
d)		2+3
6.a)	What is a thread pool? What are its advantages?	1+2
b)	Explain the problems associated with storing page tables using registers and memory. Describe the hardware used to solve these problems.	2+4
c)	Explain the copy-on-write method.	2.5
d)	What are the issues associated with deadlock recovery?	2
e)	Given below is the references made to the following pages by a program:	4
	0, 2, 4, 1, 5, 1, 4, 3, 0, 3, 1, 0, 2, 5, 6, 7, 1, 2, 0, 2	
	Show the successive pages residing in three frames.	
	i) LRU replacement	
	ii) Optimal replacement.	

# Sylhet Engineering College, Sylhet

(Shahjalal University of Science & Technology)
Department of Computer Science & Engineering

Final Examination, 2018 Course No: CSE507 Time: 03 (Three) hours

3<sup>rd</sup> year1<sup>st</sup> Semester Course Title: Operating System Full Marks: 70

N.B.: (i) Answer any two question from each PART

(iii) Marks allotted are indicated in the margin

(ii) Use separate answer scripts for each PART
(iv) Special Instruction (if any)-----N/A-----

## PART-A

(Answer any two questions)

1.	(a)	What is an operating System? Explain different types of operating system.	06
	(b)	What are the difference between Multiprogramming and Time-sharing structure?	04
	(c)	What is Batch processing? Compare the performance of batch processing system with others.	05
	(d)	What is virtual memory? How we can implement virtual memory concept?	2.5
2.	(a)	What is the difference between a Job and a Process?	2.5
	(b)	Draw and explain process state diagram.	05
	KR)	Define PCB? What are the attributes of PCB?	05
	(d)	Describe Spooling process with diagram	05
3.	(a)	What is CPU Scheduling? Explain different types of schedulers.	05
	(b)	What is Preemptive and Non Preemptive Process?	02
	(c)	What are the CPU scheduling algorithm criteria?	05
	Y(2)	Define context switching.	2.5
	(e)	Define Turnaround Time, Waiting Time and Response Time.	03

### PART-B

(Answer any two questions)

4.	(a)	Mention the primary differences between short-term, mid-term and Long-term scheduler.	05
	(b)	Define CPU-bound process and I/O-bound process.	2.5
	(c)	Consider the following situation:	07

Process	Arrival Time	Burst Time	Priority
P <sub>1</sub>	1	4	1
P <sub>2</sub>	0	3	2
P <sub>3</sub>	. 2	1	3
P <sub>4</sub>	4	2	4
P <sub>5</sub>	6	6	5

Now draw Gantt chart for Round Robin (time quantum: 2) and Preemptive Priority Scheduling. Find average waiting time, average response time and average turnaround time.

(d)	What are the different types of scheduling queues?	03
5. <b>`}</b> {\	What do you mean by Deadlocks? What are the necessary conditions for deadlocks?	04
(b)	What are the methods for Handling Deadlocks states?	02
ya)	Define compaction. Write down the differences between paging and segmentation	4.5
(d)	Consider the following snapshot of a system and answer the following questions using	07
	Banker's Algorithm.	

Process	Allocation			1	Иa	Х	Available
Fiotess	Α	В	С	Α	B	С	АВС
P <sub>1</sub>	1.	0	0	7	5	0	4 2 0
P <sub>2</sub>	3	5	4_	.3	5	6	004
P <sub>3</sub>	6	3	2	6	5	2	1
P4	0	1	4	6	5	6	
Ps	0	1	2	0	1	2	1

Is the system in a safe state? If yes then write the safe sequence.

work = work +

6.	(a)	What are the differences between logical and physical addresses?	03
	(b)	Given memory partitions of 100 KB, 500 KB, 200 KB, 300KB and 600KB, how each storage	5.5
		allocation algorithms place processes of 212KB, 417KB, 112KB, and 426KB? Explain which	
		algorithm makes the most efficient use of memory?	
٠	(c)	What do you mean by the term swapping?	03
	(d)	What is fragmentation? Explain different types of fragmentation.	06