

# SHOYEB AHMED FAHIM

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## EDUCATION

### Ahsanullah University of Science & Technology

July 2019 - November 2023

B.Sc in Electrical & Electronic Engineering

CGPA : 3.189 out of 4.00

#### Major Courses Taken:

- 1.Digital Electronics I, Digital Electronics II, Solid State Devices, VLSI I, VLSI II, Power Electronics.
2. Communication Theory, Digital Signal Processing I, Microprocessor - Interfacing and Systems.
- 3.Power System I, Control System I, Computer Architecture, Multimedia Communications.

## ENGLISH PROFICIENCY

### IELTS

January 28th 2025

Listening	Reading	Speaking	Writing
<i>C1</i>	<i>C1</i>	<i>B2</i>	<i>B2</i>

## RESEARCH EXPERIENCE

### 1.Undergraduate Research Work

Ahsanullah University of Sciene & Technology(AUST)

Thesis title: *Efficient Paddy Yield Detection in Bangladesh Using Machine Learning: A Dataset-Driven Approach.*

Thesis Supervisor : *Mr. Md. Adnan Quaium*, Assistant Professor,Department of Electrical & Electronic Engineering, Ahsanullah University of Science & Technology

## TECHNICAL SKILLS

- **Programming Languages:** C++, Python, MATLAB, Verilog.
- **Simulation Software:** OrCad, Pspice, Quartus, Proteus, Cadence, KiCAD.
- **Others:** MS Excel, MS Word, MS Power Point.

## KEY ACADEMIC PROJECTS

- **Comparative Analysis Of Different Types Of SRAM.**
- **Design and Implementation of HVDC.**
- **Design and Implementation of System On Chip using block-level netlist with PAD cells and Corner cells of WISHBONE I2C Master core.**
- **Fire Fighting Robot using Arduino.**
- **Design of a 2-Bit magnitude Comparator.**
- **Design of a Simple FDM system For DSB AM voice signal using MatLab.**

## INTERNSHIP & TRAINING

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Completed Industrial Training at **ULKASEMI Pvt. Limited.**

-Through this training I gained experience in RTL design, IC MASK Design and Circuit & System Design. This training solidified my foundational semiconductor skills, and I'm eager to apply them professionally.


Completed Industrial Training at **NEURAL Semiconductor Limited.**

-At Neural Semiconductor I gained comprehensive VLSI design experience combining theory on Analog and Digital Design with hands-on Open-ROAD tools.

## EXTRA CURRICULAR ACTIVITIES

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### AFFILIATIONS

Institute of Electrical and Electronics Engineers(IEEE) (January 22-February 2023)

-General Member

Innovation And Design Club (April 2022-November 2023)

-General Member

Aust Sporting Club (July 2021-November 2023)

-General Member

### CERTIFICATES & COMPETITIONS

MINDSPARKS23 IDEATION INTO EXISTENCE

-Participated in '**VLSITHON**' organized by AUST-Innovation And Design Club.

INTERNATIONAL STUDENT-LED CONFERENCE 2023

-Champion in the '**Undergraduate Thesis**' paper Contest

Workshop on **IOT**

Short Course on **Motor Controlling Technique**

## REFERENCES

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### 1. Md. Jakaria Rahimi

**Associate Professor**

**Department of EEE, Ahsanullah University of Science & Technology**

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### 2. Mr. Md. Adnan Quaium

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