

Перв. примен.

Справ. №

Подп. и дата

Инв. № дудл.

Взам. инв. №

Подп. и дата

Инв. № подл.

SolMPPT.0.0.1

Schematic diagram of a solar MPPT system using a MAX20361AEWC+ IC. The diagram shows a solar panel (G1) connected to a boost converter. The input is connected to the SRC pin, with a 22µF capacitor (C1) to ground. The output is connected to the SYS pin, with a 1µF capacitor (C3) to ground. The VCC pin is connected to a 6.3V source with a 1µF capacitor (C2) to ground. The REF pin is connected to a voltage divider (R1=61.9k, R2=100k) with a ~0.46V output. The THM pin is connected to ground. The SDA and SCL pins are connected to an I2C interface. The WAKE and EN pins are connected to an external control circuit. The IC is labeled D1 MAX20361AEWC+.

					SolMPPT.0.0.1				
Изм.	Лист	№ докум.	Подп.	Дата	Схема электрическая принципиальная	Лист	Масса	Масштаб	
Разраб.	Шпаков Д.					*		1:1	
Пров.	*								
Т.контр.	*					Лист	1	Листов	*
Н.контр.	*					*			
Утв.	*								

2.106-A3

Копировал

Формат А3