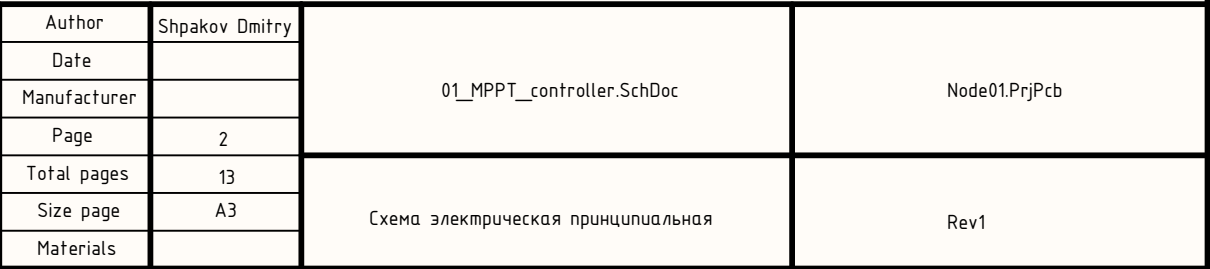
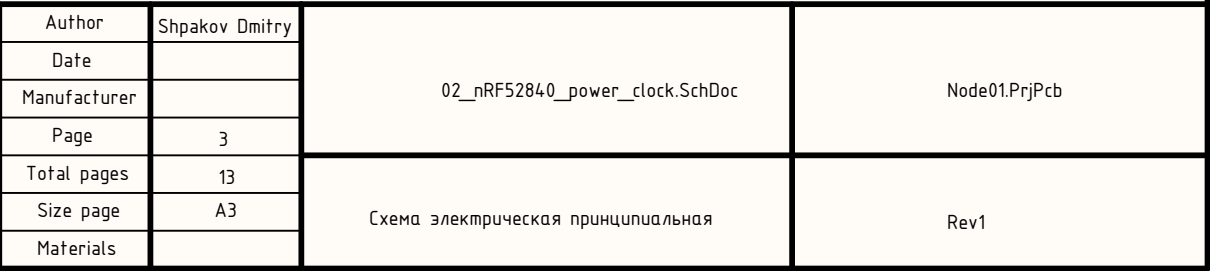
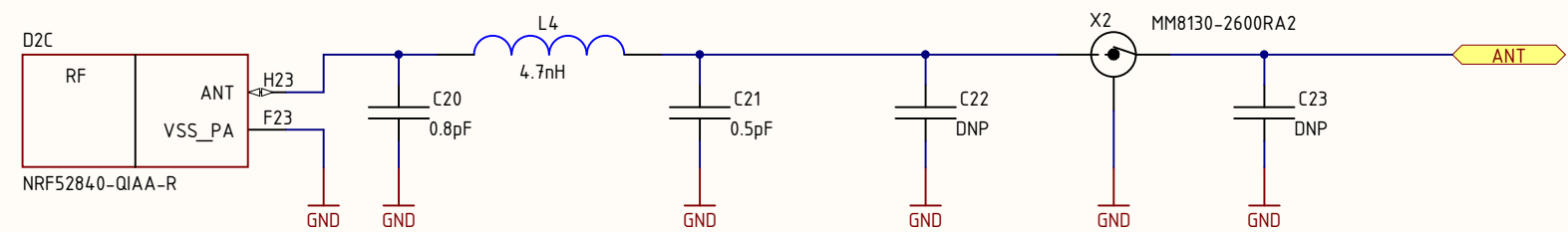


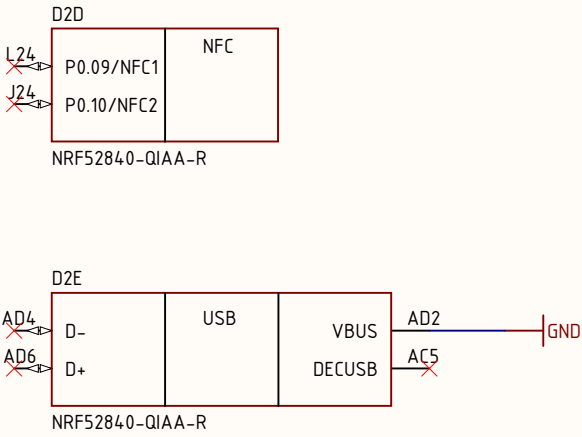
Author	Shpakov Dmitry	00_Main_Design.SchDoc	Node01.PrjPcb
Date			
Manufacturer			
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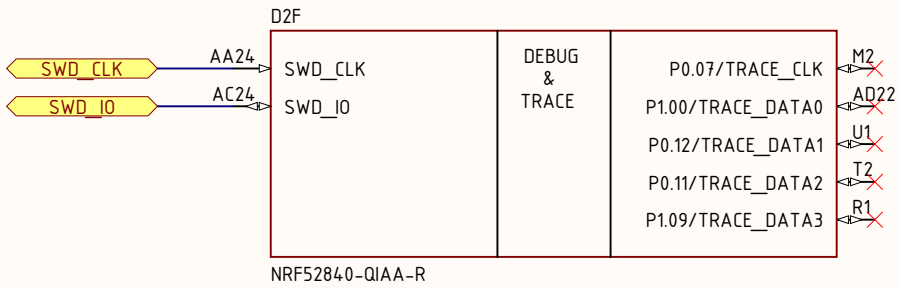




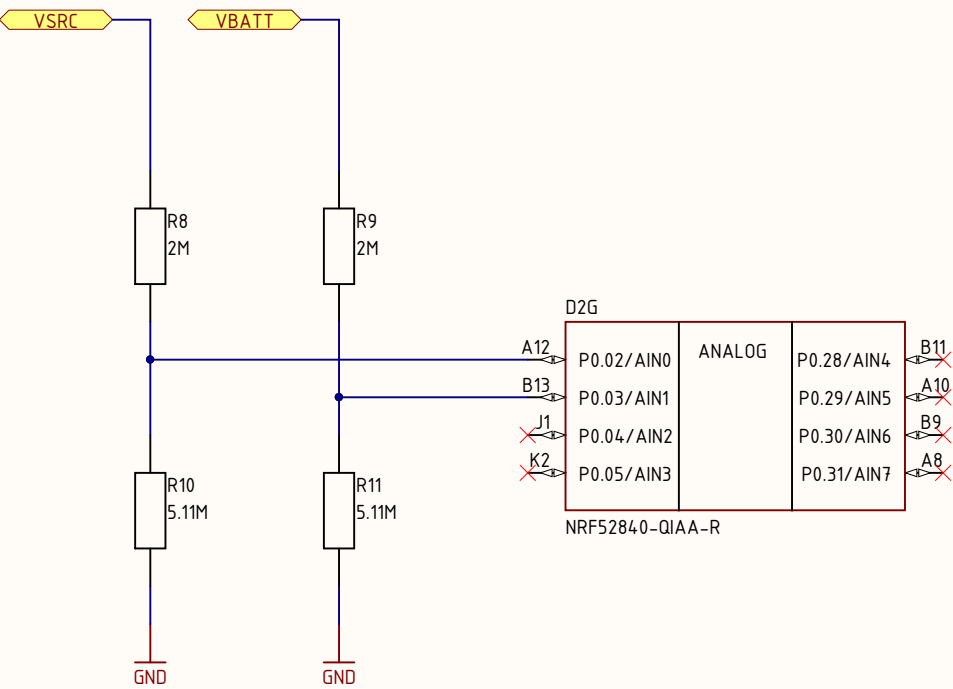
Author	Shpakov Dmitry	03_nRF52840_RF.SchDoc	Node01.PrjPcb
Date			
Manufacturer			
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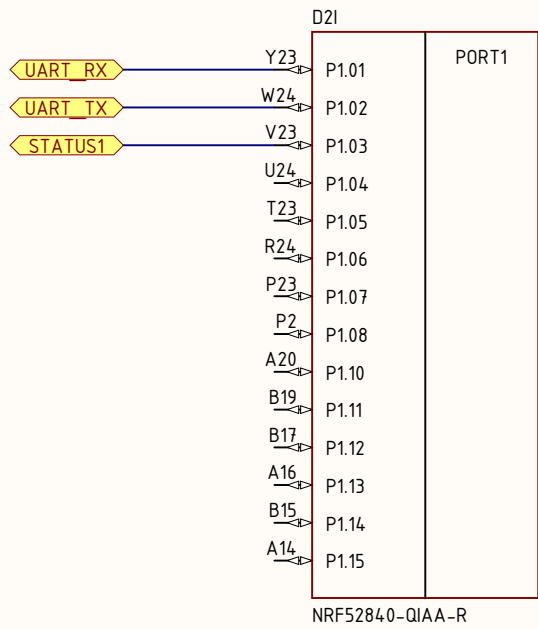
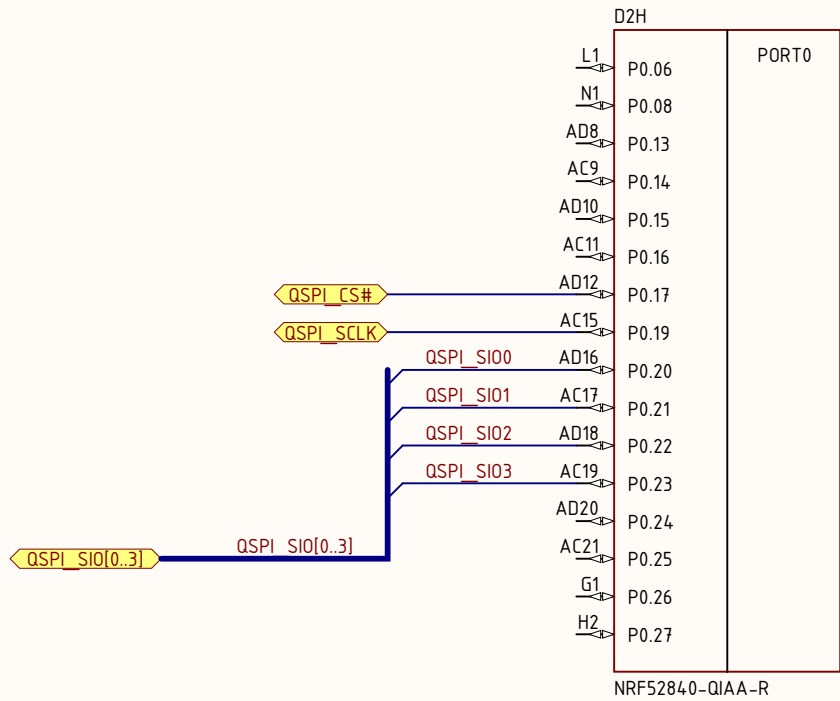
Author	Shpakov Dmitry	04_nRF52840_NFC_USB.SchDoc	Node01.PrjPcb
Date			
Manufacturer			
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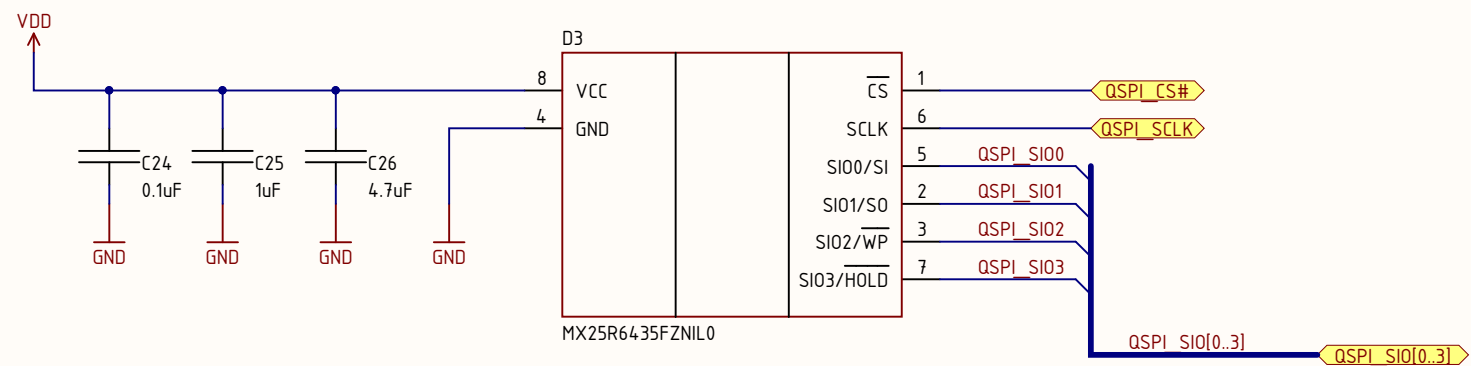
Author	Shpakov Dmitry	05_nRF52840_Debug_and_Trace.SchDoc	Node01.PrjPcb
Date			
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Manufacturer			
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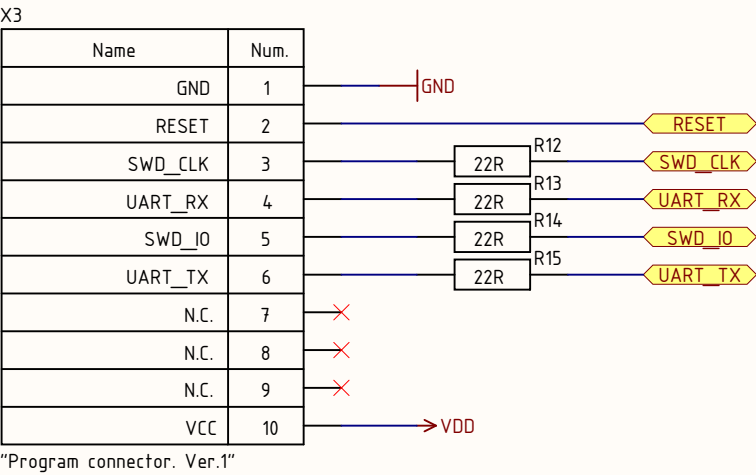


Author	Shpakov Dmitry	08_QSPI_64Mbit_Flash_Memory.SchDoc	Node01.PrjPcb
Date			
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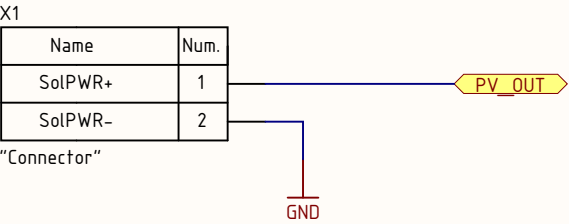
PCB antenna to be designed.
Simulations needed.
Simulations will be done when
PCB will be routed.

Author	Shpakov Dmitry	09_Antenna.SchDoc	Node01.PrjPcb
Date			
Manufacturer			
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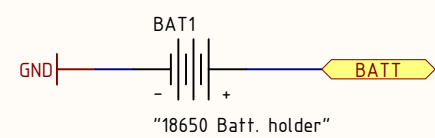


"Program connector. Ver.1"

Author	Shpakov Dmitry	10_Program_and_Debug_interface.SchDoc	Node01.PrjPcb
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