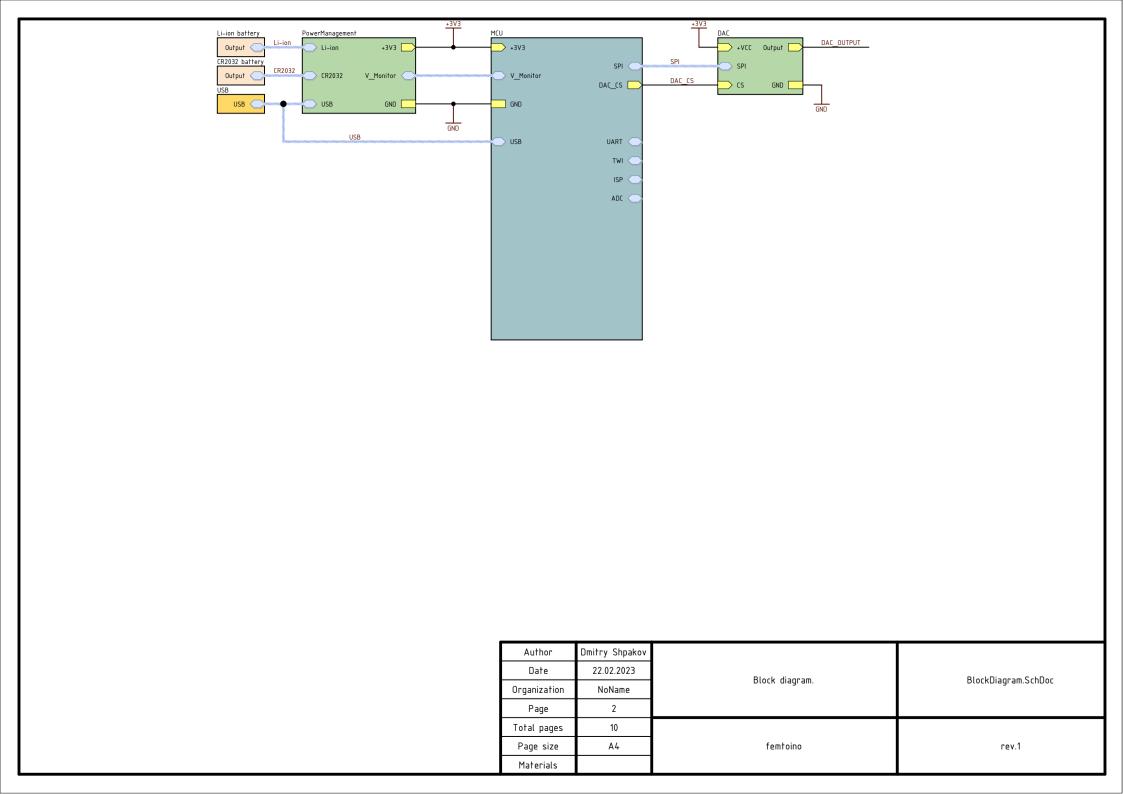
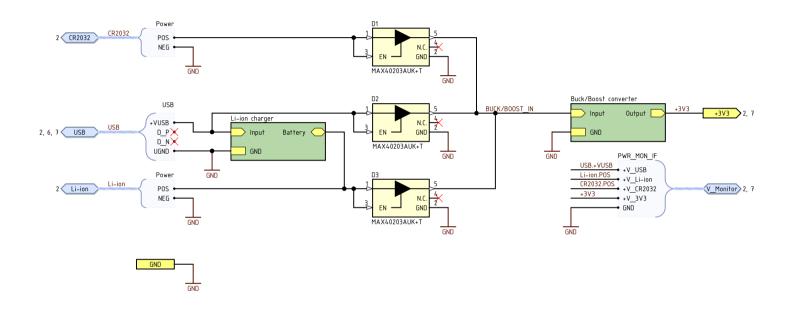
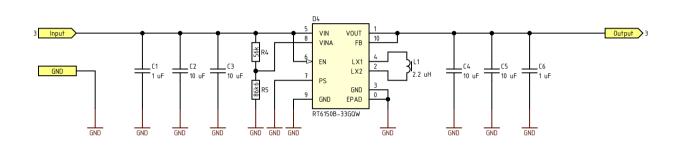
Project organization is `strict hierarchical`. Meaning that:
* power ports are local * sheet entry <-> port connection
* higher level names take priority
* power port names take priority Dmitry Shpakov Author Date 22.02.2023 BlockDiagram TitlePage.SchDoc Title page. Organization NoName Page 1 10 Total pages Page size Α4 femtoino rev.1 BlockDiagram.SchDoc Materials

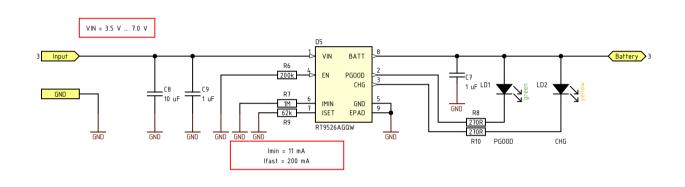




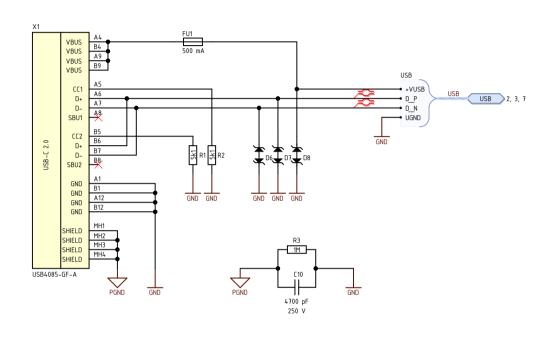
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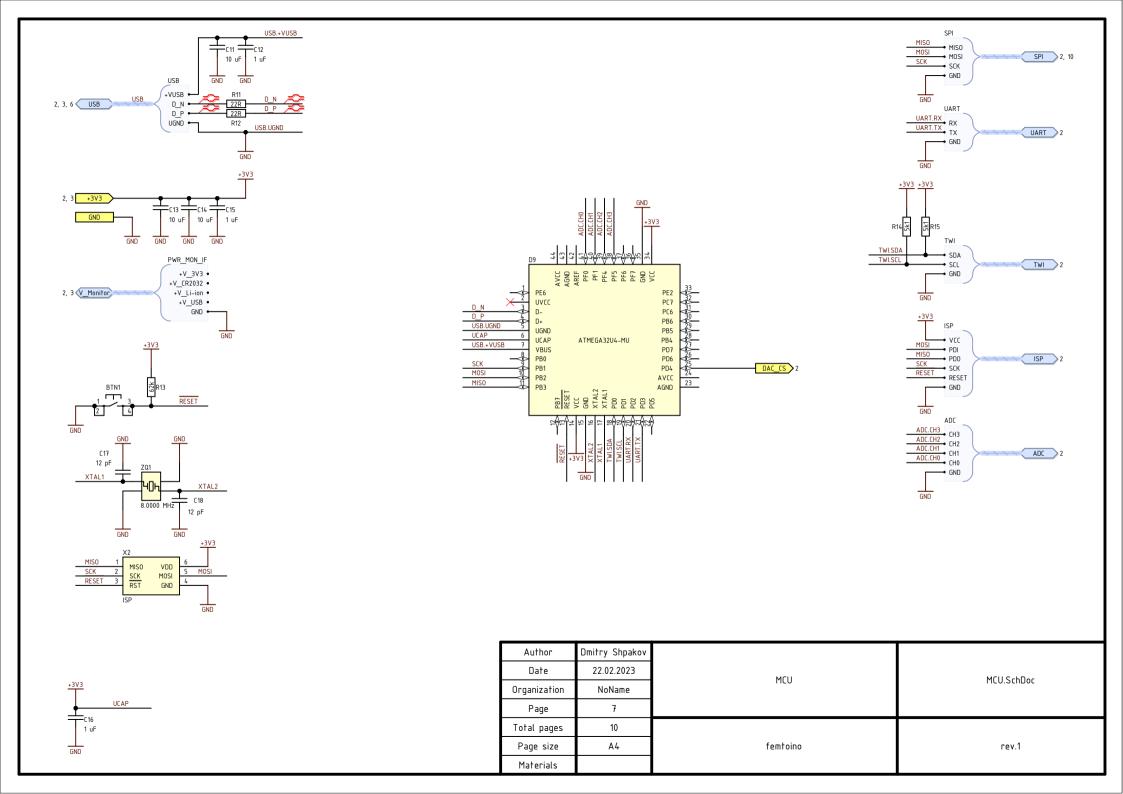
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Date	22.02.2023		
Organization	NoName		
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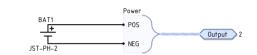


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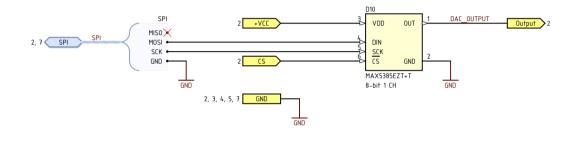




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Date	22.02.2023		
Organization	NoName		
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Date	22.02.2023		
Organization	NoName		
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Author	Dmitry Shpakov	Digital to analog converter.	DAC.SchDoc
Date	22.02.2023		
Organization	NoName		
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