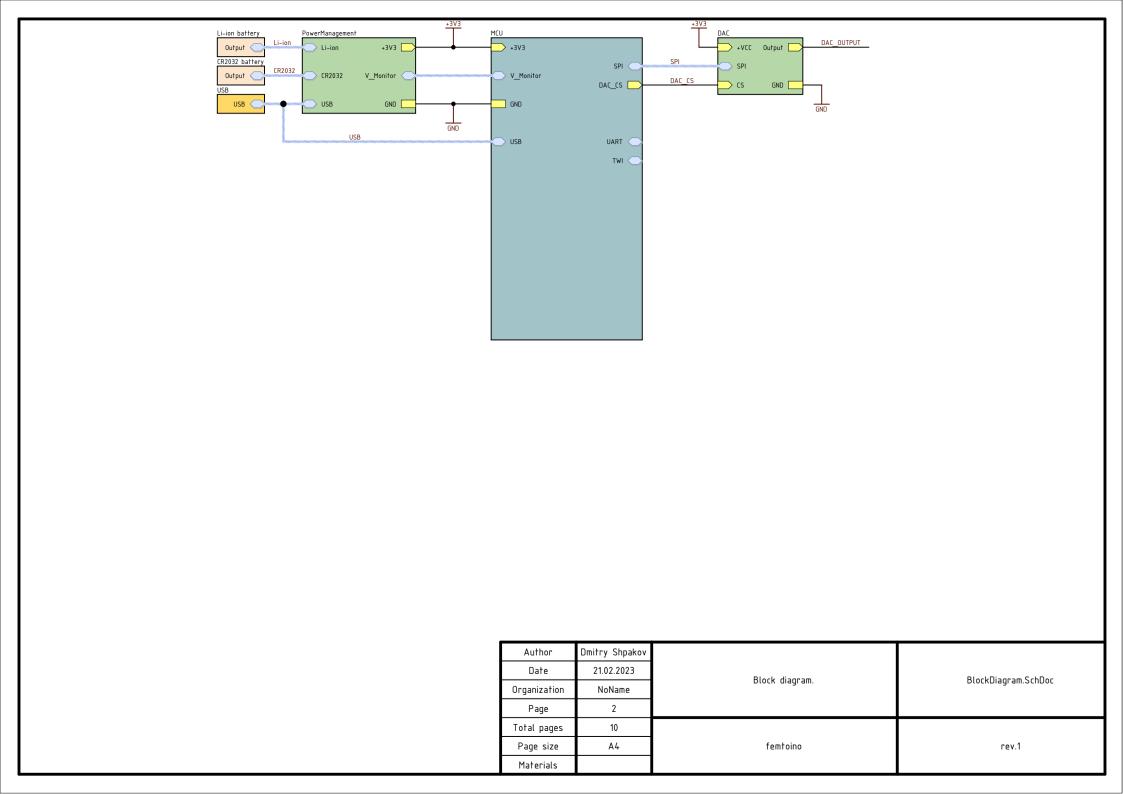
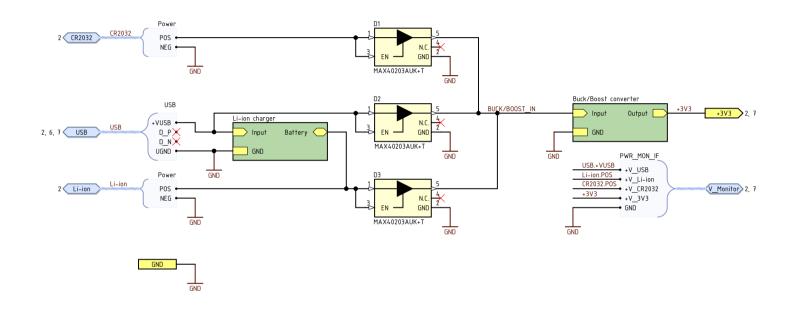
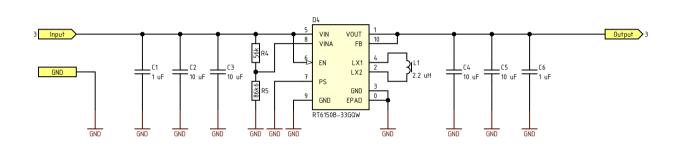
Project organization is `strict hierarchical`. Meaning that:
* power ports are local * sheet entry <-> port connection
* higher level names take priority
* power port names take priority Dmitry Shpakov Author Date 21.02.2023 BlockDiagram TitlePage.SchDoc Title page. Organization NoName Page 1 10 Total pages Page size Α4 femtoino rev.1 BlockDiagram.SchDoc Materials

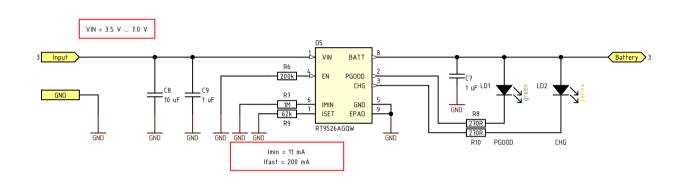




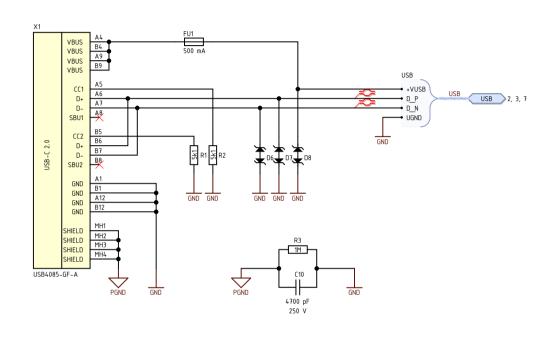
Author	Dmitry Shpakov	Power management.	PowerManagement.SchDoc
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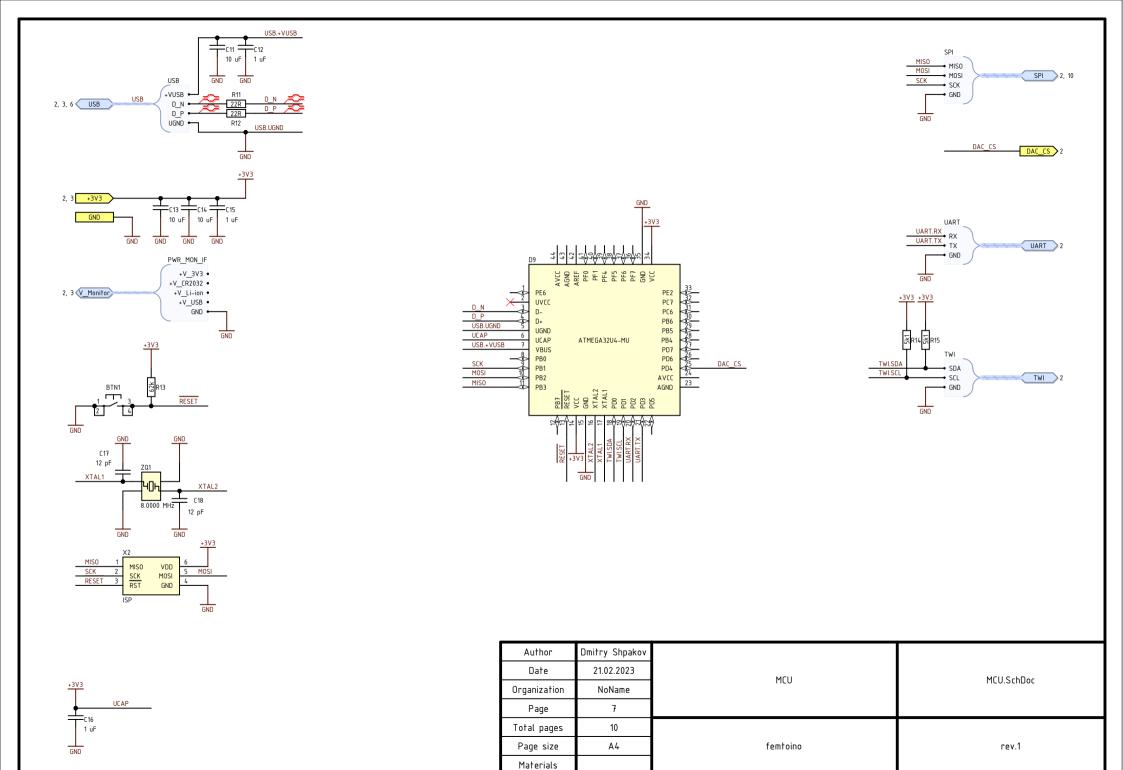
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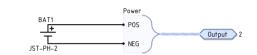


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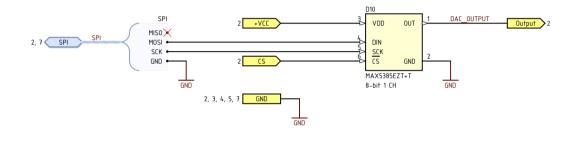




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Date	21.02.2023		
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Date	21.02.2023		
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Page	9		
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Date	21.02.2023	Digital to applied conventor	DAC.SchDoc
Organization	NoName	Digital to analog converter.	DAL.SCIIDOL
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