

Project organization is `strict hierarchical`. Meaning that:

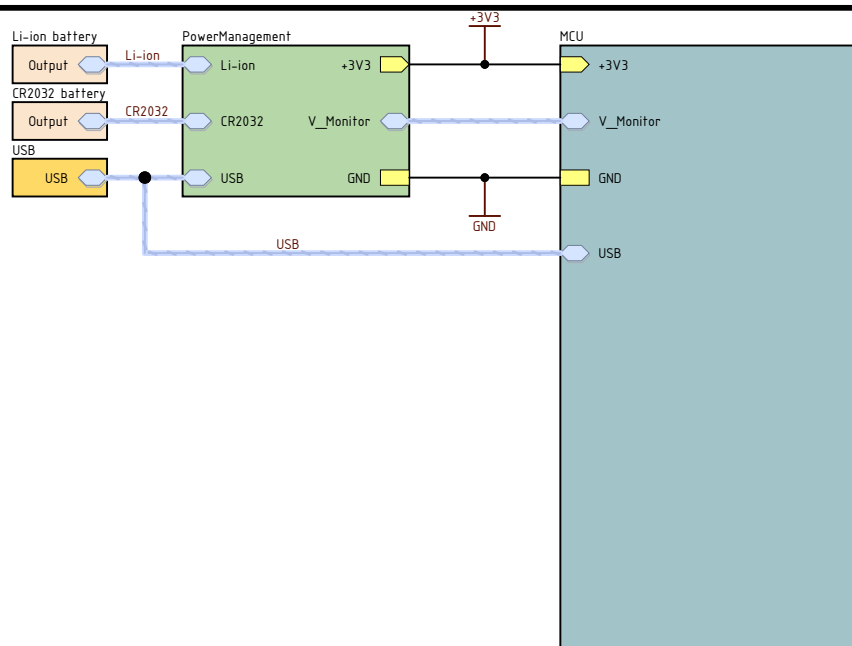
- \* power ports are local
- \* sheet entry <--> port connection
- \* higher level names take priority
- \* power port names take priority

BlockDiagram

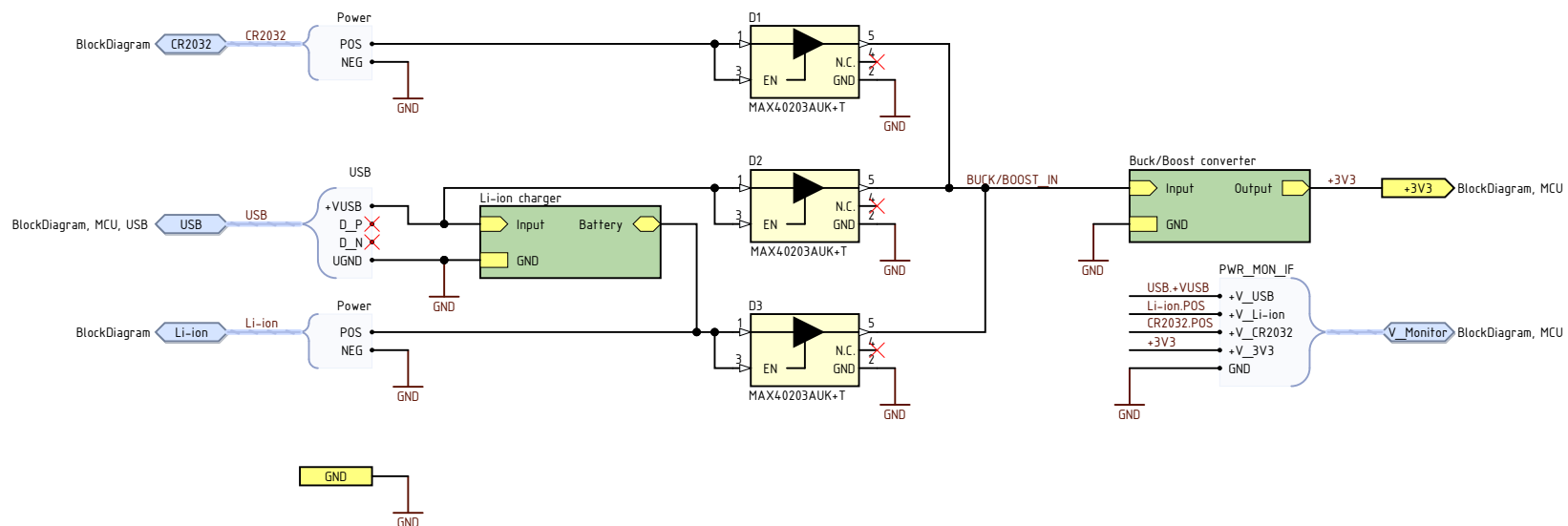


BlockDiagram.SchDoc

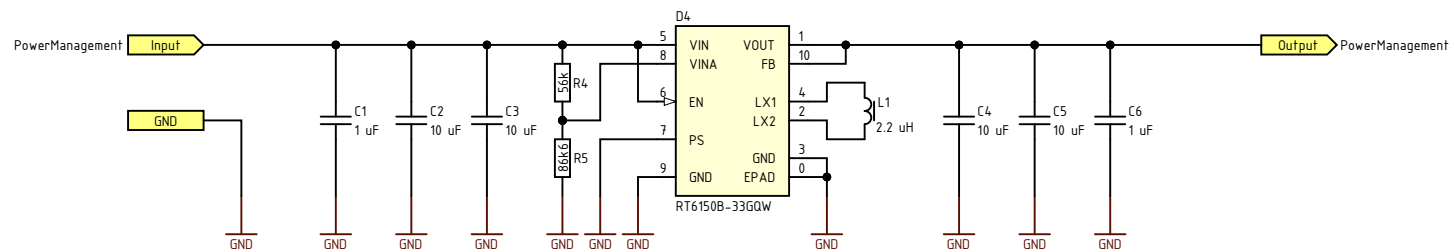
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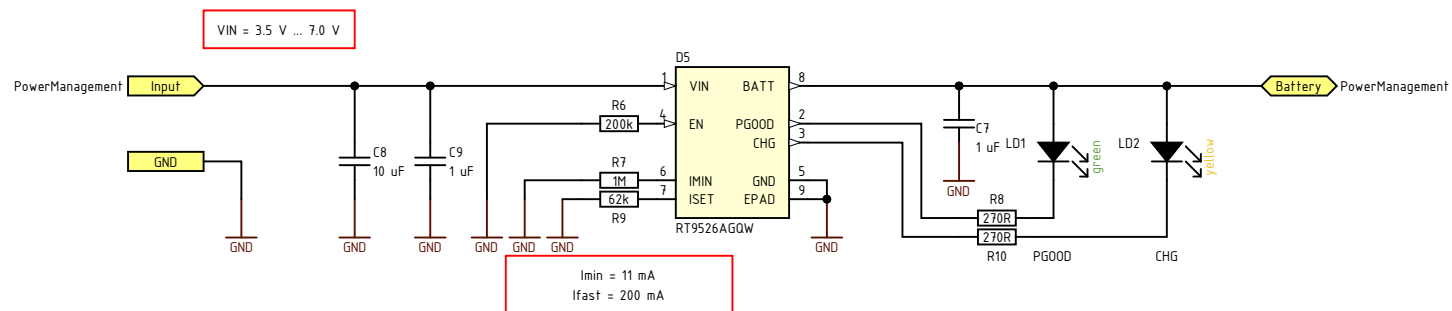
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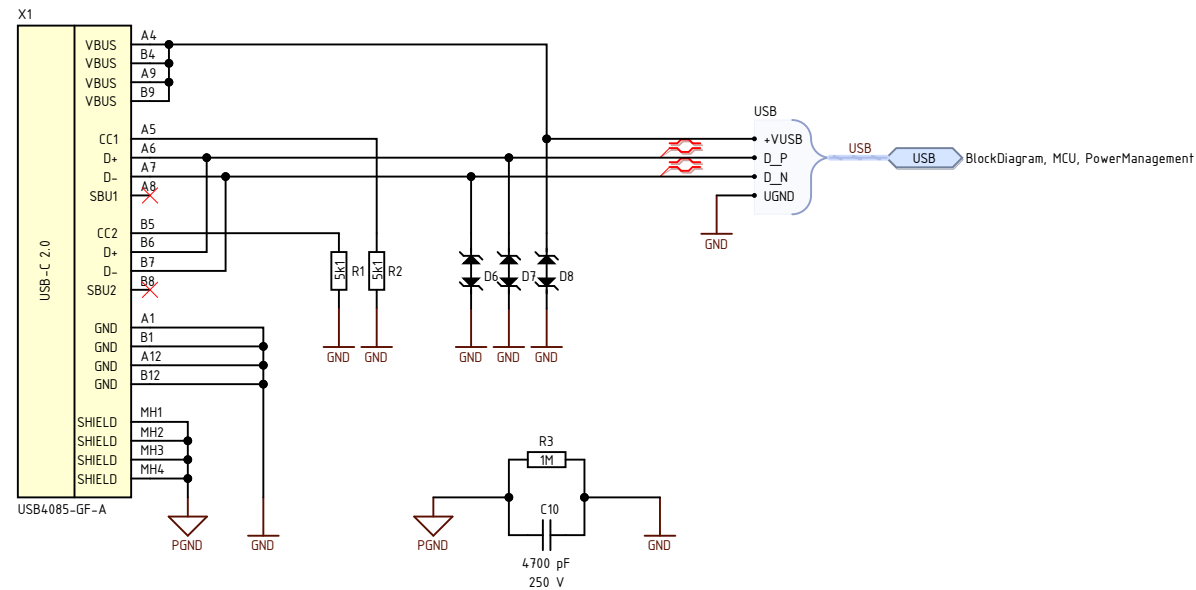
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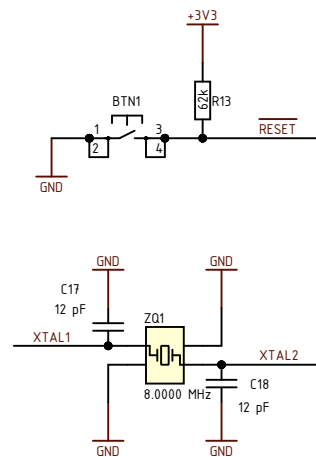
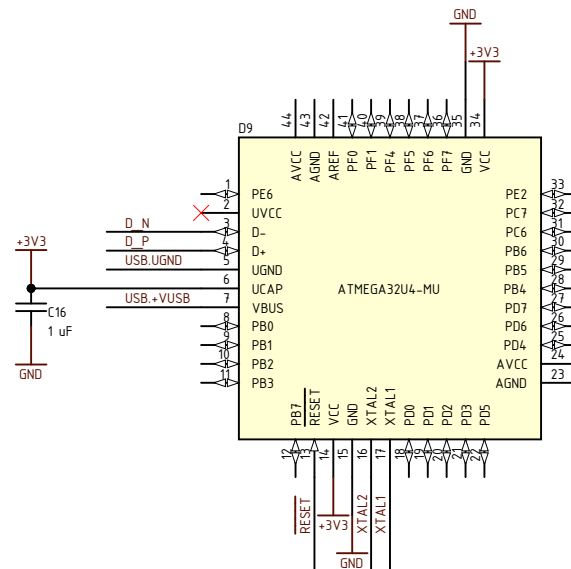
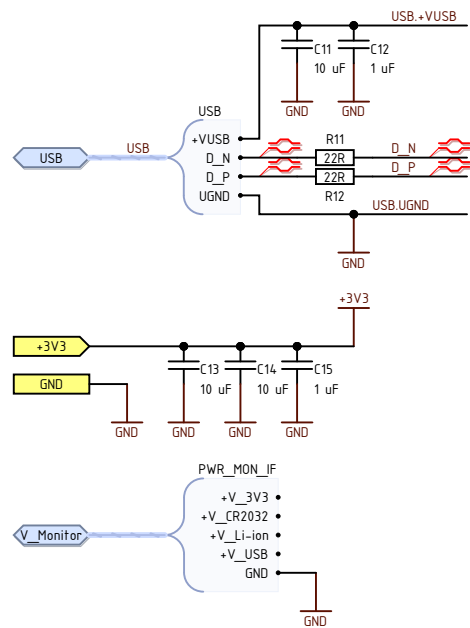
Author	Dmitry Shpakov	+3V3 buck/boost converter.	Buck_boost_converter.SchDoc
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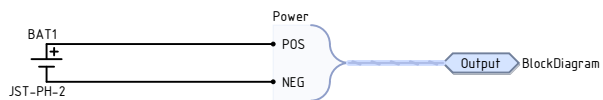
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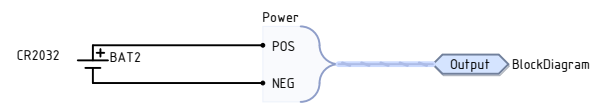


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