**Center For Developement of Advanced Computing**

**PG-DESD Feb 2020 Batch**

**Subject:-MCI Lab ARM Exam**

**ROLL NO:-**

**NAME :-**

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**Que. 1. A. Generate the below peripherals interrupts using NVIC Interrupt Pending registers & observe the execution of ISR’s when priorities are same/different in STM32 cube ide.Write few lines what you understood about peripherals interrupts using NVIC by demonstrating NVIC Register, also paste your output snapshot in pdf.**

**use TIM2 :- global interrupt**

**I2C1 :- event interrupt**

**Hint :- Use 1. Refernce manual of Board,**

1. **Cortex M3 Generic userguide.**

**OR**

**Que. 1. B. Write a program to demonstrate operational modes of cortexM3 (Thread Mode to Handler Mode interchanging) by generating IRQ interrupt. Also write the handler for same.**

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**Answer for :-**